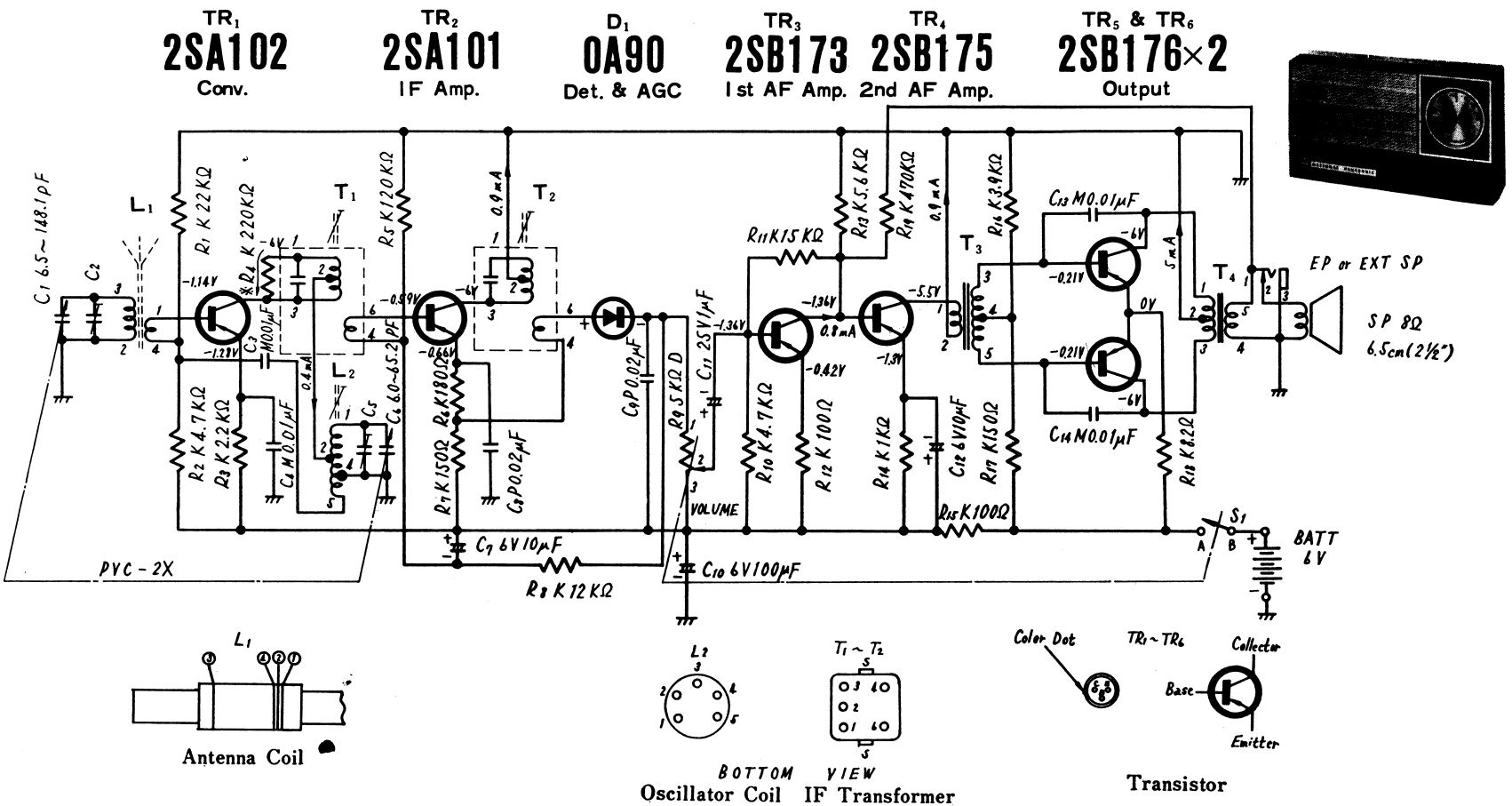
NATIONAL MODEL R-121



Notes:

1. S_1 : Power source switch in "OFF" position.
2. Voltage measurement are taken with circuit tester (10KΩ/V).
3. Measured voltages for $TR_1 \sim TR_6$ are from transistor terminal to bias line.
4. Capital letters (M,K,J,P) in the circuit diogram show allowable tolerance of resistors and capacitors as follows:
   M = ±20% K = ±10% J = ±5% P = +100% − 0%
5. PF = pico farad = mmf
   μF = micro farad = MF
6. The resistor dotted in the diogram are the standard valuse which may be variable according to the characteristics of transistor.
   *R_1 = 100KΩ or 330KΩ
7. Battery current: No signal.....10mA
   Maximum output.....80mA

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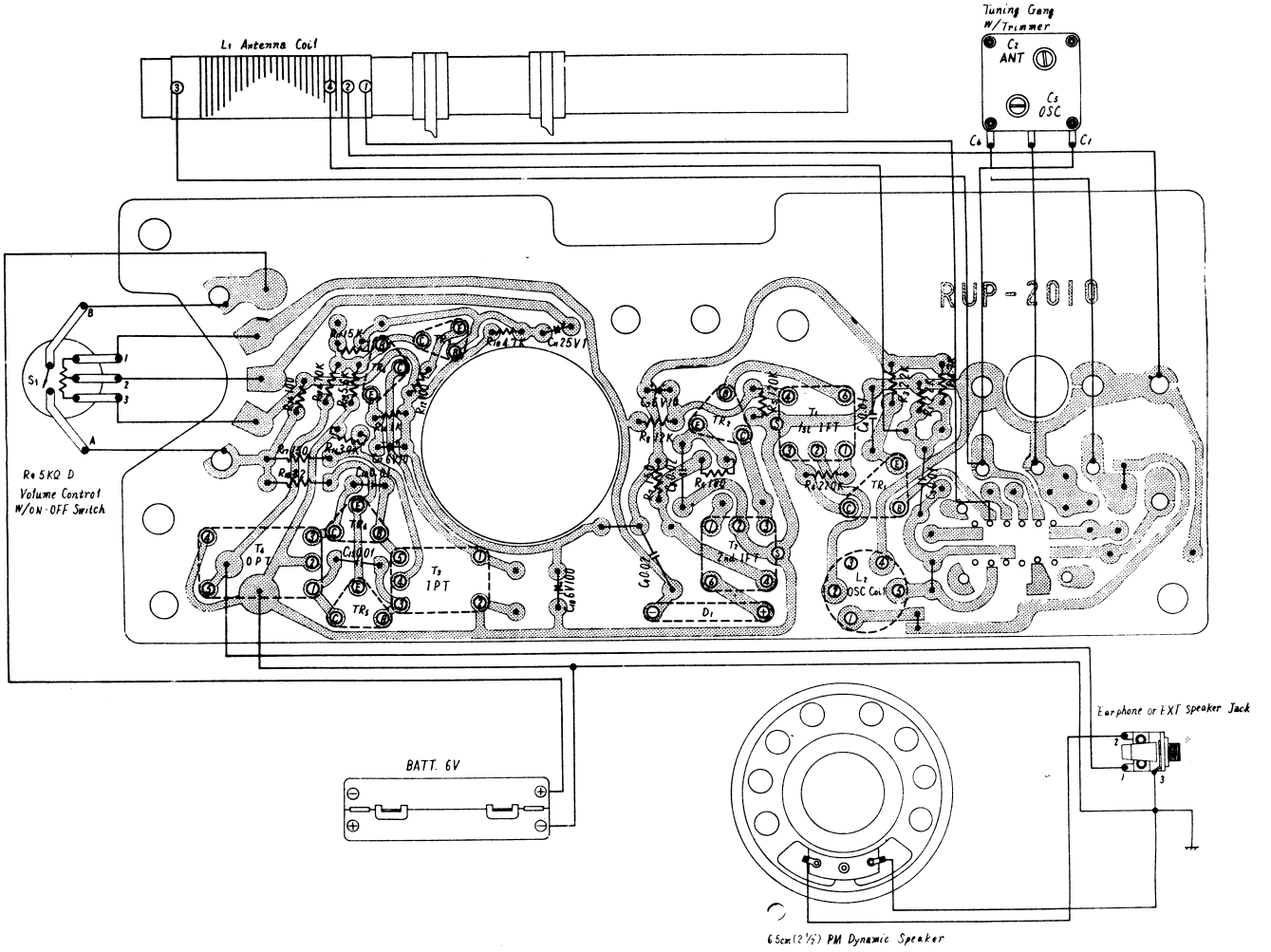

Notes:

1. All resistor values in ohms. (K=1000Ω)
2. All capacitor values in micro farads. (P=μμF).
3. S_1 : Power source switch in "OFF" position.

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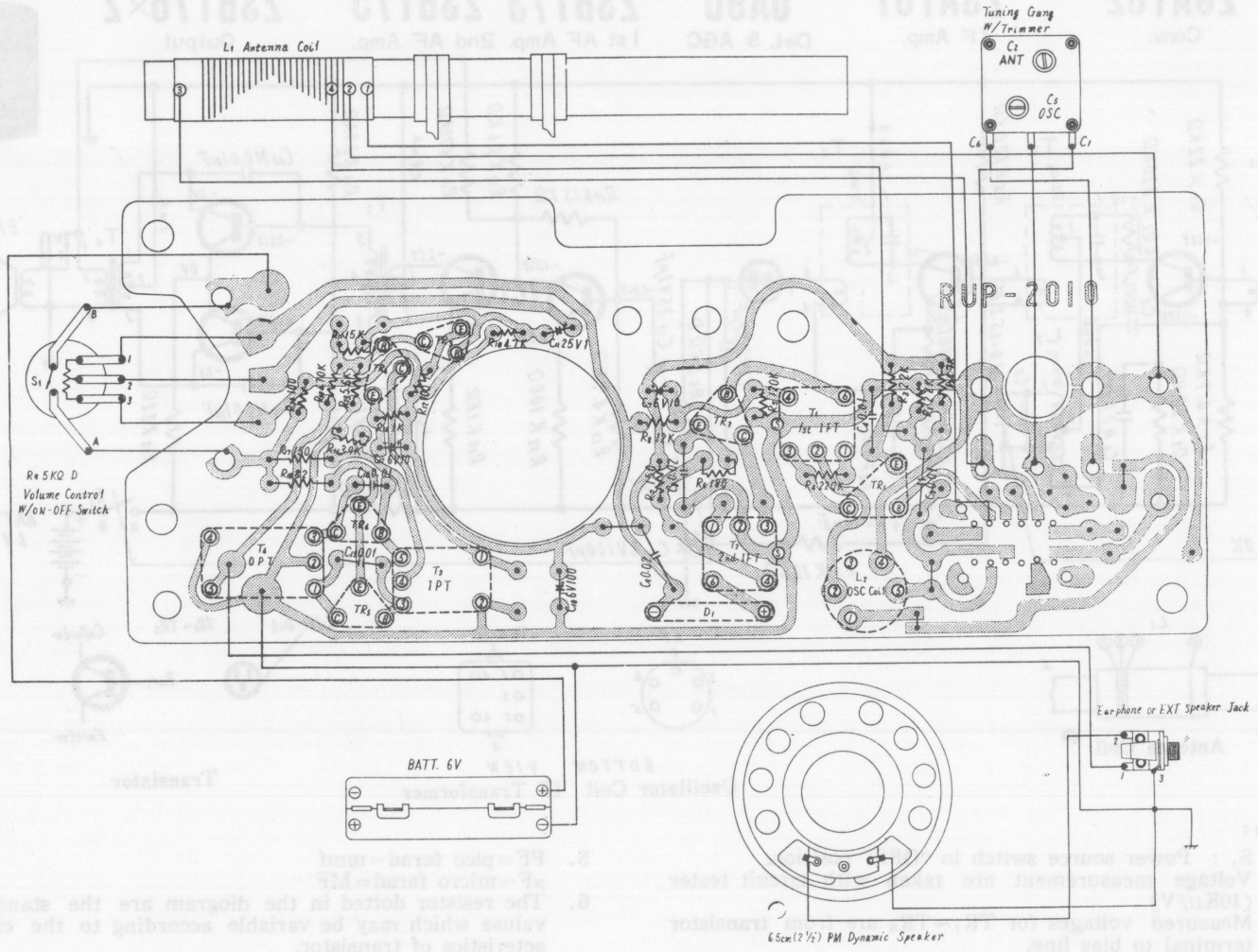

Notes:

1. All resistor values in ohms. (K=1000Ω)
2. All capacitor values in micro farads. (P=μμF).
3. S_1 : Power source switch in "OFF" position.