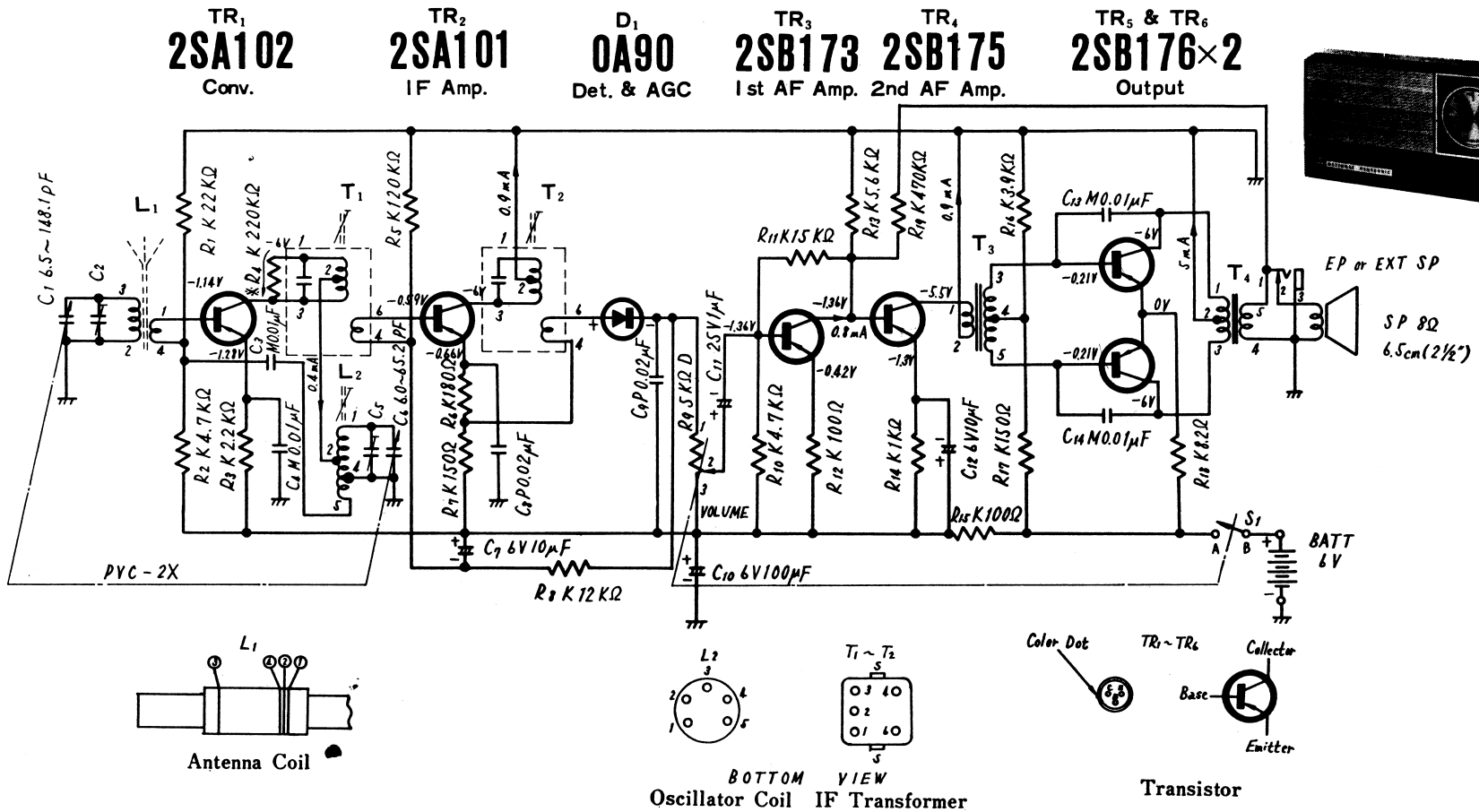
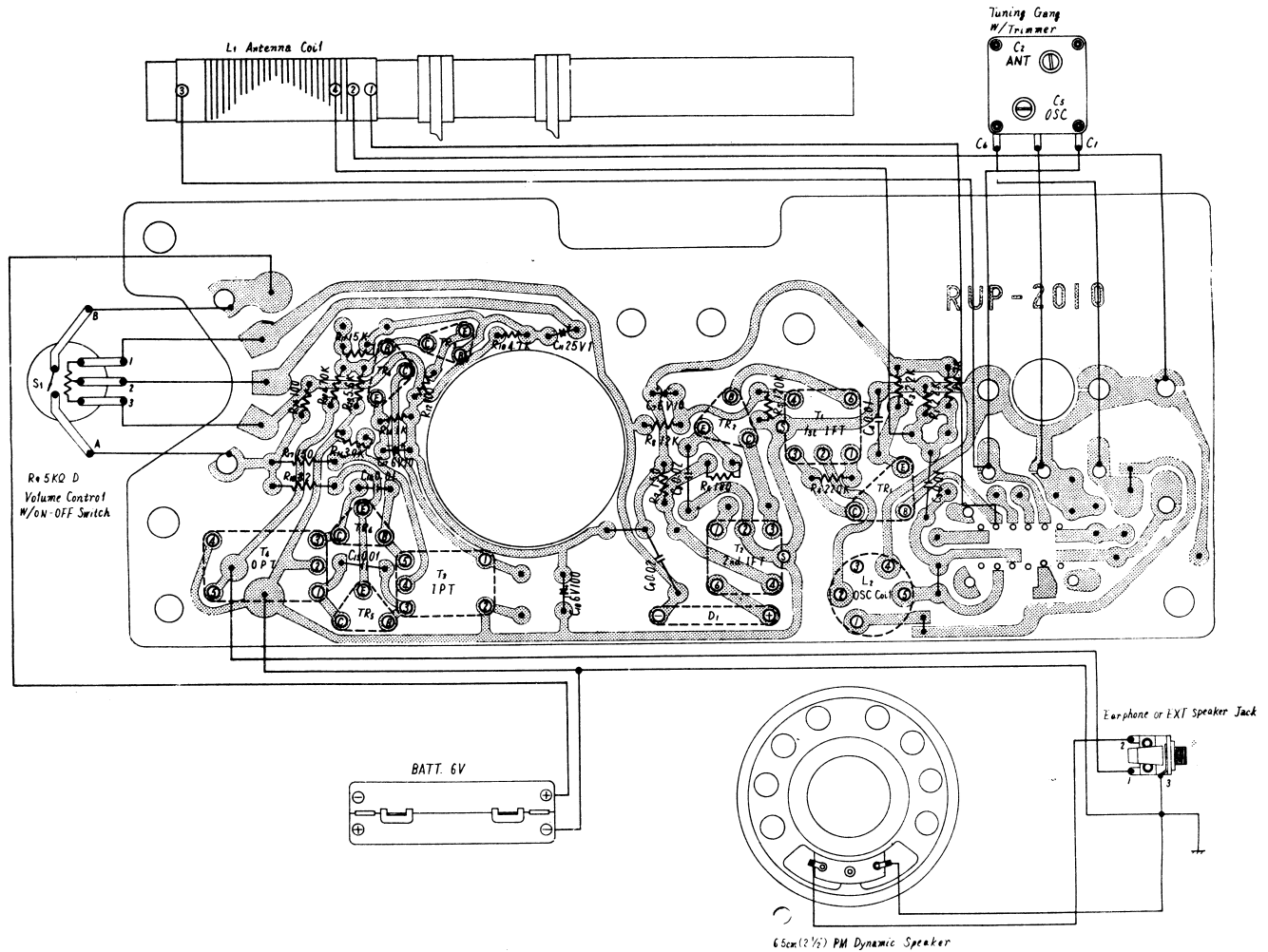


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Notes:

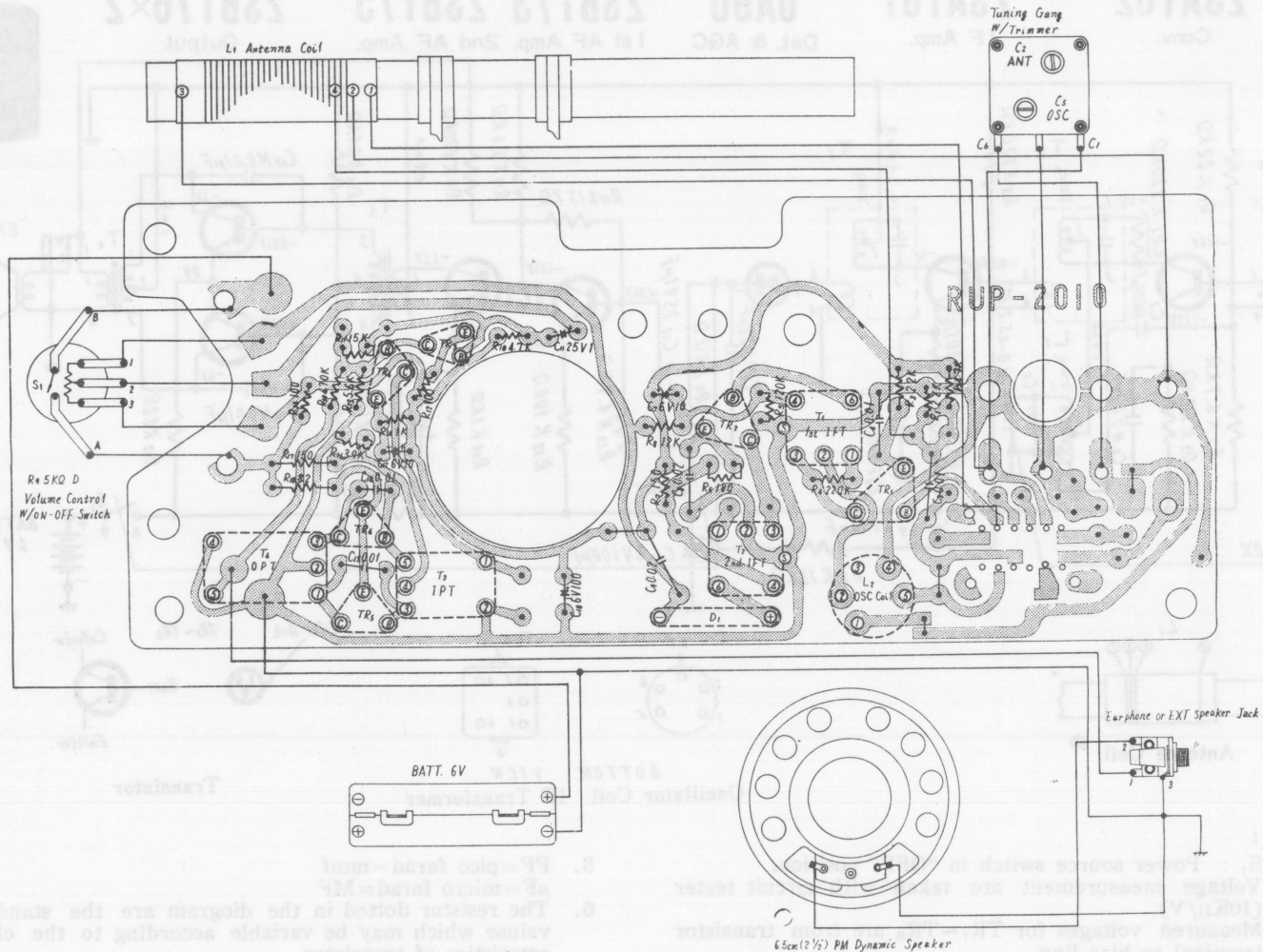
1. S₁ : Power source switch in "OFF" position.
2. Voltage measurement are taken with circuit tester (10KΩ/V).
3. Measured voltages for TR₁~TR₆ are from transistor terminal to bias line.
4. Capital letters (M,K,J,P) in the circuit diagram show allowable tolerance of resistors and capacitors as follows:
M = ±20% K = ±10% J = ±5% P = +100%
- 0%
5. PF=pico farad=mmf
µF=micro farad=MF
6. The resistor dotted in the diagram are the standard value which may be variable according to the characteristics of transistor.
*R₁=100KΩ or 330KΩ
7. Battery current: No signal.....10mA
Maximum output80mA



Notes:

1. All resistor values in ohms. (K=1000Ω)
2. All capacitor values in micro farads. (P=μμF).
3. S1: Power source switch in "OFF" position.

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Notes:

1. All resistor values in ohms. ($K=1000\Omega$)
2. All capacitor values in micro farads. ($P=\mu\text{F}$)
3. S_1 : Power source switch in "OFF" position.

Maximum output.....50mA
No signal.....10mA
Battery current:

4. Circuit letters (M, A, L, P) in the circuit diagram show allowable tolerance of resistors and capacitors as follows:
M = 20% K = ±10% L = 5% P = ±10%
3. Measured voltage for the resistor in the circuit diagram should be taken across the resistor.
2. Voltage measurement is taken with the test leads.
1. 5. Power source switch in "OFF" position.