

Service Manual

Reference No. : SM-LCD27AD-002

Revision : 0

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Page : P.1 of 84

In House Model No. : LCT27AD

Customer Model No.: LCT2701AD

BOM No. : LCT27ADNDA1CS-C01

Description : Service Manual for LCT27AD CMO USA

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Checked By: **Electronic Engineer**

Mechanical Engineer

Approved By: **Engineering Manager**

Received By: **MK Department**

AKAI

SERVICE MANUAL

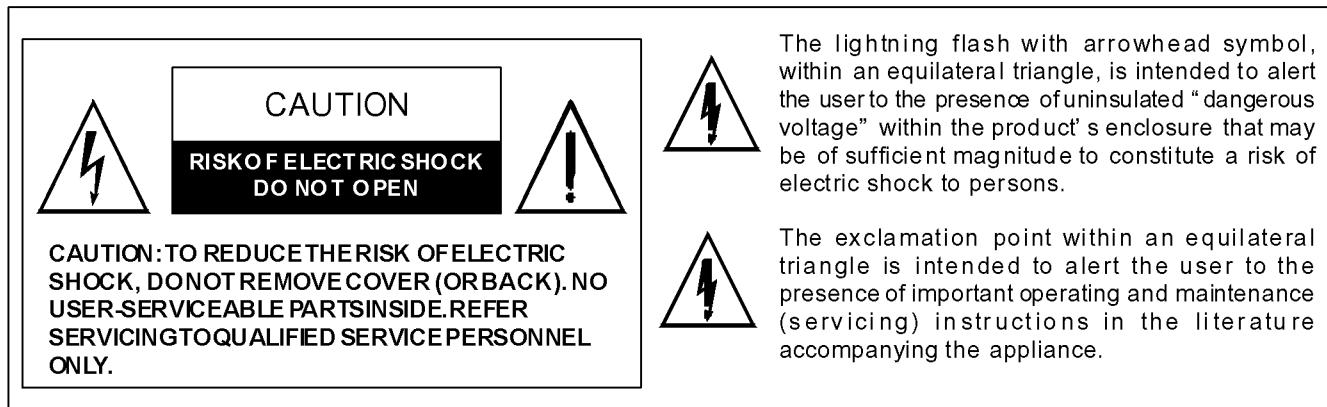
Model:

LCT2701AD

1. Safety Instructions	1~2
2. Production Specification	4~14
3. Circuit Diagram	16~40
4. Main IC Information	41
5. Panel Information.....	42~72
6. Explored View	73
7. Spare Part List.....	74~76
8. V-Chip Password.....	77
9. Software Upgrade	78~84

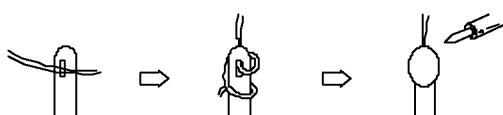
This manual is the latest at the time of printing, and does not include the modification which may be made after the printing, by the constant improvement of product.

I. Safety Instructions



PRECAUTIONS DURING SERVICING

1. In addition to safety, other parts and assemblies are specified for conformance with such regulations as those applying to spurious radiation. These must also be replaced only with specified replacements. Examples: RF converters, tuner units, antenna selection switches, RF cables, noise-blocking capacitors, noise-blocking filters, etc.
2. Use specified internal Wiring. Note especially:
 - 1) Wires covered with PVC tubing
 - 2) Double insulated wires
 - 3) High voltage leads
3. Use specified insulating materials for hazardous live parts. Note especially:
 - 1) Insulating Tape
 - 2) PVC tubing
 - 3) Spacers (insulating barriers)
 - 4) Insulating sheets for transistors
 - 5) Plastic screws for fixing micro switches
4. When replacing AC primary side components (transformers, power cords, noise blocking capacitors, etc.), wrap ends of wires securely about the terminals before soldering.



5. Make sure that wires do not contact heat generating parts (heat sinks, oxide metal film resistors, fusible resistors, etc.)
6. Check if replaced wires do not contact sharply edged or pointed parts.
7. Make sure that foreign objects (screws, solder droplets, etc.) do not remain inside the set.

MAKE YOUR CONTRIBUTION TO PROTECT THE ENVIRONMENT

Used batteries with the ISO symbol for recycling as well as small accumulators (rechargeable batteries), mini-batteries (cells) and starter batteries should not be thrown into the garbage can.
Please leave them at an appropriate depot.



The lightning flash with arrowhead symbol, within an equilateral triangle, is intended to alert the user to the presence of uninsulated "dangerous voltage" within the product's enclosure that may be of sufficient magnitude to constitute a risk of electric shock to persons.

The exclamation point within an equilateral triangle is intended to alert the user to the presence of important operating and maintenance (servicing) instructions in the literature accompanying the appliance.

WARNING:

Before servicing this TV receiver, read the X-RAY RADIATION PRECAUTION, SAFETY INSTRUCTION and PRODUCT SAFETY NOTICE.

X-RAY RADIATION PRECAUTION

1. Excessively high can produce potentially hazardous X-RAY RADIATION. To avoid such hazards, the high voltage must not exceed the specified limit. The normal value of the high voltage of this TV receiver is 27 KV at zero beam current (minimum brightness). The high voltage must not exceed 30 KV under any circumstances. Each time when a receiver requires servicing, the high voltage should be checked. The reading of the high voltage is recommended to be recorded as a part of the service record. It is important to use an accurate and reliable high voltage meter.
2. The only source of X-RAY RADIATION in this TV receiver is the picture tube. For continued X-RAY RADIATION protection, the replacement tube must be exactly the same type as specified in the parts list.
3. Some parts in this TV receiver have special safety related characteristics for X-RADIATION protection. For continued safety, the parts replacement should be undertaken only after referring the PRODUCT SAFETY NOTICE.

SAFETY INSTRUCTION

The service should not be attempted by anyone unfamiliar with the necessary instructions on this TV receiver. The following are the necessary instructions to be observed before servicing.

1. An isolation transformer should be connected in the power line between the receiver and the AC line when a service is performed on the primary of the converter transformer of the set.
2. Comply with all caution and safety related provided on the back of the cabinet, inside the cabinet, on the chassis or picture tube.
3. To avoid a shock hazard, always discharge the picture tube's anode to the chassis ground before removing the anode cap.

4. Completely discharge the high potential voltage of the picture tube before handling. The picture tube is a vacuum and if broken, the glass will explode.
5. When replacing a MAIN PCB in the cabinet, always be certain that all protective are installed properly such as control knobs, adjustment covers or shields, barriers, isolation resistor networks etc.
6. When servicing is required, observe the original lead dressing. Extra precaution should be given to assure correct lead dressing in the high voltage area.
7. Keep wires away from high voltage or high temperature components.
8. Before returning the set to the customer, always perform an AC leakage current check on the exposed metallic parts of the cabinet, such as antennas, terminals, screwheads, metal overlay, control shafts, etc., to be sure the set is safe to operate without danger of electrical shock. Plug the AC line cord directly to the AC outlet (do not use a line isolation transformer during this check). Use an AC voltmeter having 5K ohms volt sensitivity or more in the following manner.

Connect a 1.5K ohm 10 watt resistor paralleled by a 0.15 μ F AC type capacitor, between a good earth ground (water pipe, conductor etc.,) and the exposed metallic parts, one at a time.

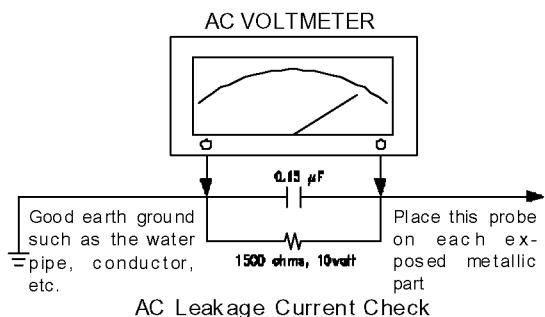
Measure the AC voltage across the combination of the 1.5K ohm resistor and 0.15 uF capacitor. Reverse the AC plug at the AC outlet and repeat the AC voltage measurements for each exposed metallic part.

The measured voltage must not exceed 0.3V RMS. This corresponds to 0.5mA AC. Any value exceeding this limit constitutes a potential shock hazard and must be corrected immediately.

The resistance measurement should be done between accessible exposed metal parts and power cord plug prongs with the power switch "ON". The resistance should be more than 6M ohms.

PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in this TV receiver have special safety-related characteristics. These characteristics are offer passed unnoticed by visual spection and the protection afforded by them cannot necessarily be obtained by using replacement components rates for a higher voltage, wattage, etc. The replacement parts which have these special safety characteristics are identified by Δ marks on the schematic diagram and on the parts list. Before replacing any of these components, read the parts list in this manual carefully. The use of substitute replacement parts which do not have the same safety characteristics as specified in the parts list may create shock, fire, X-RAY RADIATION or other hazards.



Product Specification

1.1 VIDEO SECTION	
	CMO V270B1-L01 MK8202 USA
Display size	27"/16:9
Display Resolution	1366 X 768
Pixel Pitch	146.0μm (H) x 436.5μm (V)
Peak Brightness	550(nits)
Contract Ratio	1000:1, Typical (1/100 White Window, Dark Room)
View Angle	Hor. And Vert. ≥170 degree
Color Deeps	16.7M Color (R / G / B each 256 Scales)
PC Resolution Supporting	VGA, SVGA, XGA, WXGA
HDTV Compatible	480p/720p/1080i
Progressive Scanning	Yes
Film Mode Pull Down	Yes
“GAMMA” Correction	Yes
Color Temperature Control	Yes
Comb Filter	Yes
Second De-interlace for Sub picture	No
Wide Mode	Full, 4:3 and Panoramic.
TV System	NTSC M
Dual Tuner System	No
AV Input Color System	PAL /NTSC
PIP	No
1.2 AUDIO SECTION	
Audio Output Power	7W×2 (8 ohm)
Sound Effect	Spatial Effect and Surround
Tone Control	Yes
1.3 Input Terminals	D-Sub 15 Pin Type (Analog-RGB Input) ×1 HDMI (Ver 1.1) Connector x 1 D-Sub 9 Pin (RS-232) RF (F-type Input) ×2 (ATV, DTV) Component Video-YPbPr × 1 RCA Terminals S-Video Input (Mini Din 4Pin) ×1 Video Input RCA Terminal x 1 Stereo Audio Input x 4
1.4 Output Terminals	Audio Output (RCA ; L&R Type) ×1
1.5 Others	
Closed Caption / V-Chip	Yes
Teletext	No
OSD Language	English, Français, Español

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Reference No : LCT2701AD

Stereo Decode	MTS with SAP
Power Rating	AC 120V, 60Hz
Power Consumption	≤180W

1.6 Support the Signal Mode

This machine can support the different from VGA signal mode in 6 kinds

Resolution	Horizontal Frequency (kHz)	Vertical Frequency (kHz)
640 x 480	31.50	60.00
	37.86	72.81
800 x 600	35.16	56.25
	37.90	60.32
	48.08	72.19
1024 x 768	48.40	60.00

1.7 HDTV Mode (YPbPr)

Resolution	Horizontal Frequency (KHz)	Vertical Frequency (Hz)
480i	15.734	59.94
480p(720x480)	31.468	59.94
720p(1280x720)	45.00	60.00
1080i(1920x1080)	33.75	60.00

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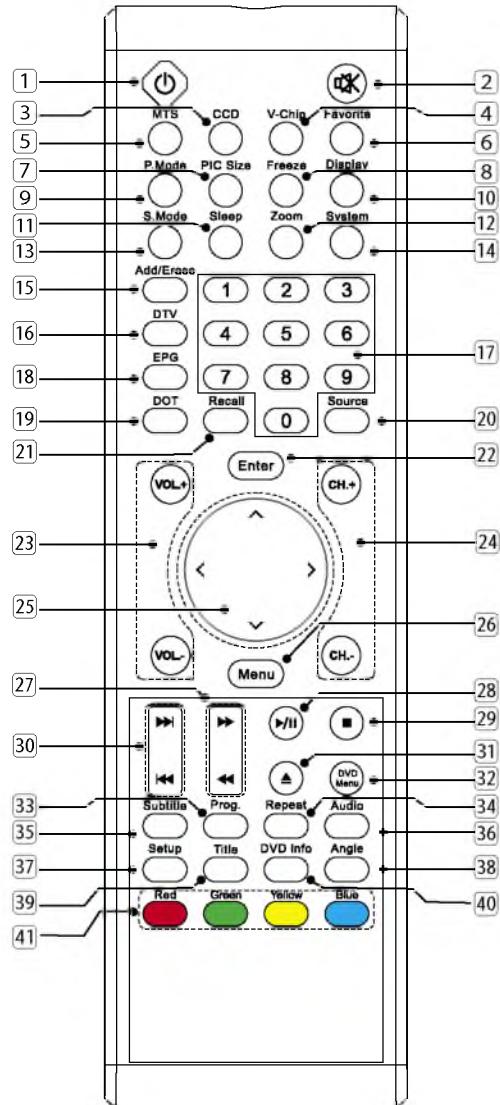
Reference No : LCT2701AD

Technical Data

1. Power supply	TV	AC 120V, 60Hz	
	Remote control	Battery 3V (UM-3/R6P/AAA×2)	
2. TV system	TV System	NTSC M	ATSC
	Stereo Decode	MTS	MPEG-2
	Closed Caption/V-Chip	Yes	Yes
	Channel	181 CH	2-69 CH
3. Intermediate frequencies	Picture	45.75MHz	
4. Scanning	Horizontal (Hz)	15625/15750	
	Vertical (Hz)	50/60	
5. AC plug		UL Plug	
6. Panel		V270B1-L01	
7. Speaker	Internal	8 ohm 10W ×2	
8. Operating temperature	Fulfill all specifications	15°C ~ 30°C	
	Accept picture/sound reproduction	5°C ~ 33°C	
9. Operating relative humidity	Fulfill all specifications	45% ~ 75%	
	Accept picture/sound reproduction	20% ~ 80%	
10. Electrical & optical specification		See the attachment 1.	
11. Circuit diagram drawing No.			
12. Cabinet			
13. Cabinet color			
14. Packing		1 set per	
15. Container stuffing method		RD/05/P/LC26HAB/CSI/02 REV: 01	
16. Dimension (mm) (No packing)	LCD-TV	698(W) × 513(H) × 99(D)mm (w/o Stand)	
		698(W) × 554(H) × 250(D)mm (with Stand)	
	Remote control unit	183(L) × 53(W) × 28(T)mm	
17. Net weight	LCD-TV	13.9Kg (with Stand) approx.	
	Remote control	93g	
18. Cell Defect		Subject to Panel supplier specification	

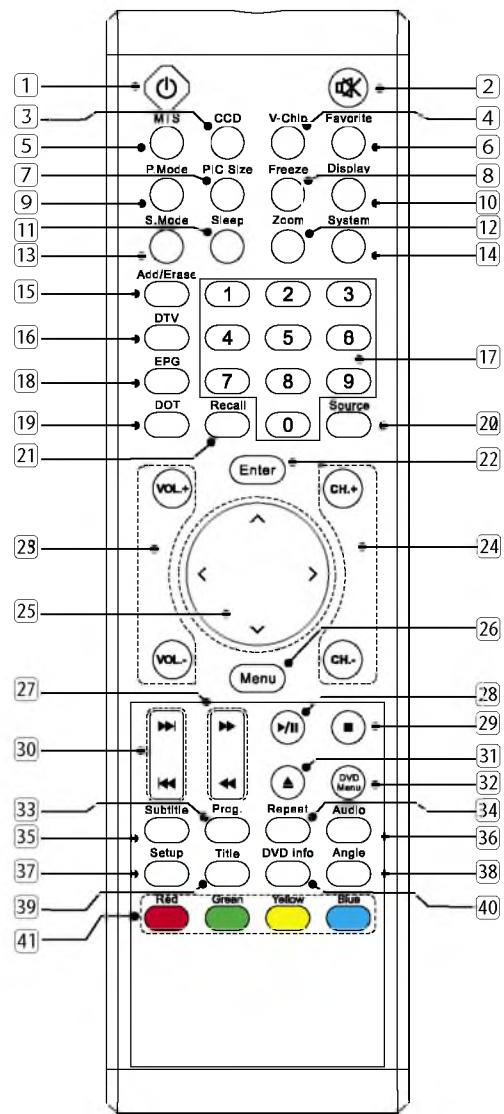
Remote Control

- 1 **Power (⊕)**: Press to turn on and off.
- 2 **Mute (🔇)**: Press to mute the sound. Press again or press VOL+/- to restore the sound.
- 3 **CCD**: Press to select the Closed Caption mode.
- 4 **V-Chip**: Press to select the child protect mode.
- 5 **MTS**: Press repeatedly to cycle through the Multi-channel TV sound (MTS) options: Mono, Stereo and SAP (Second Audio Program).
- 6 **Favorite**: Press repeatedly to cycle through the favorite channel list.
- 7 **PIC.Size**: Press to change the screen size, such as Full, 4:3, Panoramic. (Note: In VGA mode, it can select picture size is Full. While in DTV mode, it can select picture size is: Full and 4:3.)
- 8 **Freeze**: Press to freeze the picture, press again to restore the picture. (This button is not available for VGA mode.)
- 9 **P.Mode**: Press repeatedly to cycle through the picture mode: Hi-Bright, User, Cinema, Normal and Vivid.
- 10 **Display**: Press to display the channel information and it disappear after 3 seconds.
- 11 **Sleep**: Press repeatedly until it displays the time in minutes (15 Min, 30 Min, 60Min, 90 Min ,120 Min and, OFF) that you want the TV to remain on before shutting off. To cancel sleep time, press **Sleep** button repeatedly until sleep OFF appears.
- 12 **Zoom**: Press to zoom the image. (This button is not available for VGA mode.)
- 13 **S.Mode**: Press repeatedly to cycle through the sound mode: Normal, News, Cinema, Concert and User.
- 14 **System**: Press repeatedly to cycle through the system options: AUTO, NTSC3.58 and PAL. (This button is activate for AV, S-Video input source.)
- 15 **Add/Erase**: Press to add or delete favorite channel.
- 16 **DTV**: Press to select Digital TV mode.
- 17 **0~9 Number Buttons**: In TV mode, press 0~9 to select a channel; the channel changes after 2 seconds. In DVD mode, press 0~9 to input the items.
- 18 **EPG**: Press to display EPG (Electronic Program Guide) menu.
- 19 **DOT**: Press number buttons with it to select the channels directly in DTV.
- 20 **Source**: Press to select the signal source.
- 21 **Recall**: Press to return previous channel.
- 22 **Enter**: To select an item, press Enter to confirm.
- 23 **VOL +/-**: Press to adjust the volume.
- 24 **CH +/-**: Press to scan through channels. To scan quickly through channels, press and hold down either channels.
- 25 **<,>,<,>**: Press <,>,<,> to move the on-screen cursor.



(Continued on next page)

- 26 **Menu:** Press to enter on-screen setup menu, press again to exit.
 27 **◀◀, ▶▶ :** Press to search the backward or forward.
 28 **▶/II :** Press to play or pause the DVD disc.
 29 **■ :** Press to stop playing the disc.
 30 **◀◀, ▶▶ :** Press to skip the backward or forward.
 31 **▲ :** Press to open or close the disc tray.
 32 **DVD Menu:** Press to return DVD disc menu.
 33 **Prog.:** Press to display the program menu. Press it again to exit.
 34 **Repeat:** Press repeatedly to cycle through the options: CHAPTER, TITLE, ALL and nothing.
 35 **Subtitle:** Press to select desired DVD subtitle.
 36 **Audio:** Press to select desired audio track.
 37 **Setup:** Press to display a menu. Press it again to exit menu.
 38 **Angle:** Press to select desired viewing angle of the Video (disc feature).
 39 **Title:** Press to display to DVD disc title.
 40 **DVD Info:** Press to display DVD information.
 41 **Color Buttons:**
 (Only available in DTV EPG mode)
Red: Press this button to access the red item or page.
Blue: Press this button to access the blue item or page.
Green: Press this button to access the green item or page.
Yellow: Press this button to access the yellow item or page.



Note: Press CH+/- on the remote control can turn on TV set from last preview mode.

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Reference No : LCT2701AD

Attachment 1 : Electrical & Optical Specification

No.	Items		Instruction		Typical	Limit	Unit
1	Video sensitivity		For 30dB S/N		44	≤ 51	dBuV
2	FM sound sensitivity		For 30dB S/N		21	≤ 35	dBuV
3	Color sensitivity		For RF transmission		37	≤ 40	dBuV
4	CCD sensitivity		TV screen refreshes 40 times number of mistakes ≤ 8		43	≤ 50	dBuV
5	Minimum NICAM threshold		Without crackline noise		N/A	N/A	dBuV
6	Stereo Channel Separation		BTSC.		18	≥ 15	dB
7	AGC static characteristic		Accept. Picture/Sound repr.		90	≥ 90	dBuV
8	Selectivity		Adjacent sound carrier		30	≥ 28	dB
			Below adjacent sound carrier		30	≥ 30	
			Adjacent picture carrier		45	≥ 40	
			Up adjacent picture carrier		40	≥ 30	
9	IF rejection				55	≥ 45	dB
10	Image rejection		VHF		57	≥ 45	dB
			UHF		55	≥ 40	
11	AFT pull-in range				± 1.0	$\geq \pm 1.0 $	MHz
12	Chroma sync pull-in range				± 500	$\geq \pm 200 $	Hz
13	Color killer function				-11	≤ -10	dB
14	Resolution	RF	Horizontal	PAL	300	≥ 300	Lines
				NTSC	260	≥ 240	Lines
			Vertical	PAL	410	≥ 400	Lines
				NTSC	320	≥ 300	Lines
		Video	Horizontal		450	≥ 450	Lines
			Vertical		400	≥ 400	Lines

15	Color Coordination	White	Xw	Full Pattern		0.285	0.285±0.02	
			Yw			0.293	0.293±0.02	
16	View Angle(Lo/3)	Horizontal				170	≥ 170	Degree
		Vertical						
17	Overscan		Cross hatch signal		96	94~98	% mm	
18	Picture position		In all direction		± 2	$\leq \pm 3 $		
19	H sync pull-in range				± 400	$\geq \pm 200 $	Hz	
20	V sync pull-in range				6	≥ 6	Hz	
21	Audio frequency response		± 3 dB ref. to 1KHz		0.15~12	0.2~12	KHz	

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22	Max Audio Output Power		7×2	≥5.0×2	W
23	Audio output power 10% THD	1KHz 10% THD	6×2	≥4.0×2	W
24	THD	Po=0.5W	0.5	≤3	%
25	Signal to buzz ratio	coeighting	50	≥30	dB
26	Minimum volume hum	coeighting	6	≤10	mVrms
27	Maximum woofer output power		N/A	N/A	W
28	Woofer audio frequency response	?3dB ref. to 15Hz AV mode	N/A	N/A	Hz
29	Tone low frequency	100Hz ref. to 1KHz AV mode	±8	≥ ±3	dB
30	Tone high frequency	10KHz ref. to 1KHz AV mode	±8	≥ ±3	dB
31	Balance	Center	0	≤ ±2	dB
		Max.	3	>2	
		Min.	-35	≤-30	

32	Video input level		1.0	1±0.3	Vpp
33	Audio input level* (1)		1.0 *	0.5±0.3	Vrms
34	Video output level		N/A	N/A	Vrms
35	Audio output level* (2)		0.3 *	0.5±0.3	Vrms
36	AV Audio input max. level		2	≤2	Vrms
37	AV Audio output L/R Separation		35	≥30	
38	Power consumptuon	Operating	200	≤200	W
		Stand by	3	≤5	W
39	IR receiving distance	0 Degree	7	≥6	m
40	IR receiving angle	5m	60	≥45	Degree
			20	≥15	Degree
41	Dielectric strength	DC 3KV 1min.	5	≤10	mArms
42	The vibration noise from electromagnetic devices in LCD- TV set	The distance between the tester and the LCD-TV set is four times as many as the screen height	No obvious vibration noise can be heard		

Test Condition

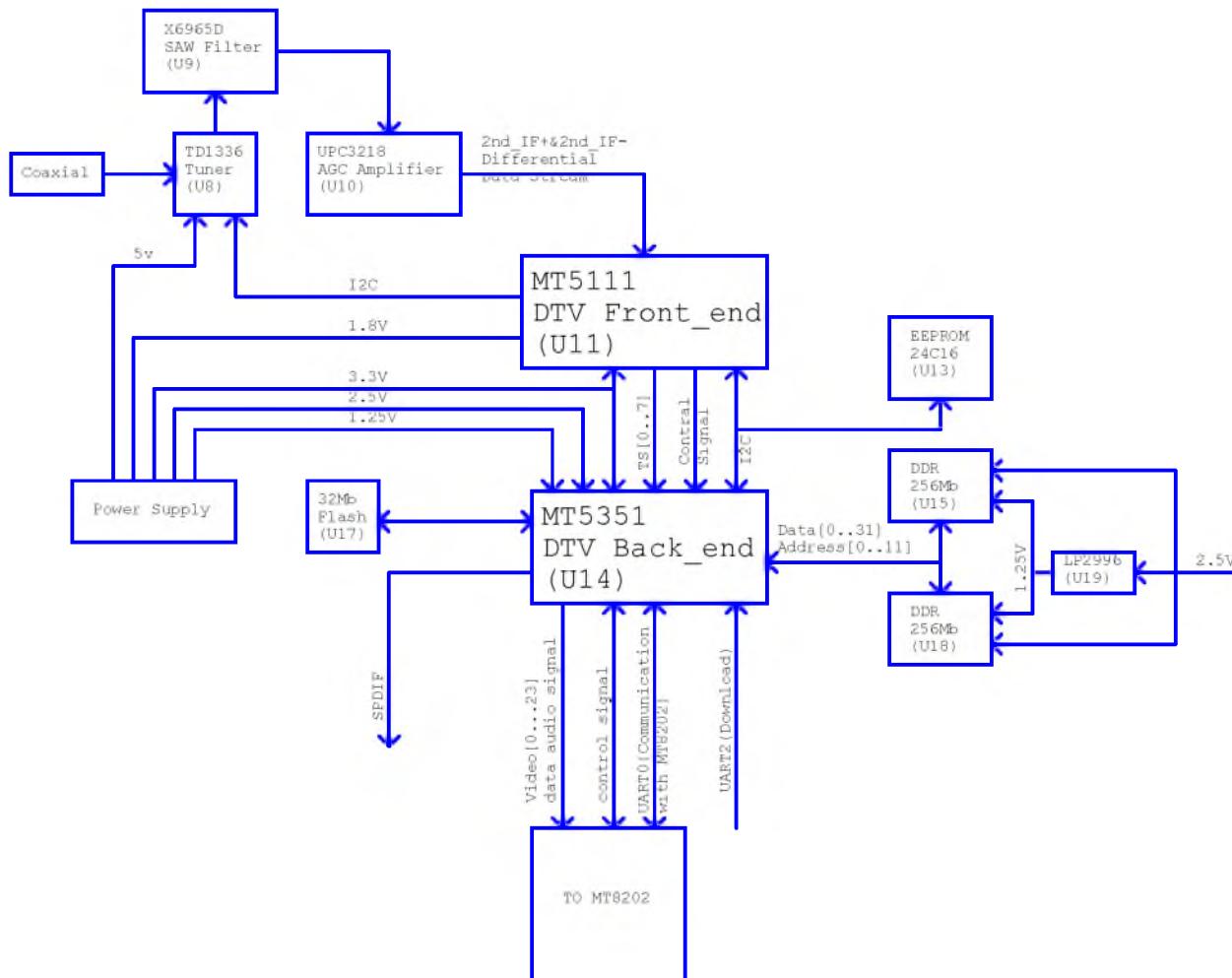
All tests shall be performed under the following conditions unless otherwise specified

1	Picture Modulation	87.5%
2	Sound Modulation	27KHz Dev. For DK/I/BG 15KHz Dev. For M/N
3	Picture to Sound Ratio	10dB
4	Sound Artificial Load Resistor	8 ohm
5	Video signal	Stair and Special
6	Audio signal	1KHz sine wave 0.5Vrms
7	Other conditions:	A. Switch LCD-TV on and let it warm up for more than 30 minutes. Viewing distance: 3H (H: Panel High) in front of LCD, about 2M. B. Brightness, Contrast, Saturation, Tint, sharpness set at normal. C. RF test point: Video output.
8	Note:	*(1) Now this project cannot fit the limited spec. the typical audio input level is 1.0 Vrms, *(2) The audio out level is controlled by the volume level, the range is from 0 to 0.5Vrms.

DVD player's spec. For LCD-TV Combo

Division	Section	Remarks
General	name	AKAI
	Marketing Area(setup default language)	USA
	Power supply	+5v,+3.3v
	Power Consumption	15W
	Manufactuer of Loader mechanism	Foryou DL06-LS
DVD Module	Optical Pick UP	Sanyo HD-62/65
	Chipset used	MTK 1389FE
Playback Disc Type	Playable Media Type	Playable Disc Type: DVD, CD,
	Playable Disc Type	DVD(Single/ Dual layer, Double sided), CD
	Disc Size	8cm/12cm
	Regional code	Regional 1
	NTSC/ PAL Disc playback	O/O
Video	Video output signal	NTSC
	Video DAC	27MHz/ 10bit
Audio	Audio DAC	48Khz/ 96KHz/24-bit:selectable
	Dynamic range	Present
	Dolby digital decoder	Present
	DTS decoder	optional
	SRS + TruSurround for 2 channel	Not present
	3D Virtual surround for 2 channel	Not present
Playback Features	Fast forward/backward	x2,x4,x8,x16,x32
	Slow motion forward	x1/2,x1/4,x1/8,x1/16
	Slow motion backward	optional
	Still picture	Present
	Frame by frame forward/reverse	Forward only (Step function)
	Skip forward/reverse	Present
	Repeat function	Present
	DVD closed caption	Present
	Transition Effect for picture CD	Not present
	Rotation of picture for picture CDs	Present
Display user operation	Last Memory	Present
	Graphical user interface	Not present
	OSD Language	3 (ENG is base ,SPA and French)
	Subtitle	Present
	Screen saver	Present
	Resume play	Present
	Program function	Present
	PBC ON/OFF	Default on PCB
	Parental lock	Password : 0000
	Picture mode selector	16:9, 4:3 LB, 4:3 PS(4:3 PS as default)
	Intro scan	Not present
	Digest in VCD	Present, only for PIC CD
	Time search	Present
	Multi angle	Present
Front Panel	Selectable audio language streams	Present
	kalaoke function	x
	VFD/ LED	x
Rear Panel	No. of keys	3(Open/Close, Play, Stop)
	Composite Video output	x
	Component Video output	x
	Progressive scan output (480P)	Present
	2 channel audio output	Present
	Coaxial audio output	Present

ATSC SYSTEM



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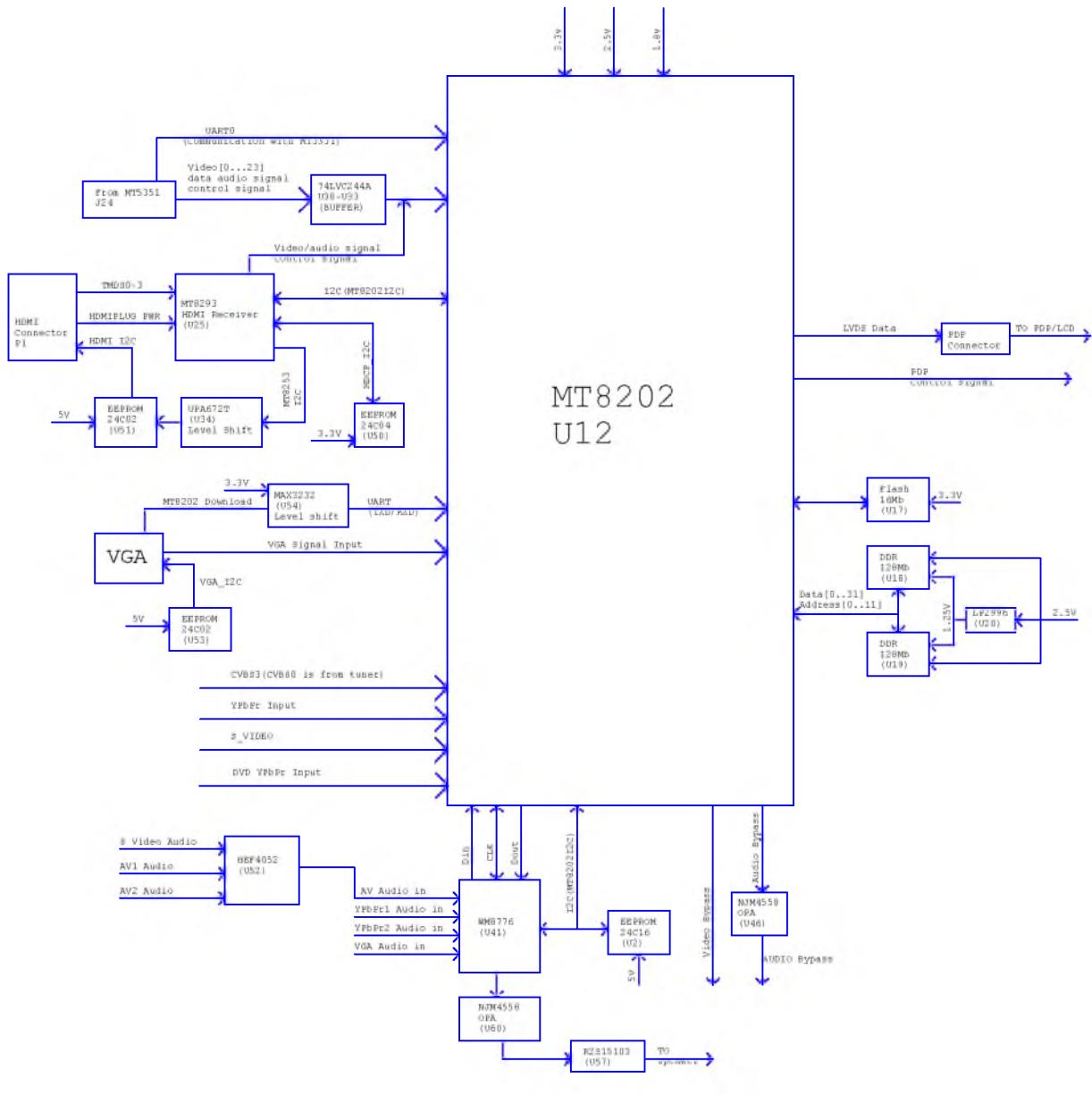
MT5351 Block

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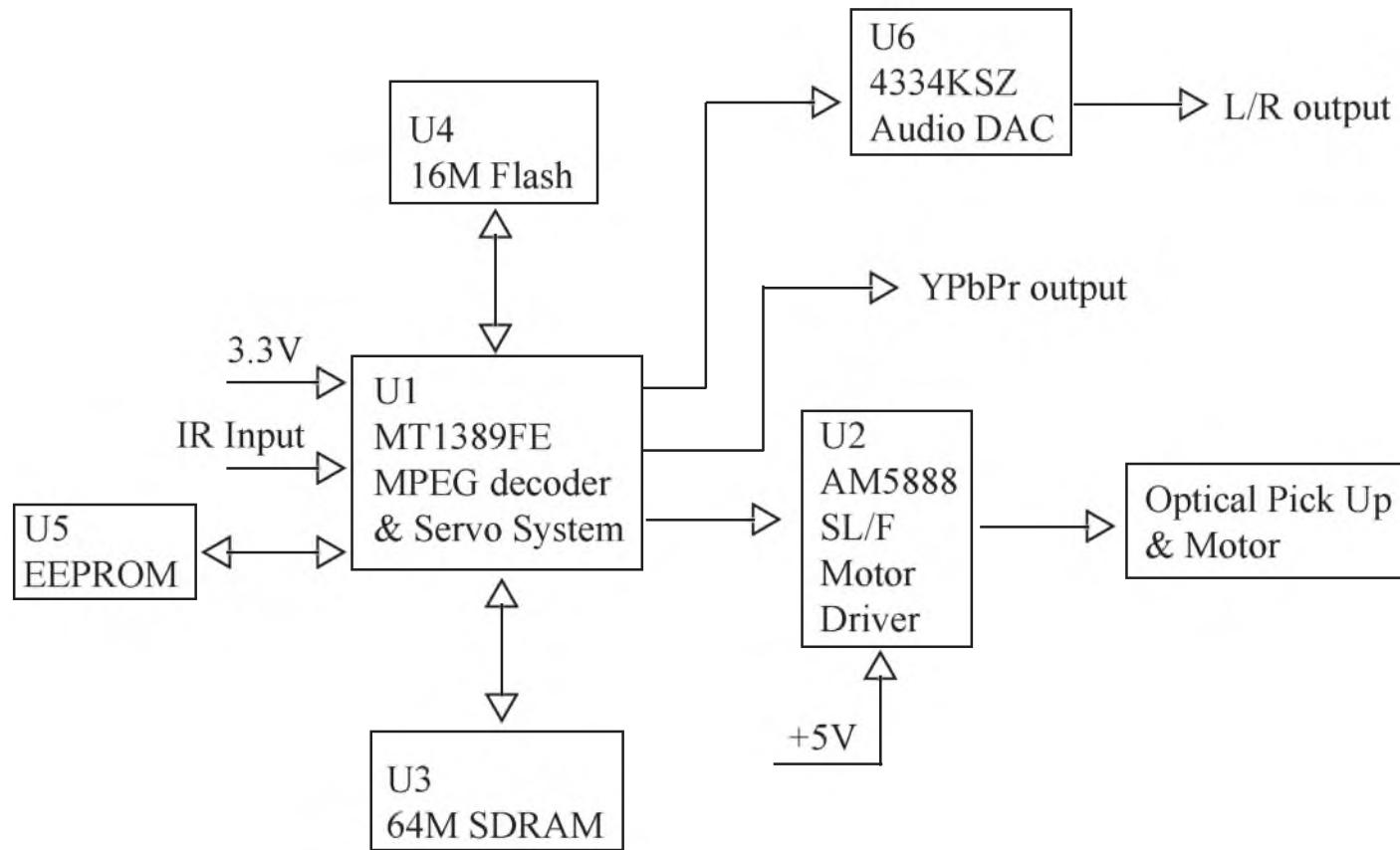
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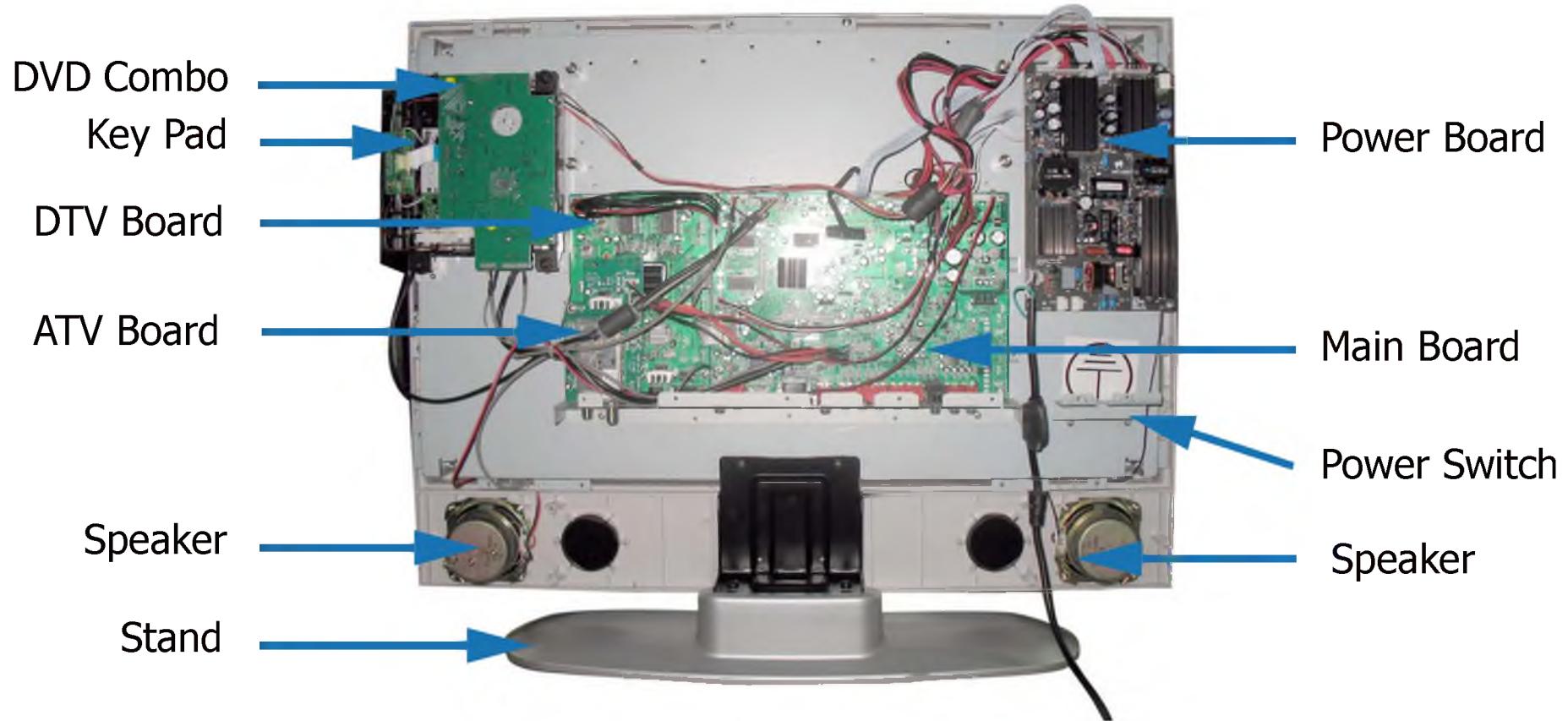
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Combo DVD



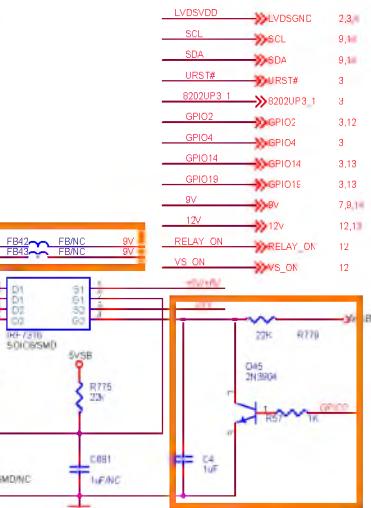
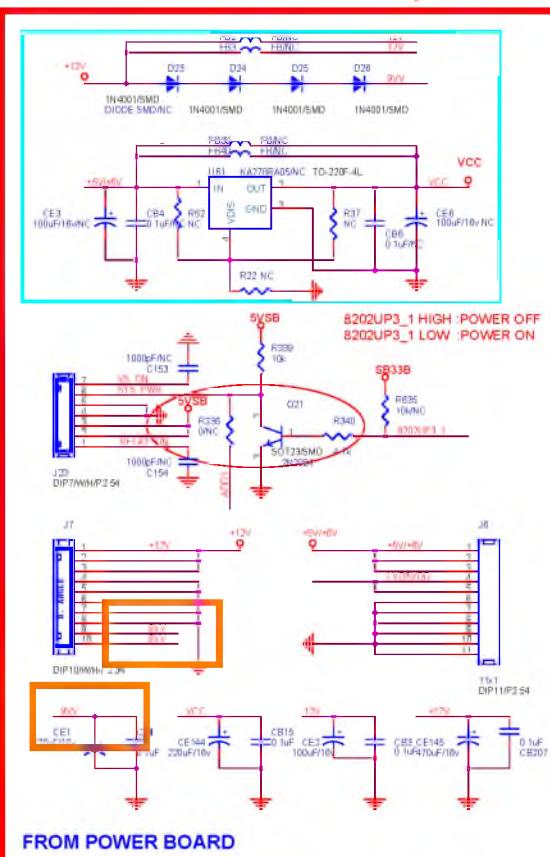
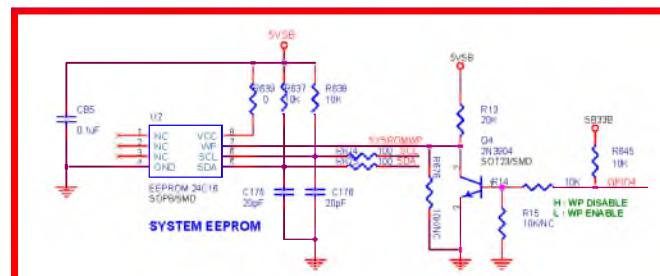
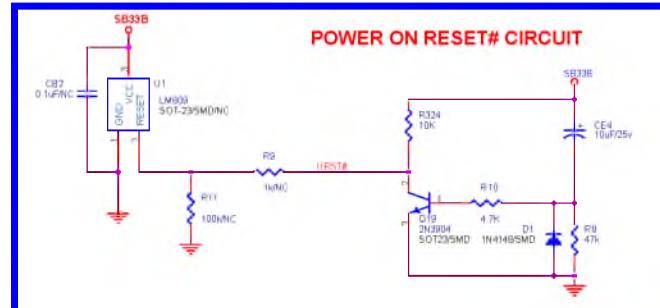
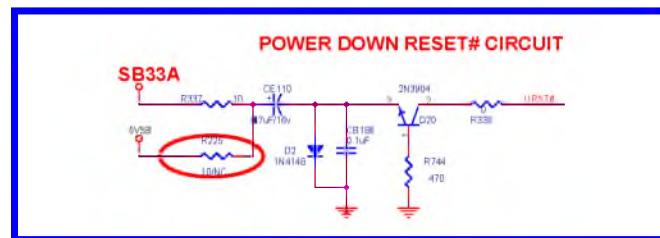
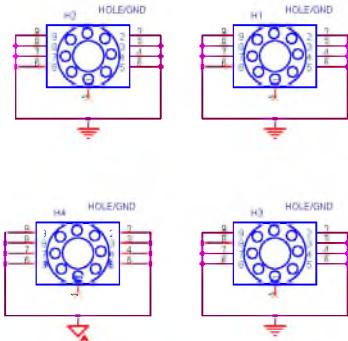
Parts Position



MT8202E (PBGA388) LCDTV BOARD 4 LAYERS FOR AKAI

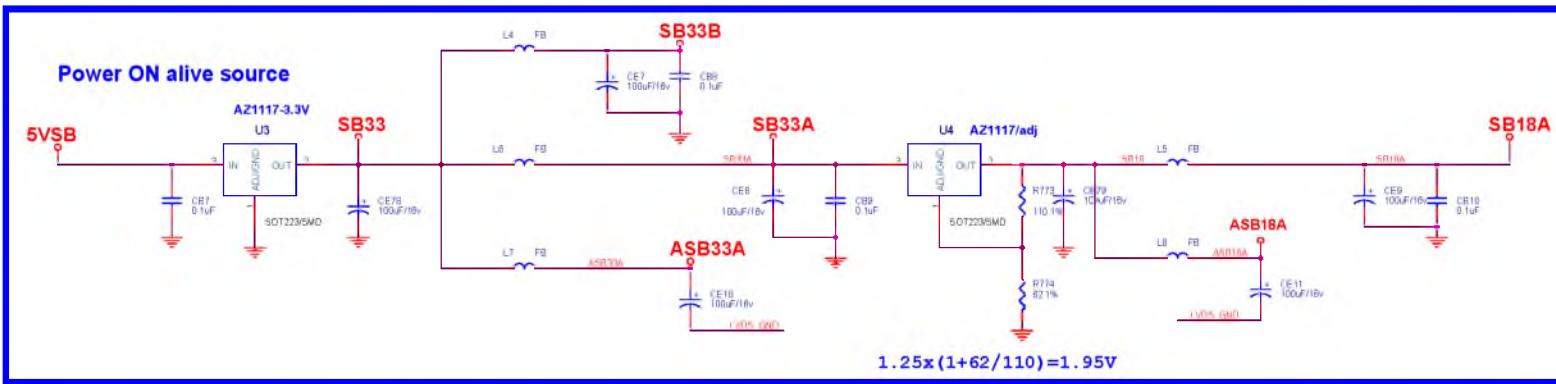
1. INDEX / POWER / RESET / EEPROM
 2. LDO
 3. MT8202E PBGA388
 4. MT8202 DECOUPLING
 5. DDR MEMORY & FLASH
 6. MT5351 INTERFACE
 7. HDMI MT8293
 8. DAUGHTER BOARD IN
 9. WM8776 & VIDEO BYPASS
 10. AUDIO / VIDEO IN CIRCUIT
 11. VGA & PC AUDIO IN
 12. LVDS OUT
 13. BACK LIGHT / KEYPAD
 14. TUNER IN
 15. AV IN
 16. AUDIO IN
 17. AUDIO Amplifier

Rev	History	P#	Date
AKAI_MT8202_27US_LVDS_V0.0	New		2005/11/22
AKAI_MT8202_27US_HDMI_LVDS_V0.0	ADD HDMI / VIDEO /AUDIO CONNECTOR INPUT IN		

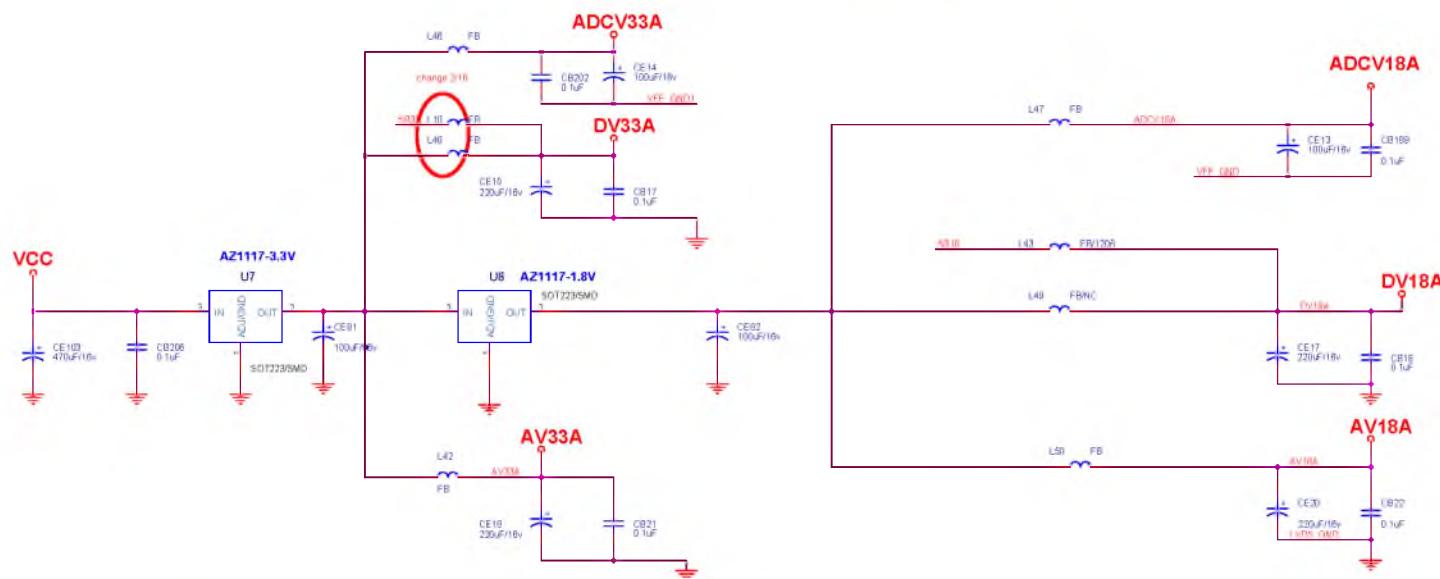


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INDEX / POWER / RESET / EEPROM			
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C	AKAL_MTS202_27US_LVDS_V0.0	Checked	Checked
Date	Thursday, April 13, 2006	Comments	1

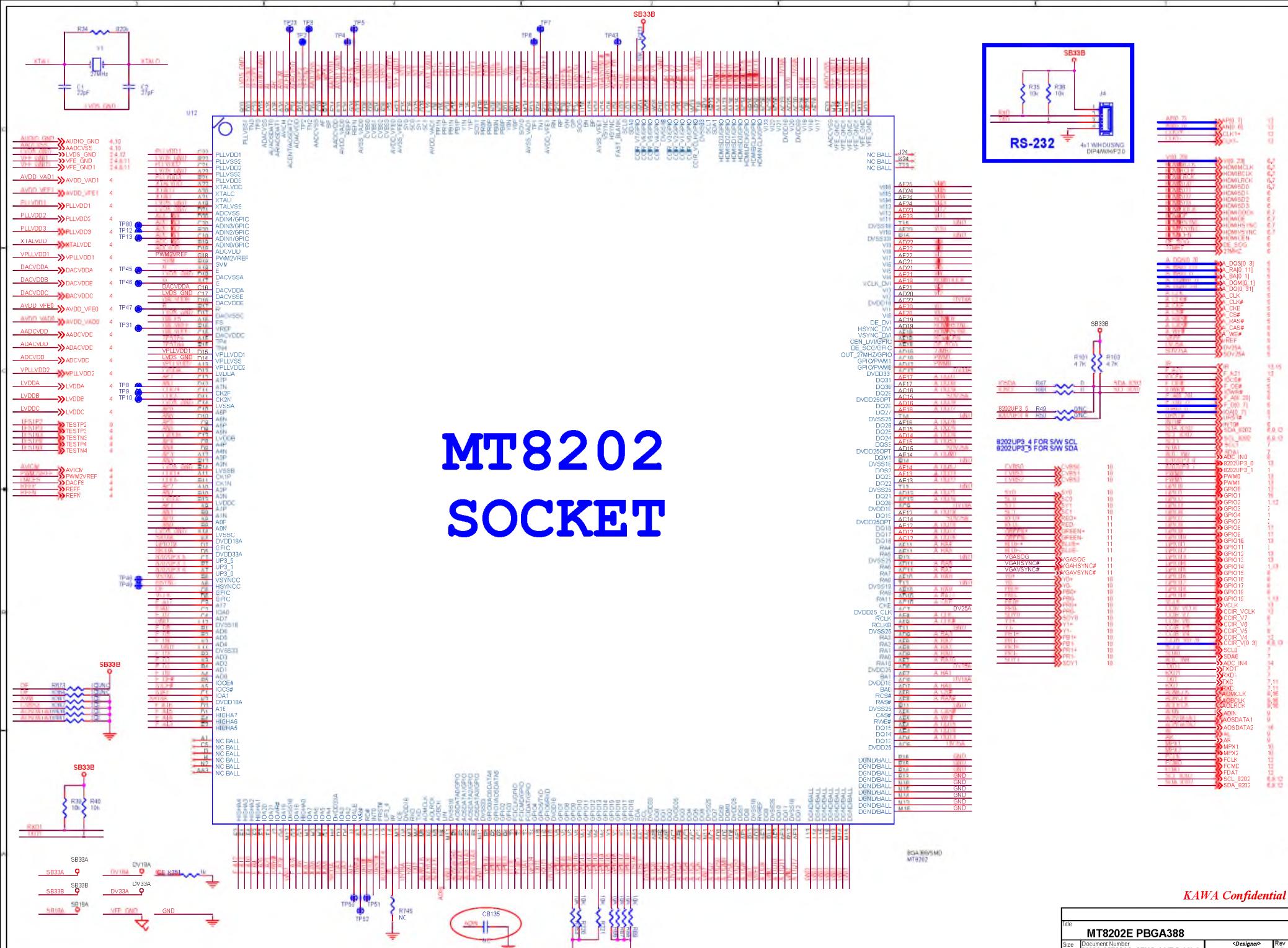


L4FB >> LVDS_GND 3.4.12
SOF_ZND >> VFE_GND 3.4.8.11
SOF_ZND >> VTE_GND 3.4.8.11



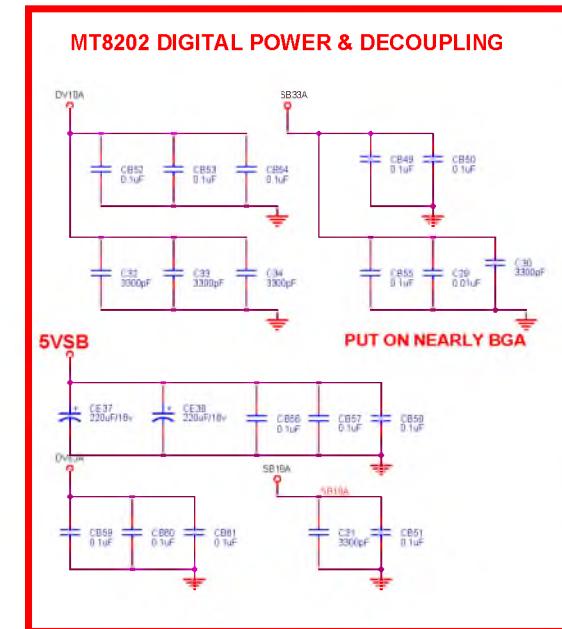
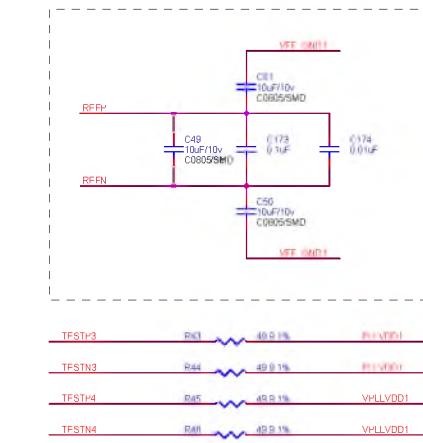
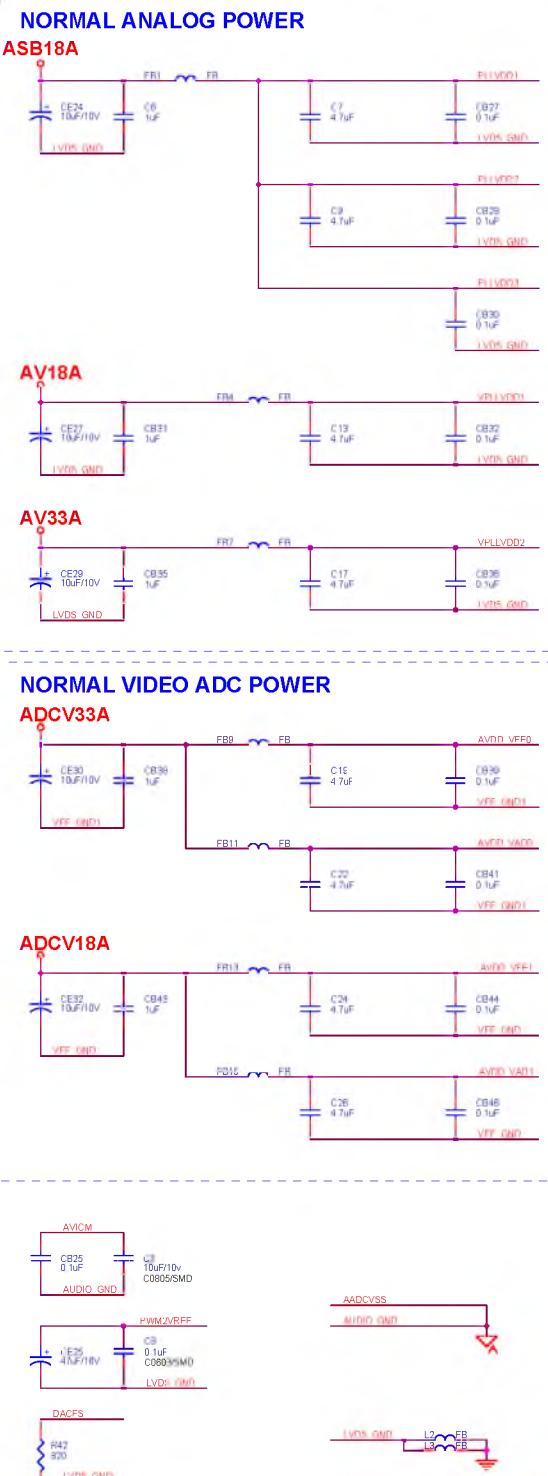
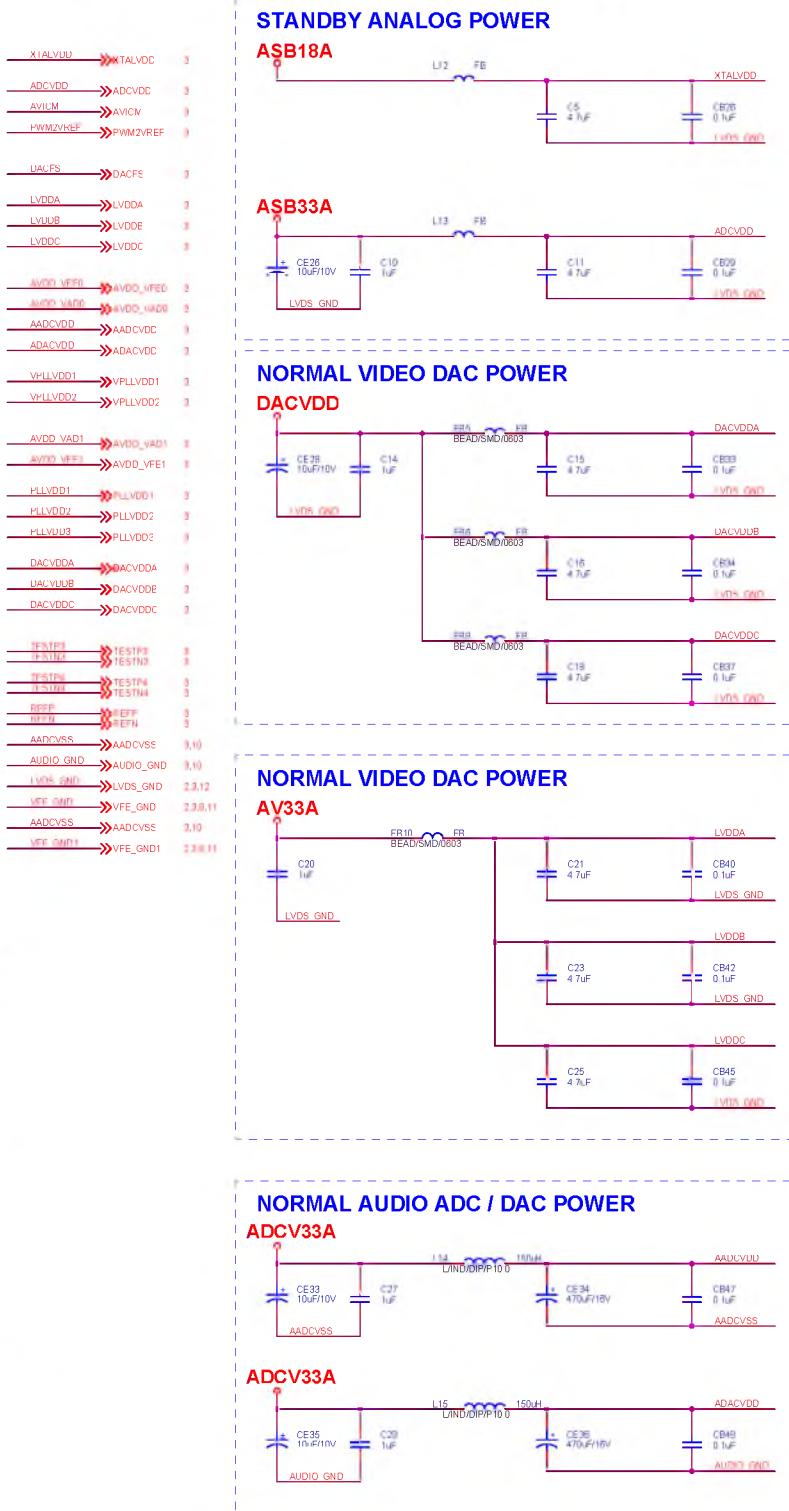
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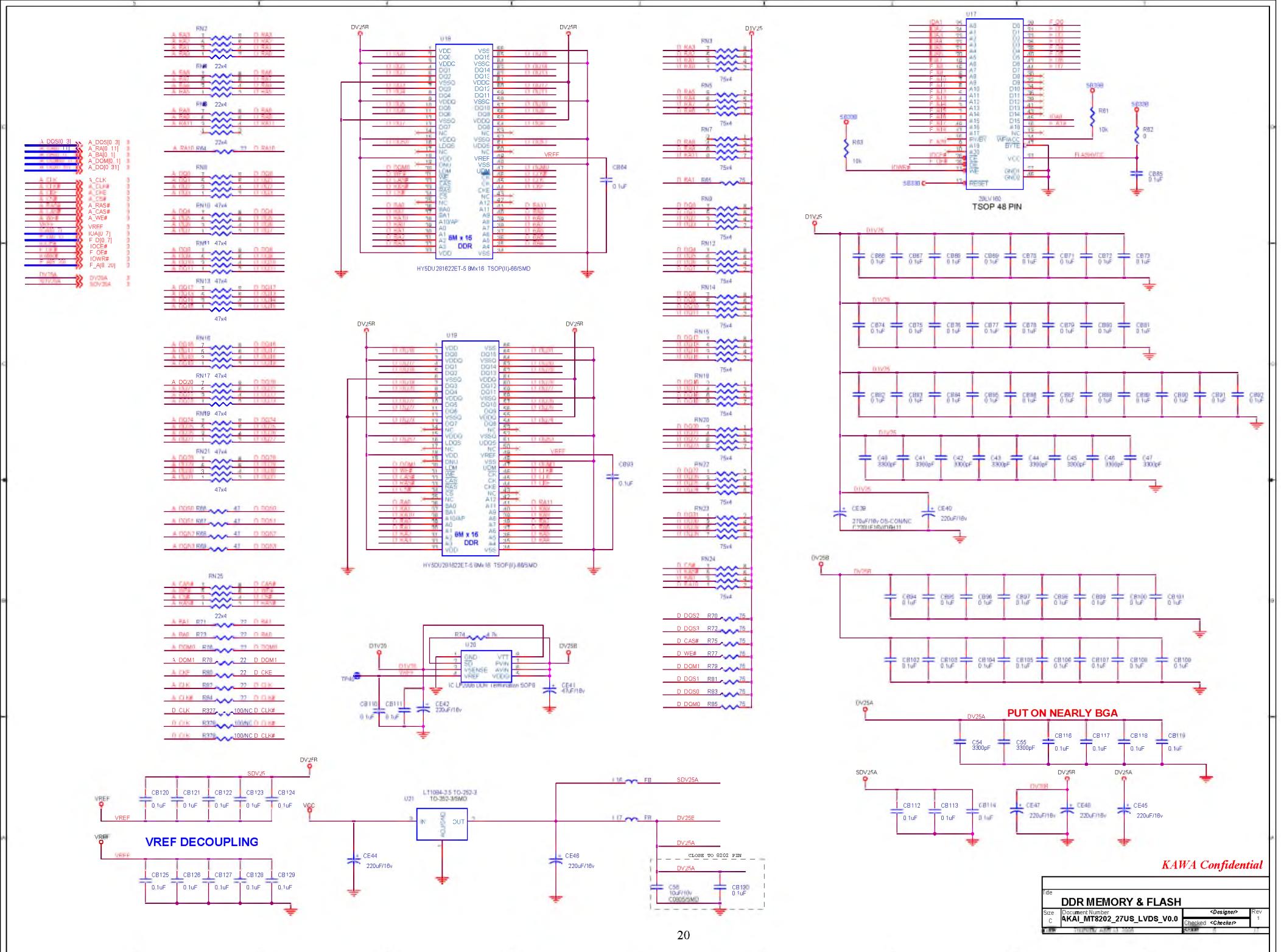
LDO	
Size	Document Number
C	AKAI_MT8202_27US_LVDS_V0.0
1.00W	4Designers 1REV 4Required 4Checkers 1.00W 1.00W 1.00W 1.00W



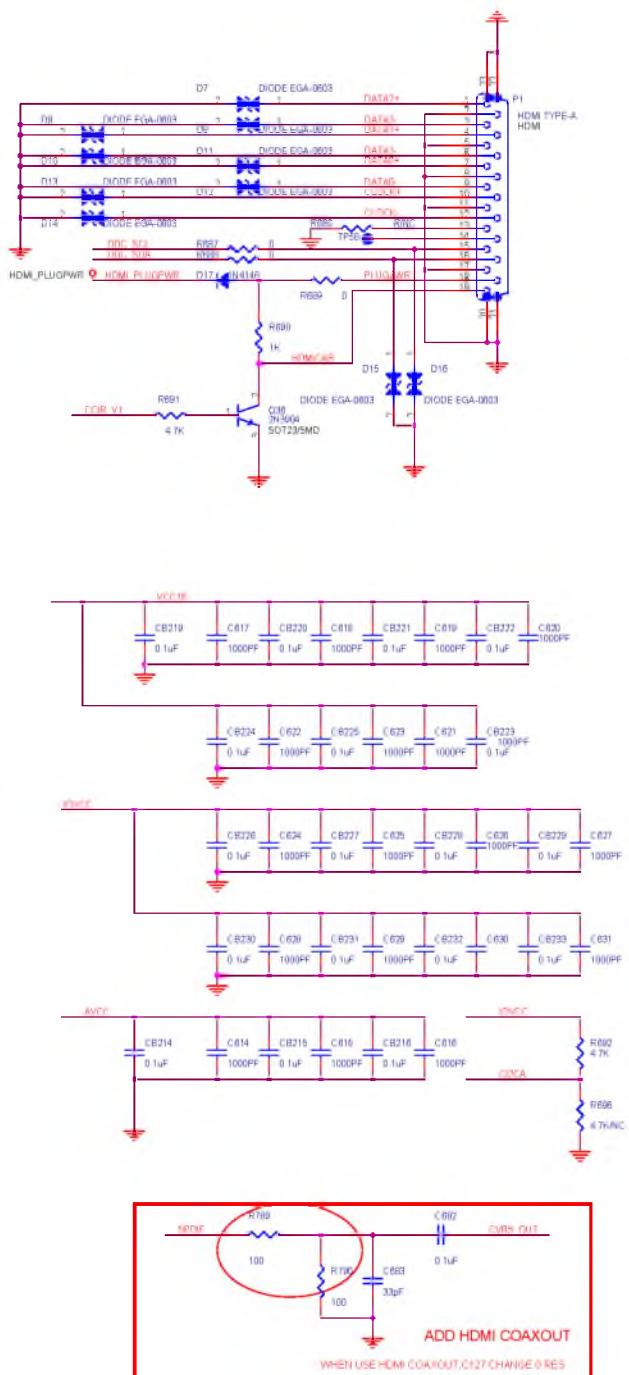
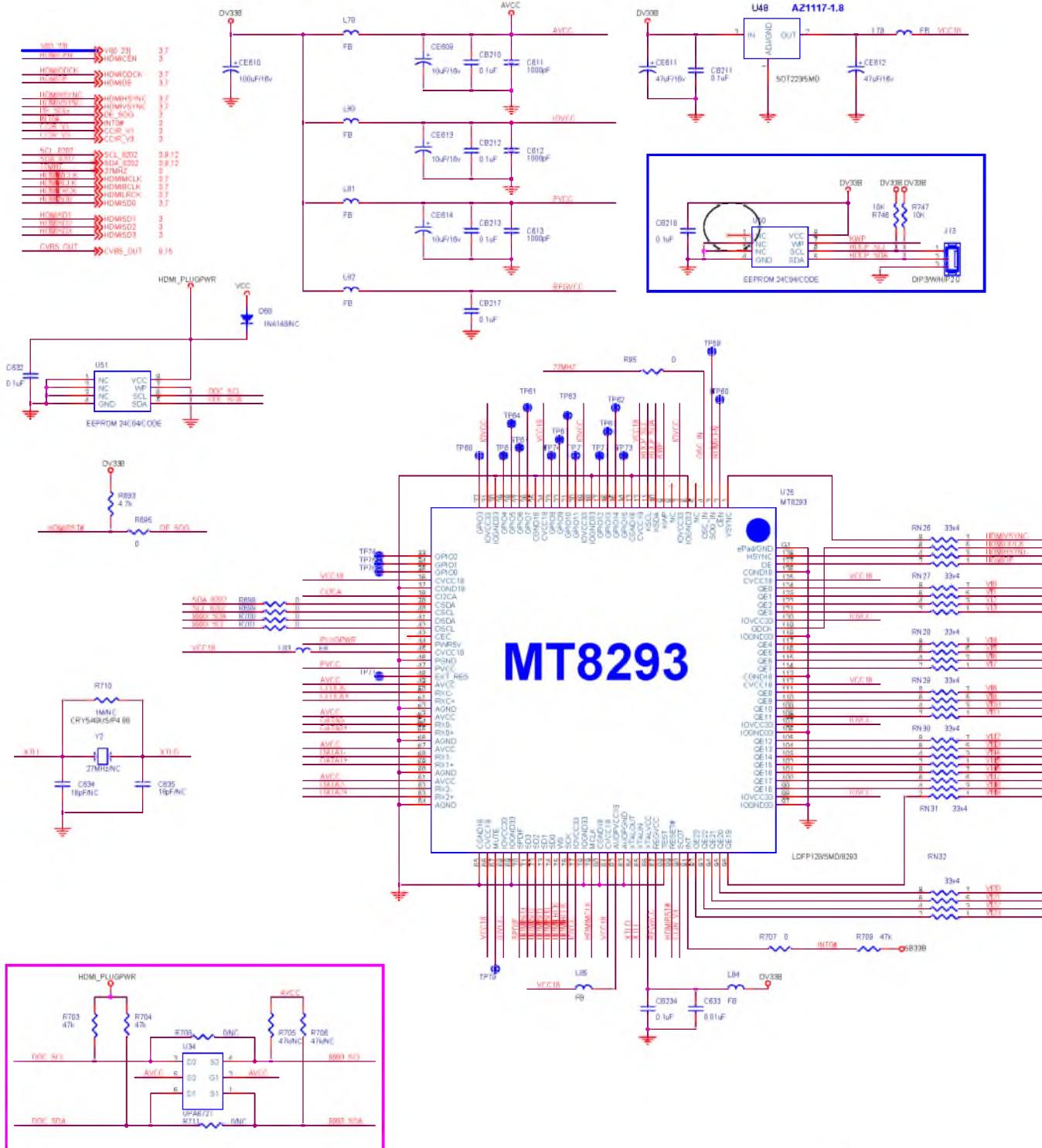
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Title		MT8202E PBGA388	
Size	Document Number:	<Designer>	Rev
C	AKAI_MT8202_27US_LVDS_V0.0	Checked	1
Date	Thursday April 30 2008	Sheet	17



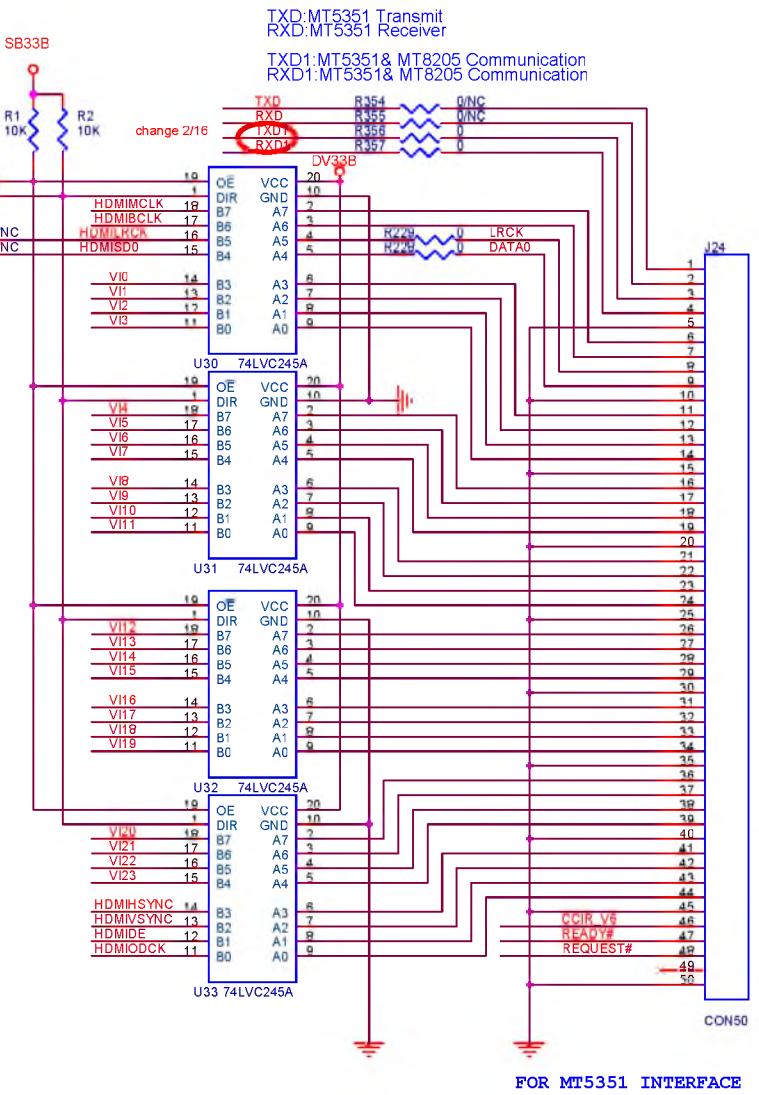
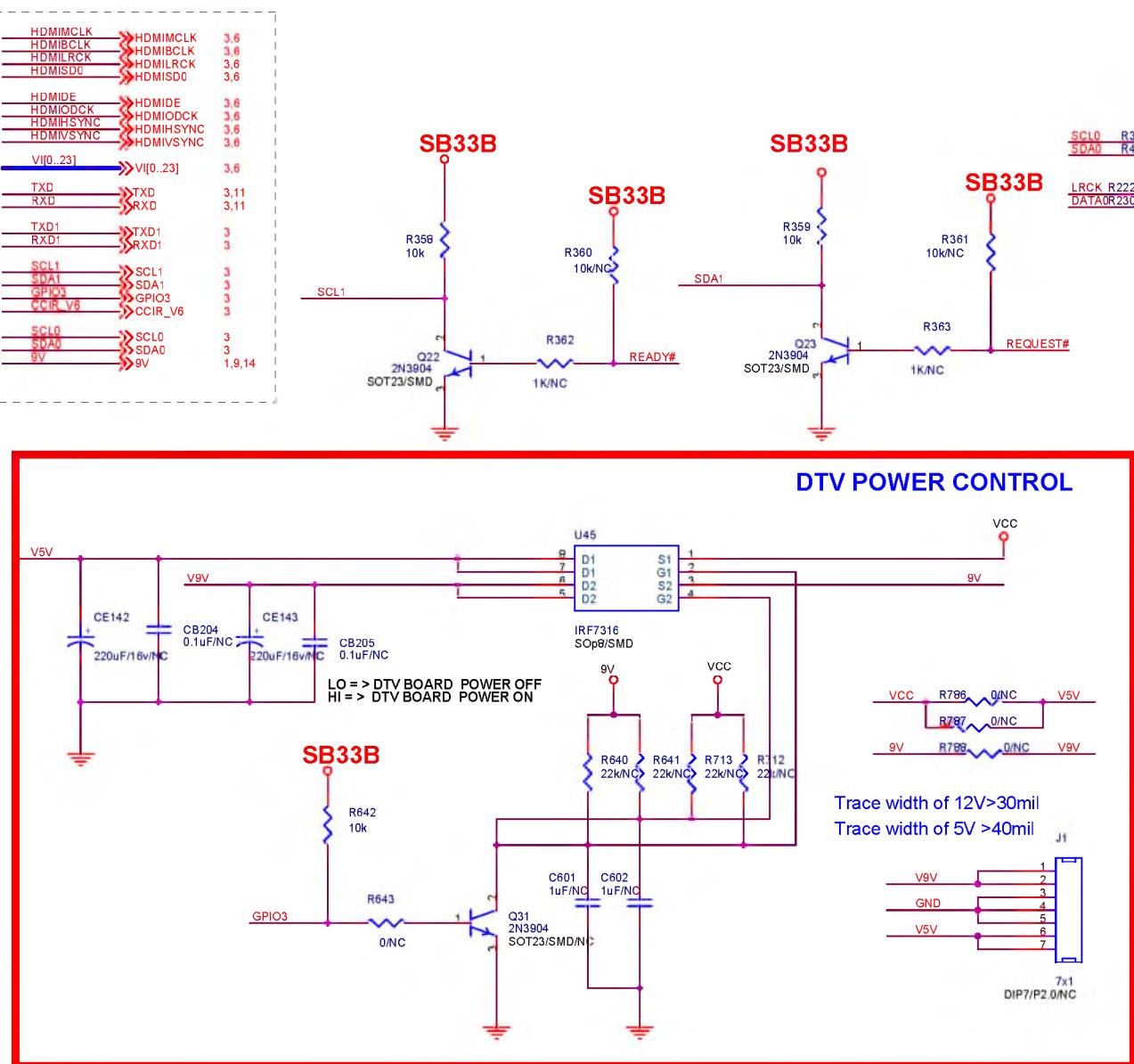


MT8293



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HDMI INPUT MT8293	
Size	Document number
C	AKAI_MT8202_27US_LVDS_V0.0
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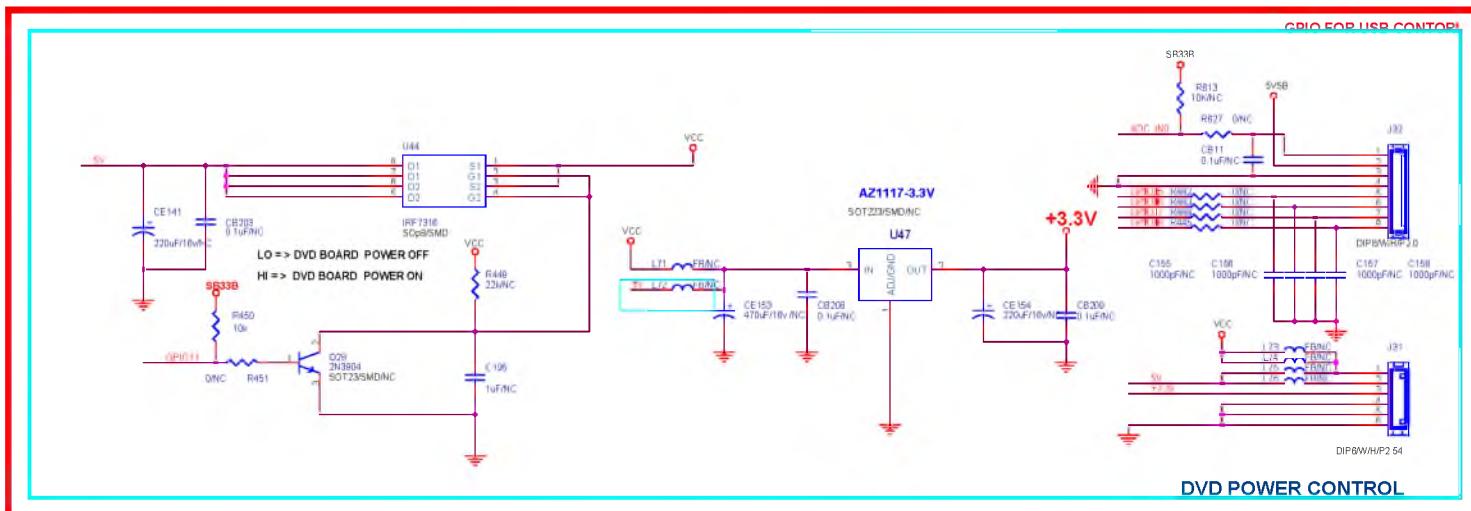
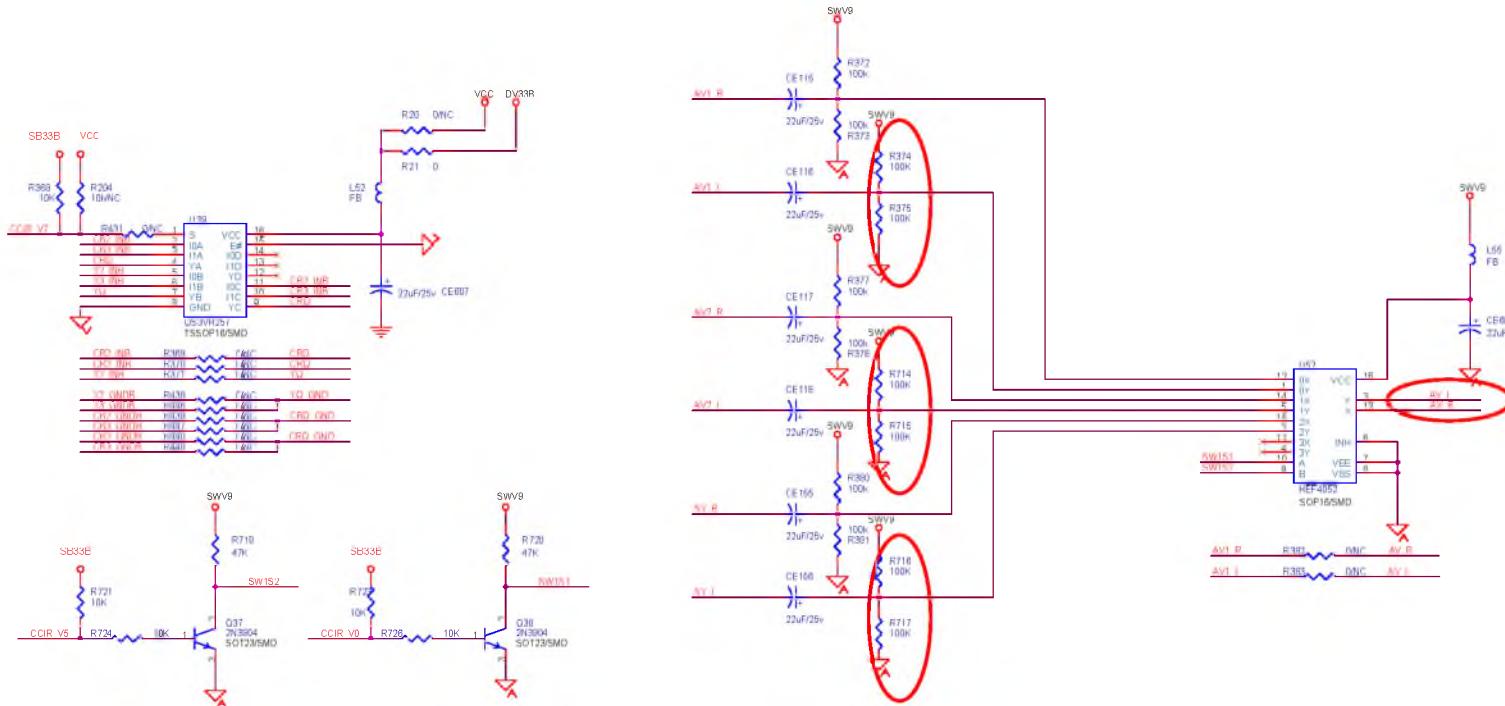
Title	MT5351 INTERFACE		
Size	Document Number	<Designer>	Rev
B	AKAI_MT8202_27US_LVDS_V0.0	Checked: <checker>	1
Date:	Thursday, April 13, 2006	Sheet	7 of 17

INPUT

ADC IN0	9
CCIR V0	10
CCIR V5	11
CCIR V7	12
GHD11	13
GHD12	14
GHD13	15
GHD14	16
GHD15	17
GHD16	18
VFD SND	23, 24, 25
AADVS5	26, 27, 28
AV1_B	29
AV1_L	30
AV2_B	31
AV2_L	32
SY_R	33
SY_L	34
Y1_NIN	35
Y2_GNDR	36
CR1_NIN	37
CR2_NINR	38
CR2_GNDR	39
Y3_NIN	40
CR3_NIN	41
CR3_GNDR	42
CR4_NIN	43
CR4_GNDR	44
UH3_GNDB	45
8V	46
	1, 2, 34

OUTPUT

AV_B	8
AV_L	9
CRO	10
CHD	11
Y0_GND	12
CRO GND	13
CHD GND	14
	15


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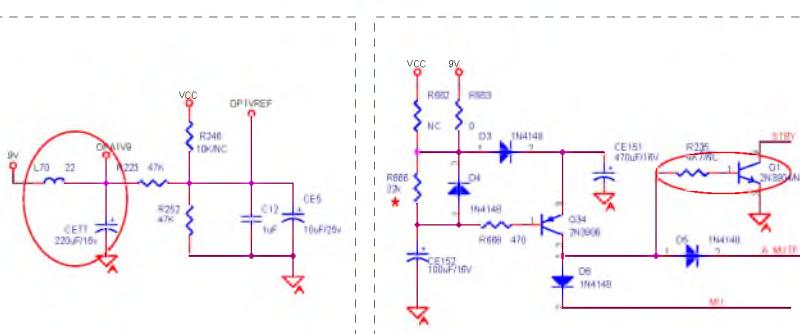
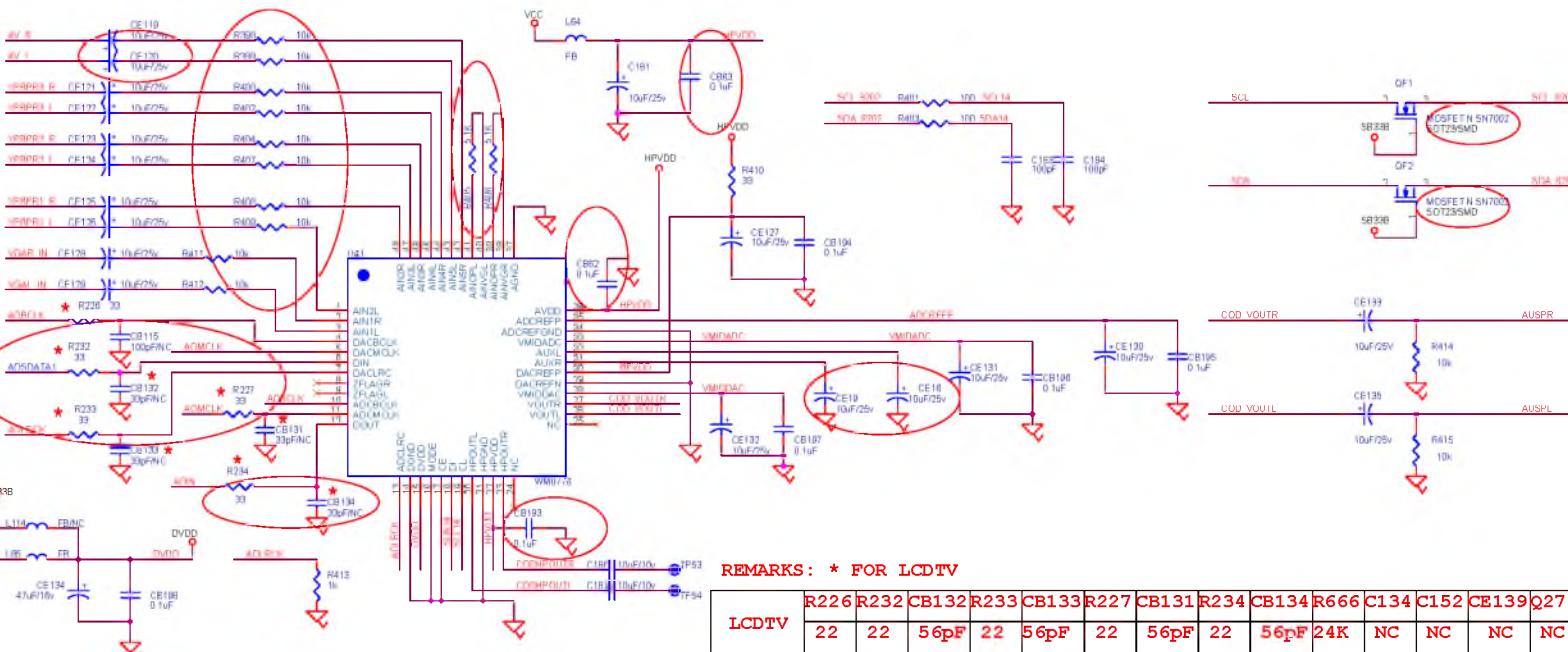
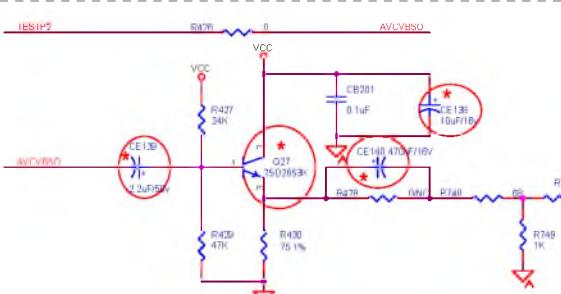
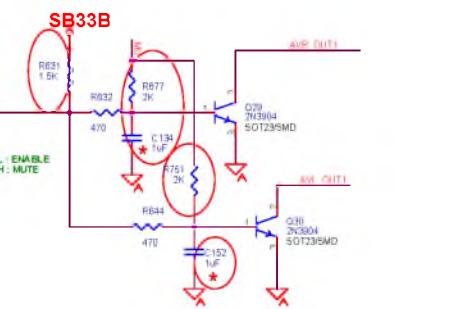
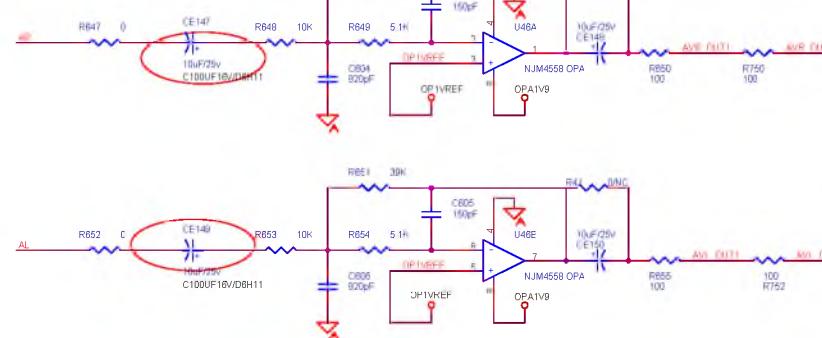
DAUGHTER BOARD IN	
Size	Document Number
AKAI_MT8202_27US_LVDS_V0.0	Design:
Page	Rev:
1	1
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Page	Sheet:
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INPUT

GPIO7	GP107	3
SCL	SCL	1.4
SPI_0[3]	SDA_B022	1.4
SPI_0[2]	SCL_B022	3.6.12
SPI_0[1]	SPI_0[1]	3.6.12
SPI_0[0]	AOGCLK	3.16
AGMCLK	AGMCLK	3.16
AGOLCK	AGOLCK	3.16
AGOLCK	AGOLCK	3.16
ADIN	ADIN	3
AV_IN	AV_IN	8
YBPR1_L	YBPR1_L	15
YBPR1_R	YBPR1_R	15
YBPR2_L	YBPR2_L	15
YBPR2_R	YBPR2_R	15
YBPR3_L	YBPR3_L	15
YBPR3_R	YBPR3_R	15
VGA_IN	VGA_IN	11
TESTP2	TESTP2	11
AR	AR	12
BI	BI	16
A_MUTE	A_MUTE	17
IP	IP	1.7.14

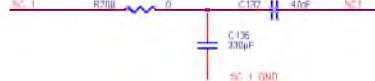
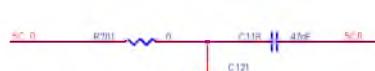
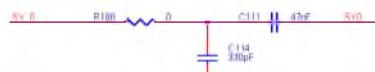
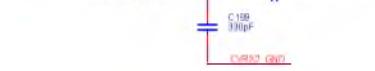
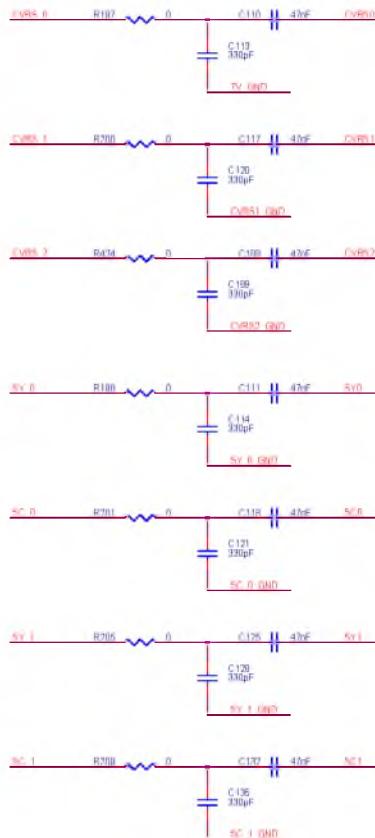
OUTPUT

AUSPR	AUSPR	16
AUSR	AUSR	15
AUSL	AUSL	15
AVL_OUT	AVL_OUT	15
AVL_OUT	AVL_OUT	8.15


BYPASS VIDEO OUTPUT

AUDIO BYPASS MUTE

BYPASS AUDIO OUTPUT

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Size	Document Number	<Designer>	Rev
C	AKAI_MT8202_27US_LVDS_V0.0	Checked	<Checker>
Date	Saturday April 22, 2006	Sheet	1/1

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FROM AV BOARD

AMV05 → MADC155 34



Y0 GND

Y1 GND

Y2 GND

Y3 GND

Y4 GND

Y5 GND

Y6 GND

Y7 GND

Y8 GND

Y9 GND

Y10 GND

Y11 GND

Y12 GND

Y13 GND

Y14 GND

Y15 GND

Y16 GND

Y17 GND

Y18 GND

Y19 GND

Y20 GND

Y21 GND

Y22 GND

Y23 GND

Y24 GND

Y25 GND

Y26 GND

Y27 GND

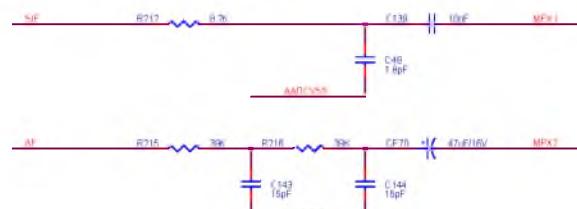
Y28 GND

Y29 GND

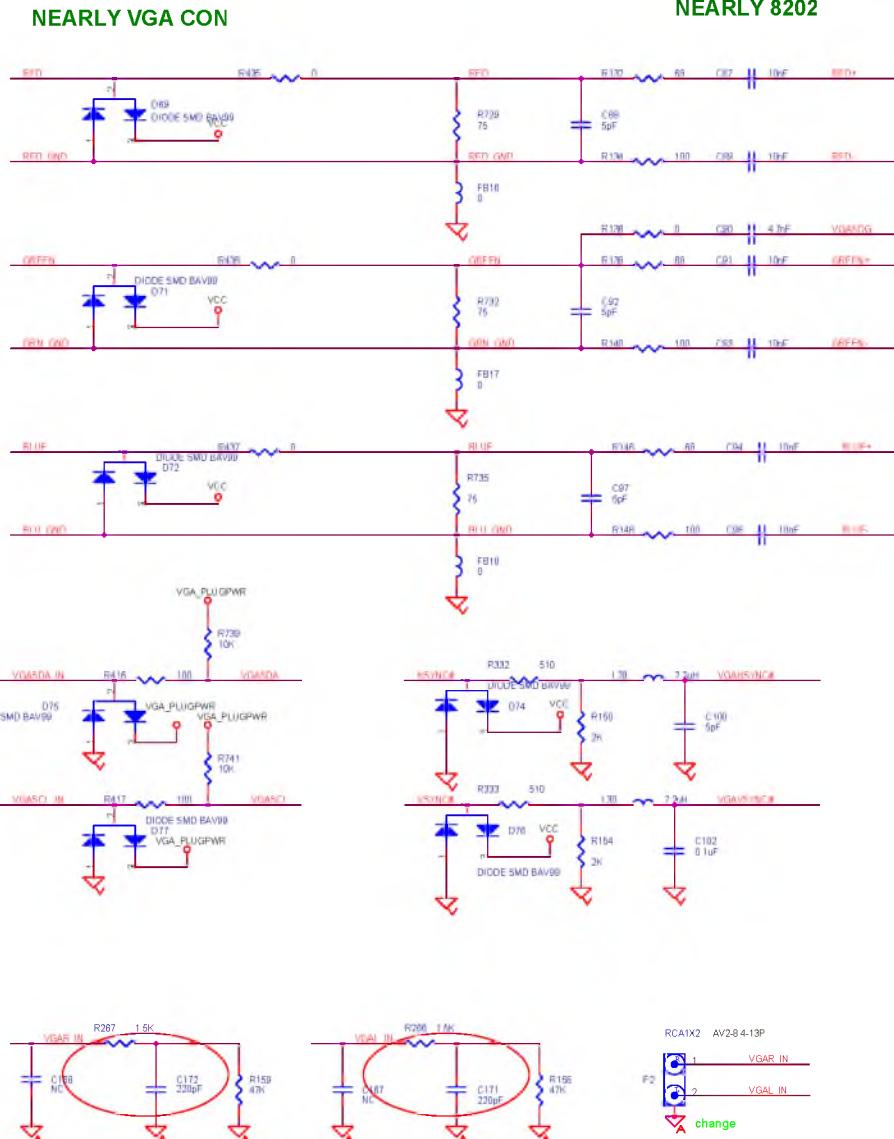
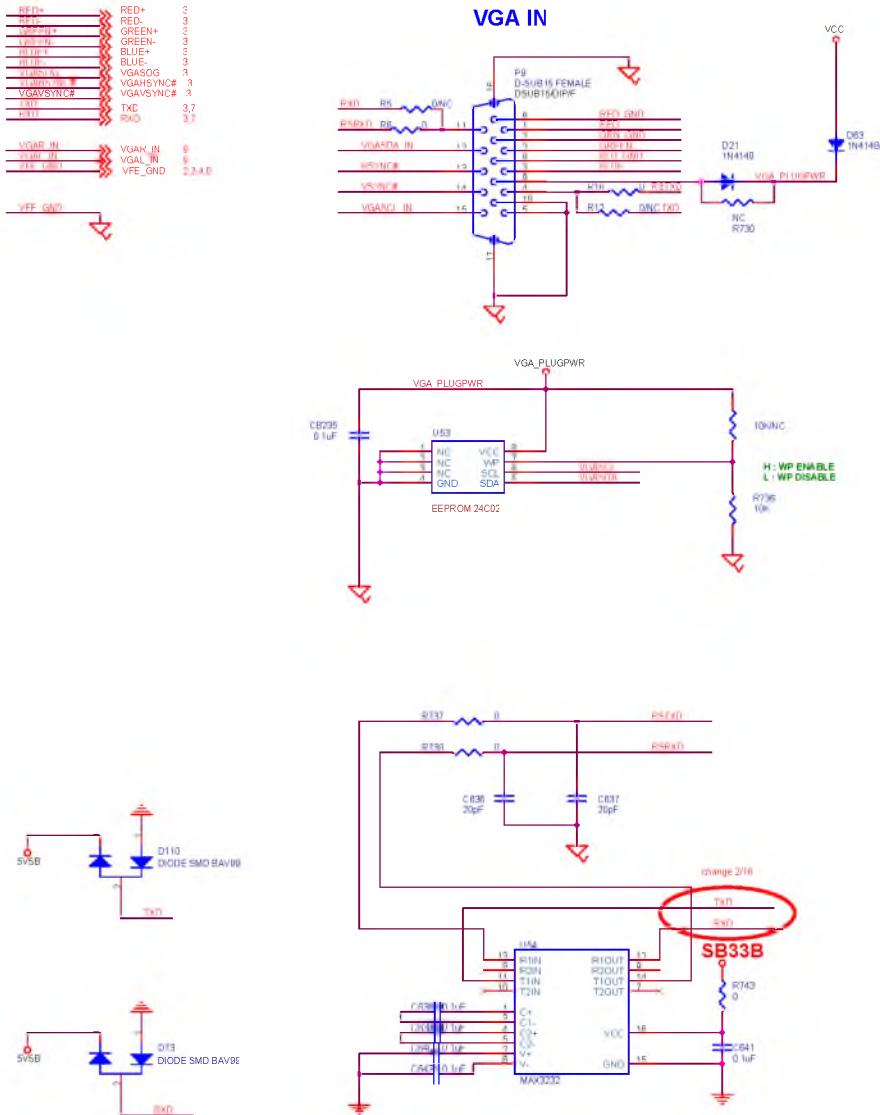
Y30 GND

Y31 GND

FROM Tuner



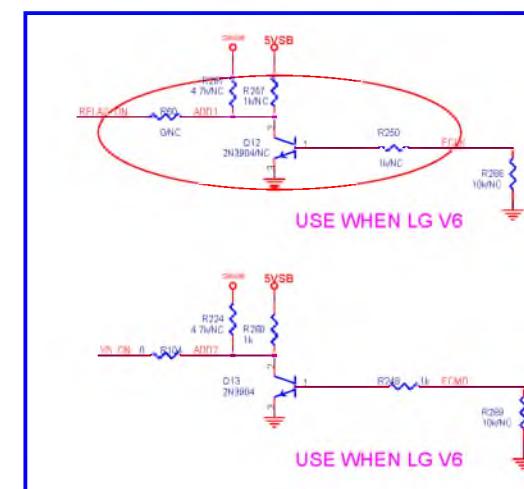
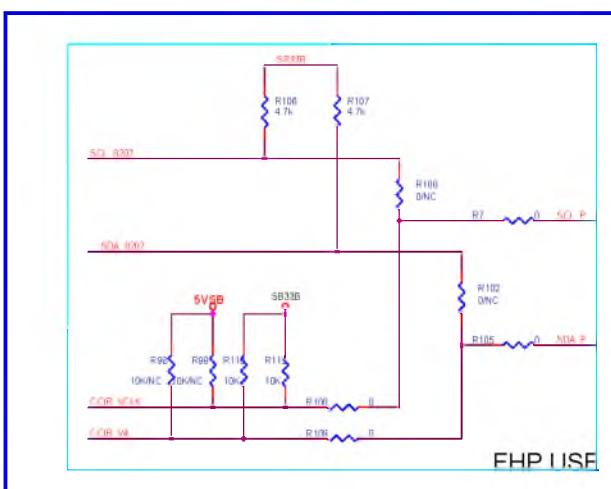
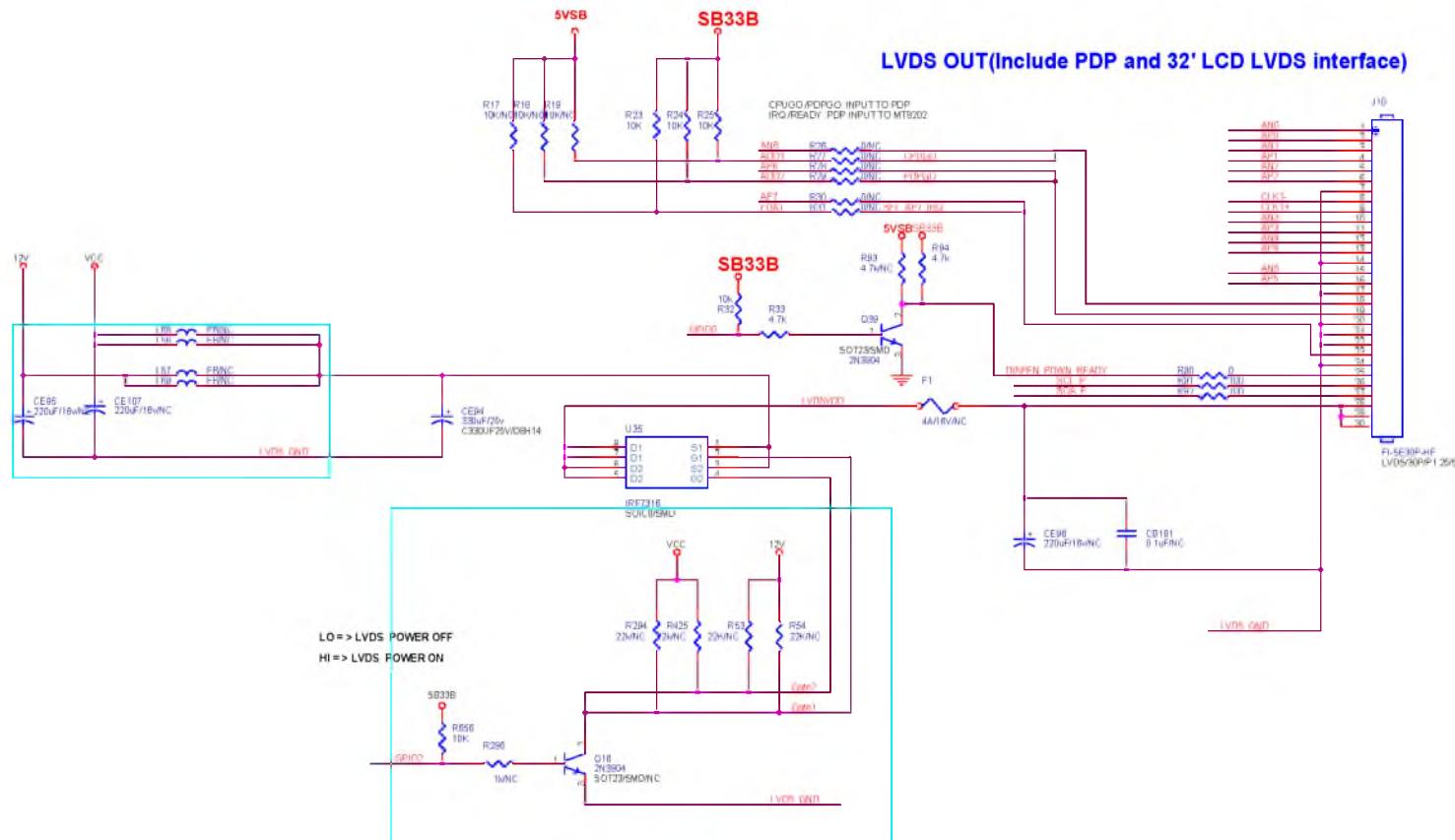
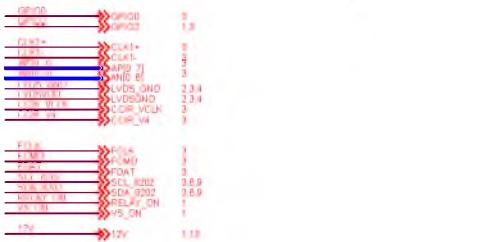
AUDIO / VIDEO IN CIRCUIT			
Size	Document Number	<Design>	REV
C	AKAI_MT8202_27US_LVDS_V0.0	<Checklist>	1
None	10/10/09 08:00:00	Change	10



VGA/DVI AUDIO INPUT

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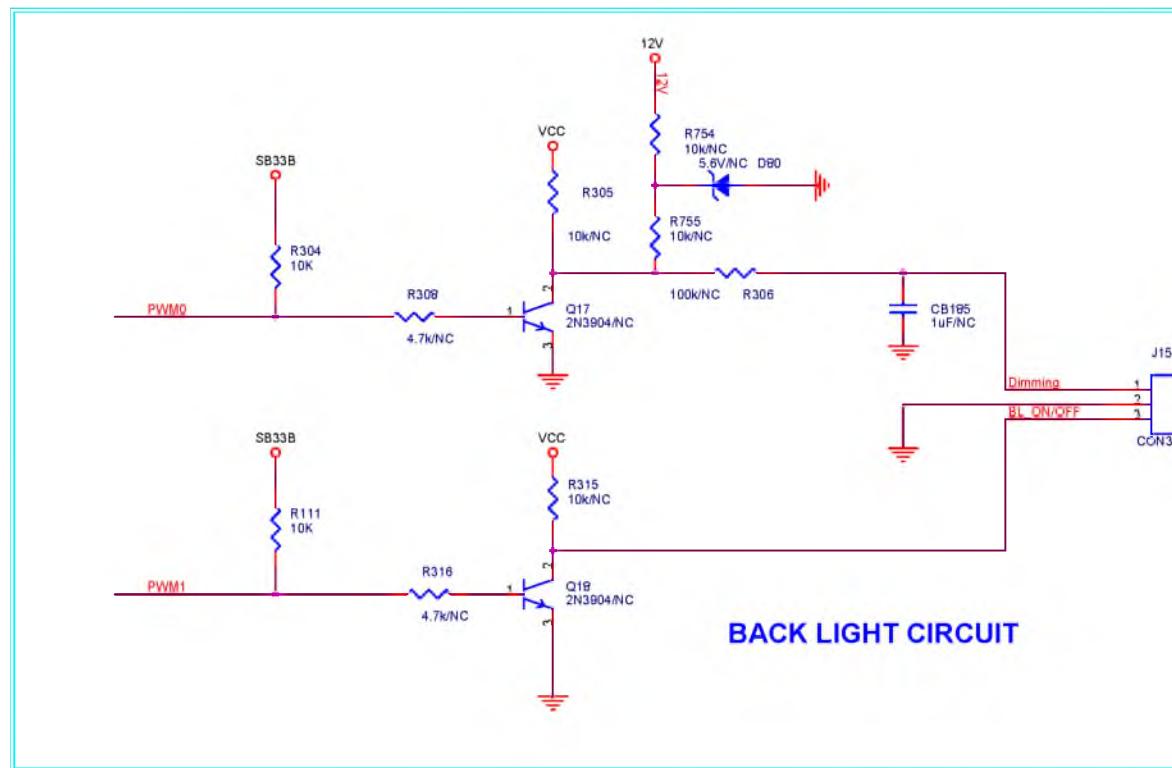
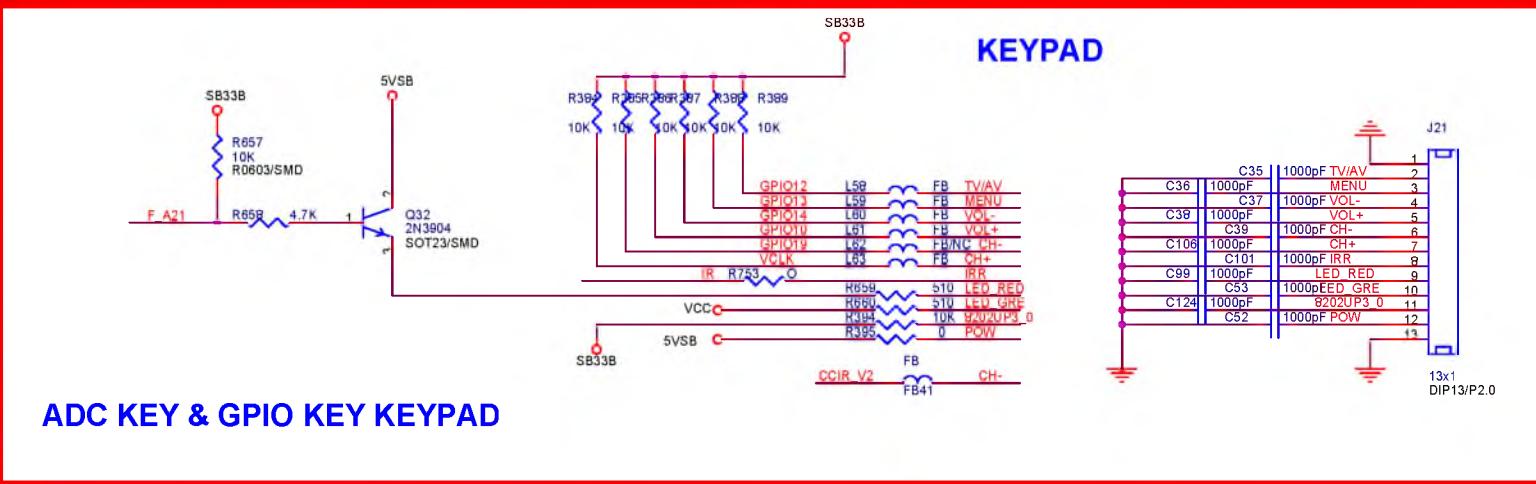
File	VGA IN & PC AUDIO IN		
Size	Document Number:	<Designer>	Rev
C	AKAI_MT5202_27US_LVDS_V0.0	Checked	1
Comments:		Page 1 of 1	



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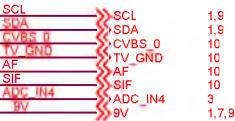
BR	LVDS OUT	4Designers	REV
Size	Document Number	Checked	Checked
C	RKA1_MT8202_27US_LVDS_V0.0	✓	✓
User	HIPPIE, ade, 1, 2006	Date	11

IR	→ IR	3,15
GPIO10	→ GPIO10	3
GPIO12	→ GPIO12	3
GPIO13	→ GPIO13	3
GPIO14	→ GPIO14	1,3
PWM0	→ PWM0	3
PWM1	→ PWM1	3
8202UP3_0	→ 8202UP3_0	3
GPIO14	→ GPIO14	1,3
GPIO19	→ GPIO19	1,3
VCLK	→ VCLK	3
F_A21	→ F_A21	3
CCIR_V2	→ CCIR_V2	3
12V	→ 12V	1,12

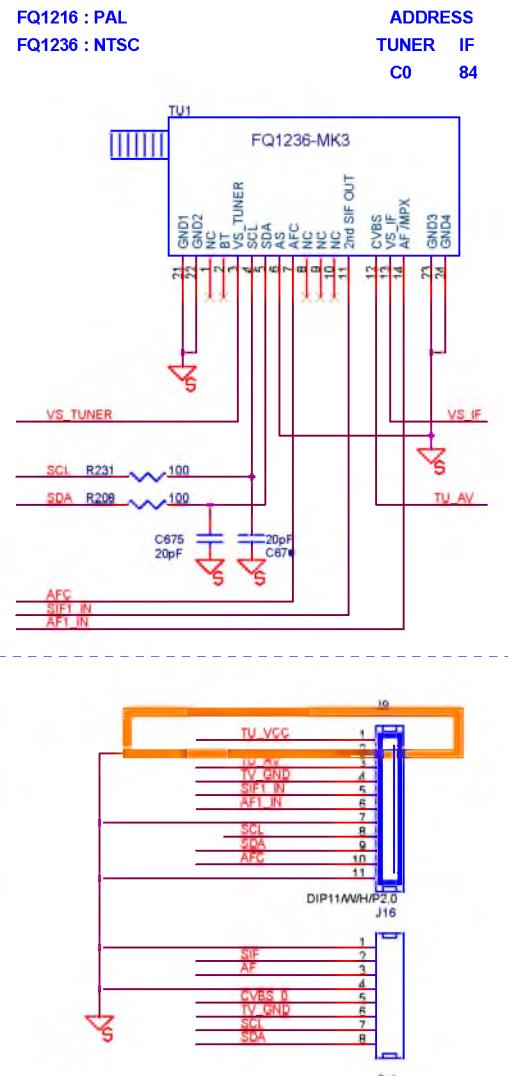
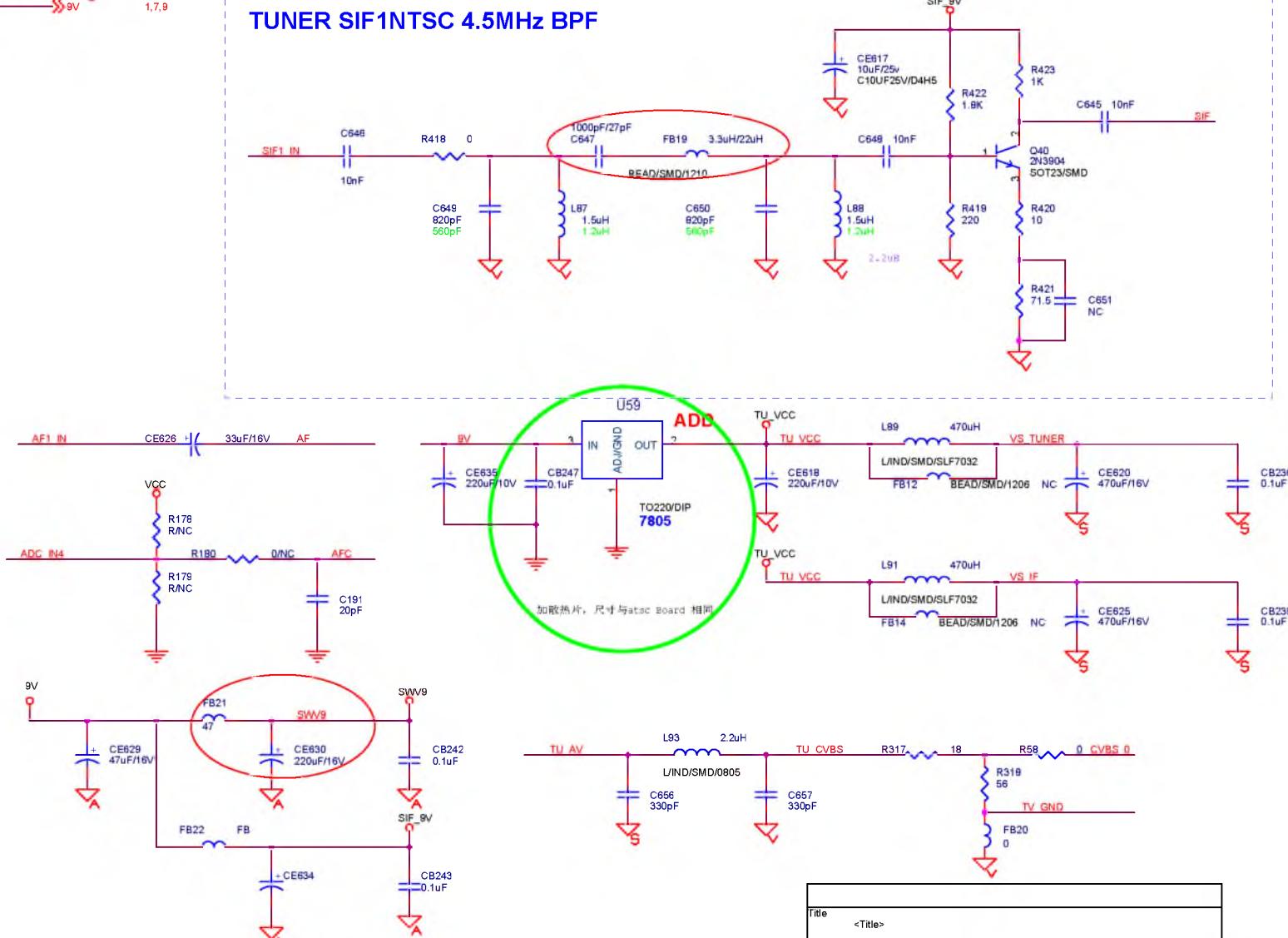


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Title		BACK LIGHT / KEYPAD	
Size	Document Number	<Designer>	Rev
B	AKAI_MT8202_27US_LVDS_V0.0	Checked <Checker>	1
Date:	Thursday, April 13, 2006	Sheet	13



TUNER SIF1NTSC 4.5MHz BPF

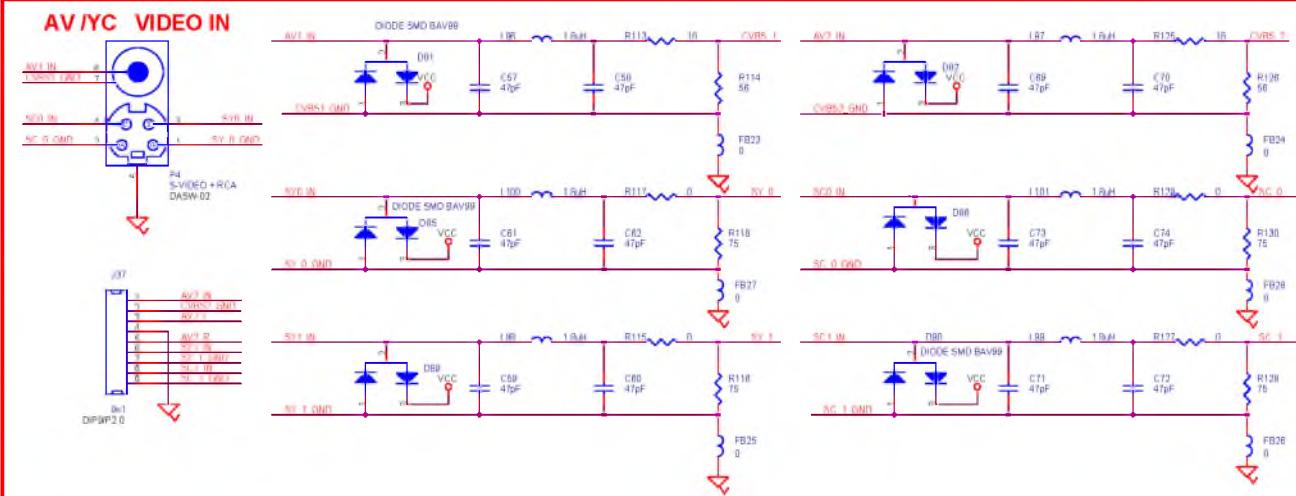


DIP8/P2.0
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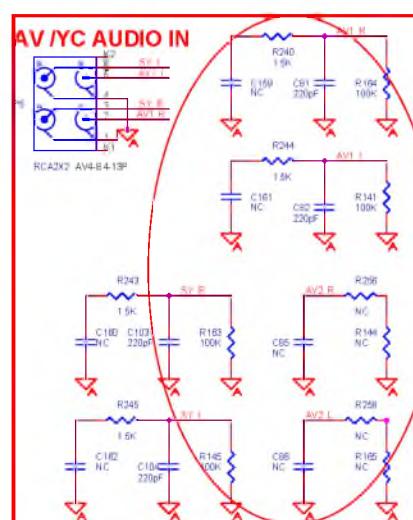
Title		<Title>		
Size Custom	Document Number Custom<Doc>			
Date:	Thursday, April 13, 2006	Sheet	1	of 1

Title		TUNER IN	
Size	Document Number	<Designer>	Rev
Custom	AKAL_MT8202_27US_LVDS_V0.0	Checked: <Checker>	1
Date:	Thursday, April 13, 2006	Sheet 14	17

AV/YC VIDEO IN

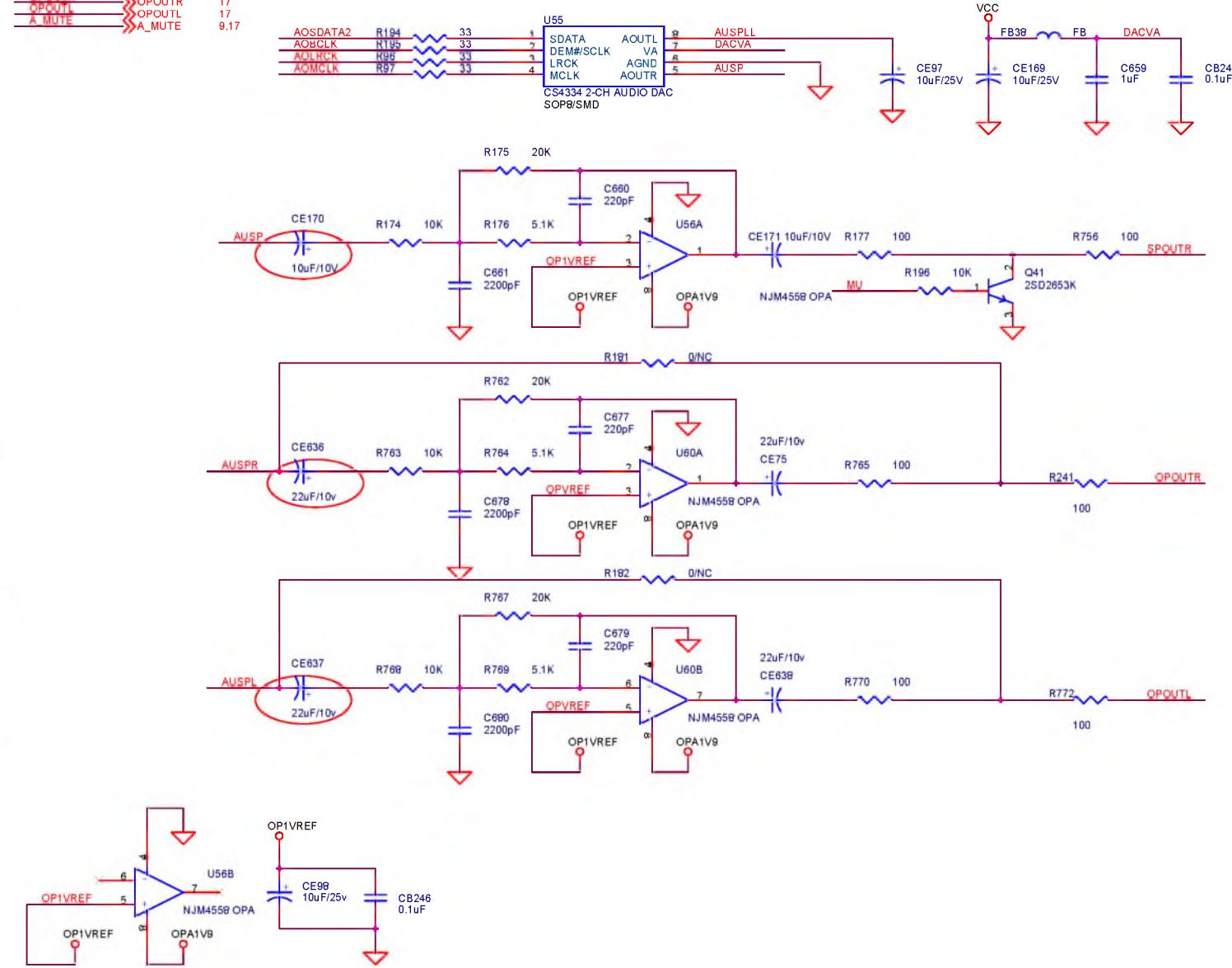


AV/YC AUDIO IN



Pin	Name	Function
1	AV1 OUT	AV1 OUT
2	AV2 OUT	AV2 OUT
3	AV3 OUT	AV3 OUT
4	AV4 OUT	AV4 OUT
5	AV5 OUT	AV5 OUT
6	AV6 OUT	AV6 OUT
7	AV7 OUT	AV7 OUT
8	AV8 OUT	AV8 OUT
9	AV9 OUT	AV9 OUT
10	AV10 OUT	AV10 OUT
11	AV11 OUT	AV11 OUT
12	AV12 OUT	AV12 OUT
13	AV13 OUT	AV13 OUT
14	AV14 OUT	AV14 OUT
15	AV15 OUT	AV15 OUT
16	AV16 OUT	AV16 OUT
17	AV17 OUT	AV17 OUT
18	AV18 OUT	AV18 OUT
19	AV19 OUT	AV19 OUT
20	AV20 OUT	AV20 OUT
21	AV21 OUT	AV21 OUT
22	AV22 OUT	AV22 OUT
23	AV23 OUT	AV23 OUT
24	AV24 OUT	AV24 OUT
25	AV25 OUT	AV25 OUT
26	AV26 OUT	AV26 OUT
27	AV27 OUT	AV27 OUT
28	AV28 OUT	AV28 OUT
29	AV29 OUT	AV29 OUT
30	AV30 OUT	AV30 OUT
31	AV31 OUT	AV31 OUT
32	AV32 OUT	AV32 OUT
33	AV33 OUT	AV33 OUT
34	AV34 OUT	AV34 OUT
35	AV35 OUT	AV35 OUT
36	AV36 OUT	AV36 OUT
37	AV37 OUT	AV37 OUT
38	AV38 OUT	AV38 OUT
39	AV39 OUT	AV39 OUT
40	AV40 OUT	AV40 OUT
41	AV41 OUT	AV41 OUT
42	AV42 OUT	AV42 OUT
43	AV43 OUT	AV43 OUT
44	AV44 OUT	AV44 OUT
45	AV45 OUT	AV45 OUT
46	AV46 OUT	AV46 OUT
47	AV47 OUT	AV47 OUT
48	AV48 OUT	AV48 OUT
49	AV49 OUT	AV49 OUT
50	AV50 OUT	AV50 OUT
51	AV51 OUT	AV51 OUT
52	AV52 OUT	AV52 OUT
53	AV53 OUT	AV53 OUT
54	AV54 OUT	AV54 OUT
55	AV55 OUT	AV55 OUT
56	AV56 OUT	AV56 OUT
57	AV57 OUT	AV57 OUT
58	AV58 OUT	AV58 OUT
59	AV59 OUT	AV59 OUT
60	AV60 OUT	AV60 OUT
61	AV61 OUT	AV61 OUT
62	AV62 OUT	AV62 OUT
63	AV63 OUT	AV63 OUT
64	AV64 OUT	AV64 OUT
65	AV65 OUT	AV65 OUT
66	AV66 OUT	AV66 OUT
67	AV67 OUT	AV67 OUT
68	AV68 OUT	AV68 OUT
69	AV69 OUT	AV69 OUT
70	AV70 OUT	AV70 OUT
71	AV71 OUT	AV71 OUT
72	AV72 OUT	AV72 OUT
73	AV73 OUT	AV73 OUT
74	AV74 OUT	AV74 OUT
75	AV75 OUT	AV75 OUT
76	AV76 OUT	AV76 OUT
77	AV77 OUT	AV77 OUT
78	AV78 OUT	AV78 OUT
79	AV79 OUT	AV79 OUT
80	AV80 OUT	AV80 OUT
81	AV81 OUT	AV81 OUT
82	AV82 OUT	AV82 OUT
83	AV83 OUT	AV83 OUT
84	AV84 OUT	AV84 OUT
85	AV85 OUT	AV85 OUT
86	AV86 OUT	AV86 OUT
87	AV87 OUT	AV87 OUT
88	AV88 OUT	AV88 OUT
89	AV89 OUT	AV89 OUT
90	AV90 OUT	AV90 OUT
91	AV91 OUT	AV91 OUT
92	AV92 OUT	AV92 OUT
93	AV93 OUT	AV93 OUT
94	AV94 OUT	AV94 OUT
95	AV95 OUT	AV95 OUT
96	AV96 OUT	AV96 OUT
97	AV97 OUT	AV97 OUT
98	AV98 OUT	AV98 OUT
99	AV99 OUT	AV99 OUT
100	AV100 OUT	AV100 OUT
101	AV101 OUT	AV101 OUT
102	AV102 OUT	AV102 OUT
103	AV103 OUT	AV103 OUT
104	AV104 OUT	AV104 OUT
105	AV105 OUT	AV105 OUT
106	AV106 OUT	AV106 OUT
107	AV107 OUT	AV107 OUT
108	AV108 OUT	AV108 OUT
109	AV109 OUT	AV109 OUT
110	AV110 OUT	AV110 OUT
111	AV111 OUT	AV111 OUT
112	AV112 OUT	AV112 OUT
113	AV113 OUT	AV113 OUT
114	AV114 OUT	AV114 OUT
115	AV115 OUT	AV115 OUT
116	AV116 OUT	AV116 OUT
117	AV117 OUT	AV117 OUT
118	AV118 OUT	AV118 OUT
119	AV119 OUT	AV119 OUT
120	AV120 OUT	AV120 OUT
121	AV121 OUT	AV121 OUT
122	AV122 OUT	AV122 OUT
123	AV123 OUT	AV123 OUT
124	AV124 OUT	AV124 OUT
125	AV125 OUT	AV125 OUT
126	AV126 OUT	AV126 OUT
127	AV127 OUT	AV127 OUT
128	AV128 OUT	AV128 OUT
129	AV129 OUT	AV129 OUT
130	AV130 OUT	AV130 OUT
131	AV131 OUT	AV131 OUT
132	AV132 OUT	AV132 OUT
133	AV133 OUT	AV133 OUT
134	AV134 OUT	AV134 OUT
135	AV135 OUT	AV135 OUT
136	AV136 OUT	AV136 OUT
137	AV137 OUT	AV137 OUT
138	AV138 OUT	AV138 OUT
139	AV139 OUT	AV139 OUT
140	AV140 OUT	AV140 OUT
141	AV141 OUT	AV141 OUT
142	AV142 OUT	AV142 OUT
143	AV143 OUT	AV143 OUT
144	AV144 OUT	AV144 OUT
145	AV145 OUT	AV145 OUT
146	AV146 OUT	AV146 OUT
147	AV147 OUT	AV147 OUT
148	AV148 OUT	AV148 OUT
149	AV149 OUT	AV149 OUT
150	AV150 OUT	AV150 OUT
151	AV151 OUT	AV151 OUT
152	AV152 OUT	AV152 OUT
153	AV153 OUT	AV153 OUT
154	AV154 OUT	AV154 OUT
155	AV155 OUT	AV155 OUT
156	AV156 OUT	AV156 OUT
157	AV157 OUT	AV157 OUT
158	AV158 OUT	AV158 OUT
159	AV159 OUT	AV159 OUT
160	AV160 OUT	AV160 OUT
161	AV161 OUT	AV161 OUT
162	AV162 OUT	AV162 OUT
163	AV163 OUT	AV163 OUT
164	AV164 OUT	AV164 OUT
165	AV165 OUT	AV165 OUT
166	AV166 OUT	AV166 OUT
167	AV167 OUT	AV167 OUT
168	AV168 OUT	AV168 OUT
169	AV169 OUT	AV169 OUT
170	AV170 OUT	AV170 OUT
171	AV171 OUT	AV171 OUT
172	AV172 OUT	AV172 OUT
173	AV173 OUT	AV173 OUT
174	AV174 OUT	AV174 OUT
175	AV175 OUT	AV175 OUT
176	AV176 OUT	AV176 OUT
177	AV177 OUT	AV177 OUT
178	AV178 OUT	AV178 OUT
179	AV179 OUT	AV179 OUT
180	AV180 OUT	AV180 OUT
181	AV181 OUT	AV181 OUT
182	AV182 OUT	AV182 OUT
183	AV183 OUT	AV183 OUT
184	AV184 OUT	AV184 OUT
185	AV185 OUT	AV185 OUT
186	AV186 OUT	AV186 OUT
187	AV187 OUT	AV187 OUT
188	AV188 OUT	AV188 OUT
189	AV189 OUT	AV189 OUT
190	AV190 OUT	AV190 OUT
191	AV191 OUT	AV191 OUT
192	AV192 OUT	AV192 OUT
193	AV193 OUT	AV193 OUT
194	AV194 OUT	AV194 OUT
195	AV195 OUT	AV195 OUT
196	AV196 OUT	AV196 OUT
197	AV197 OUT	AV197 OUT
198	AV198 OUT	AV198 OUT
199	AV199 OUT	AV199 OUT
200	AV200 OUT	AV200 OUT
201	AV201 OUT	AV201 OUT
202	AV202 OUT	AV202 OUT
203	AV203 OUT	AV203 OUT
204	AV204 OUT	AV204 OUT
205	AV205 OUT	AV205 OUT
206	AV206 OUT	AV206 OUT
207	AV207 OUT	AV207 OUT
208	AV208 OUT	AV208 OUT
209	AV209 OUT	AV209 OUT
210	AV210 OUT	AV210 OUT
211	AV211 OUT	AV211 OUT
212	AV212 OUT	AV212 OUT
213	AV213 OUT	AV213 OUT
214	AV214 OUT	AV214 OUT
215	AV215 OUT	AV215 OUT
216	AV216 OUT	AV216 OUT
217	AV217 OUT	AV217 OUT
218	AV218 OUT	AV218 OUT
219	AV219 OUT	AV219 OUT
220	AV220 OUT	AV220 OUT
221	AV221 OUT	AV221 OUT
222	AV222 OUT	AV222 OUT
223	AV223 OUT	AV223 OUT
224	AV224 OUT	AV224 OUT
225	AV225 OUT	AV225 OUT
226	AV226 OUT	AV226 OUT
227	AV227 OUT	AV227 OUT
228	AV228 OUT	AV228 OUT
229	AV229 OUT	AV229 OUT
230	AV230 OUT	AV230 OUT
231	AV231 OUT	AV231 OUT
232	AV232 OUT	AV232 OUT
233	AV233 OUT	AV233 OUT
234	AV234 OUT	AV234 OUT
235	AV235 OUT	AV235 OUT
236	AV236 OUT	AV236 OUT
237	AV237 OUT	AV237 OUT
238	AV238 OUT	AV238 OUT
239	AV239 OUT	AV239 OUT
240	AV240 OUT	AV240 OUT
241	AV241 OUT	AV241 OUT
242	AV242 OUT	AV242 OUT
243	AV243 OUT	AV243 OUT
244	AV244 OUT	AV244 OUT
245	AV245 OUT	AV245 OUT
246	AV246 OUT	AV246 OUT
247	AV247 OUT	AV247 OUT
248	AV248 OUT	AV248 OUT
249	AV249 OUT	AV249 OUT
250	AV250 OUT	AV250 OUT
251	AV251 OUT	AV251 OUT
252	AV252 OUT	AV252 OUT
253	AV253 OUT	AV253 OUT
254	AV254 OUT	AV254 OUT
255	AV255 OUT	AV255 OUT
256	AV256 OUT	AV256 OUT
257	AV257 OUT	AV257 OUT
258	AV258 OUT	AV258 OUT
259	AV259 OUT	AV259 OUT
260	AV260 OUT	AV260 OUT
261	AV261 OUT	AV261 OUT
262	AV262 OUT	AV262 OUT
263	AV263 OUT	AV263 OUT
264	AV264 OUT	AV264 OUT
265	AV265 OUT	AV265 OUT
266	AV266 OUT	AV266 OUT
267	AV267 OUT	AV267 OUT
268	AV268 OUT	AV268 OUT
269	AV269 OUT	AV269 OUT
270	AV270 OUT	AV270 OUT
271	AV271 OUT	AV271 OUT
272	AV272 OUT	AV272 OUT
273	AV273 OUT	AV273 OUT
274	AV274 OUT	AV274 OUT
275	AV275 OUT	AV275 OUT
276	AV276 OUT	AV276 OUT
277	AV277 OUT	AV277 OUT
278	AV278 OUT	AV278 OUT
279	AV279 OUT	AV279 OUT
280	AV280 OUT	AV280 OUT
281	AV281 OUT	AV281 OUT
282	AV282 OUT	AV282 OUT
283	AV283 OUT	AV283 OUT
284	AV284 OUT	AV284 OUT
285	AV285 OUT	AV285 OUT
286	AV286 OUT	AV286 OUT
287	AV287 OUT	AV287 OUT
288	AV288 OUT	AV288 OUT
289	AV289 OUT	AV289 OUT
290	AV290 OUT	AV290 OUT
291	AV291 OUT	AV291 OUT
292	AV292 OUT	AV292 OUT
293	AV293 OUT	AV293 OUT
294	AV294 OUT	AV294 OUT
295	AV295 OUT	AV295 OUT
296	AV296 OUT	AV296 OUT
297	AV297 OUT	AV297 OUT
298	AV298 OUT	AV298 OUT
299	AV299 OUT	AV299 OUT
300	AV300 OUT	AV300 OUT
301	AV301 OUT	AV301 OUT
302	AV302 OUT	AV302 OUT
303	AV303 OUT	AV303 OUT
304	AV304 OUT	AV304 OUT
305	AV305 OUT	AV305 OUT
306	AV306 OUT	AV306 OUT
307	AV307 OUT	AV307 OUT
308	AV308 OUT	AV308 OUT
309	AV309 OUT	AV309 OUT
310	AV310 OUT	AV310 OUT
311	AV311 OUT	AV311 OUT
312	AV312 OUT	AV312 OUT
313	AV313 OUT	AV313 OUT
314	AV314 OUT	AV314 OUT
315	AV315 OUT	AV315 OUT
316	AV316 OUT	AV316 OUT
317	AV317 OUT	AV317 OUT
318	AV318 OUT	AV318 OUT
319	AV319 OUT	AV319 OUT
320	AV320 OUT	AV320 OUT
321	AV321 OUT	AV321 OUT
322	AV322 OUT	AV322 OUT
323	AV323 OUT	AV323 OUT
324	AV324 OUT	AV324 OUT
325	AV325 OUT	AV325 OUT
326	AV326 OUT	AV326 OUT
327	AV327 OUT	AV327 OUT
328	AV328 OUT	AV328 OUT
329	AV329 OUT	AV329 OUT
330	AV330 OUT	AV330 OUT
331	AV331 OUT	AV331 OUT
332	AV332 OUT	AV332 OUT
333	AV333 OUT	AV333 OUT
334	AV334 OUT	AV334 OUT
335	AV335 OUT	AV335 OUT
336	AV336 OUT	AV33

AOSDATA2	AOSDATA2	3.8
AOBCLK	AOBCLK	3.9
AOLCK	AOLCK	3.9
MU	MU	9
SPOUTR	SPOUTR	15
AUSPR	AUSPR	8
AUSPL	AUSPL	9
OPOUTR	OPOUTR	17
OPOUTL	OPOUTL	17
A MUTE	A MUTE	9.17

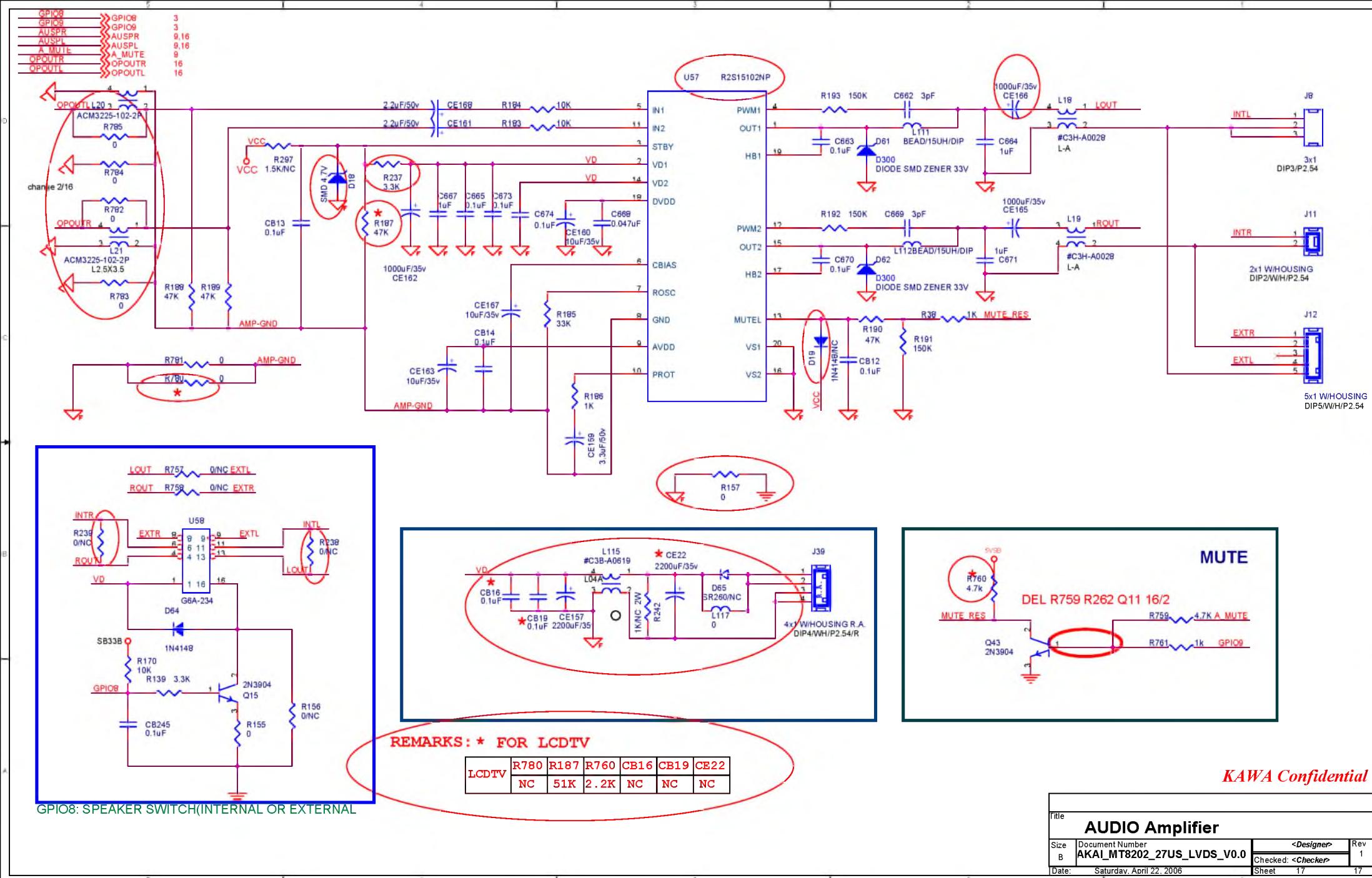


GPIO DESCRIPTION

- UP3_4 : SW SCL
- UP3_5 : SW SDA
- ERO0/UP3_0 : KEYPAD POWER
- ERO1/UP3_1 : MAIN POWER SWITCH
- VCLK : KEPAD CH+
- GPIO19 : KEPAD CH-
- DE/GPIO : DVD IR
- CCIR_CLK : PDP USE
- CCIR_V4 : PDP USE
- GPIO0 : PDP USE
- GPIO1 : NO USE
- GPIO2 : LVDS POWER SW
- GPIO3 : DTV POWER CONTROL
- GPIO4 : EEPROM WRITE PROTECT
- GPIO5/TXD : 2nd UART FOR MT5351
- GPIO6/RXD : 2nd UART FOR MT5351
- GPIO7 : AUDIO BYPASS MUTE CONTROL
- GPIO8 : SPEAKER SWITCH
- GPIO9 : AUDIO MUTE
- GPIO10 : Indicates active video at HDMI port
- GPIO11 : DVD POWER CONTROL
- GPIO12 : AV SWITCH
- GPIO13 : HDMI Hot Plug Detect
- GPIO14 : NO USE**
- GPIO[15..18] : FOR DVD CONTROL
- GPIO/PWM0 : DIMMING
- GPIO/PWM1 : BACKLIGHT ON/OFF
- OUT_27Mhz/GPIO : HDMI CRYSTAL
- SDA1 : TO MT5351 I/F REQUEST
- SCL1 : TO MT5351 I/F READY
- F_A21 : KEYPAD(LED RED)
- ADCIN0 : KEYPAD
- ADCIN3:PDP 5VD DETECT
- ADCIN4:FOR TUNER AFC
- CCIR_V[0-3] : KEYPAD
- CCIR_V5 : AUDIO SWITCH
- CCIR_V6 : RESET DTV
- CCIR_V7 : YPBPR VIDEO SWITCH

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Title		SUB WOOFER	
Size	Document Number	<Designer>	Rev
B	AKAI_MT8202_27US_LVDS_V0.0	Checked: <Checker>	1
Date:	Thursday, April 13, 2006	Sheet	16 / 17



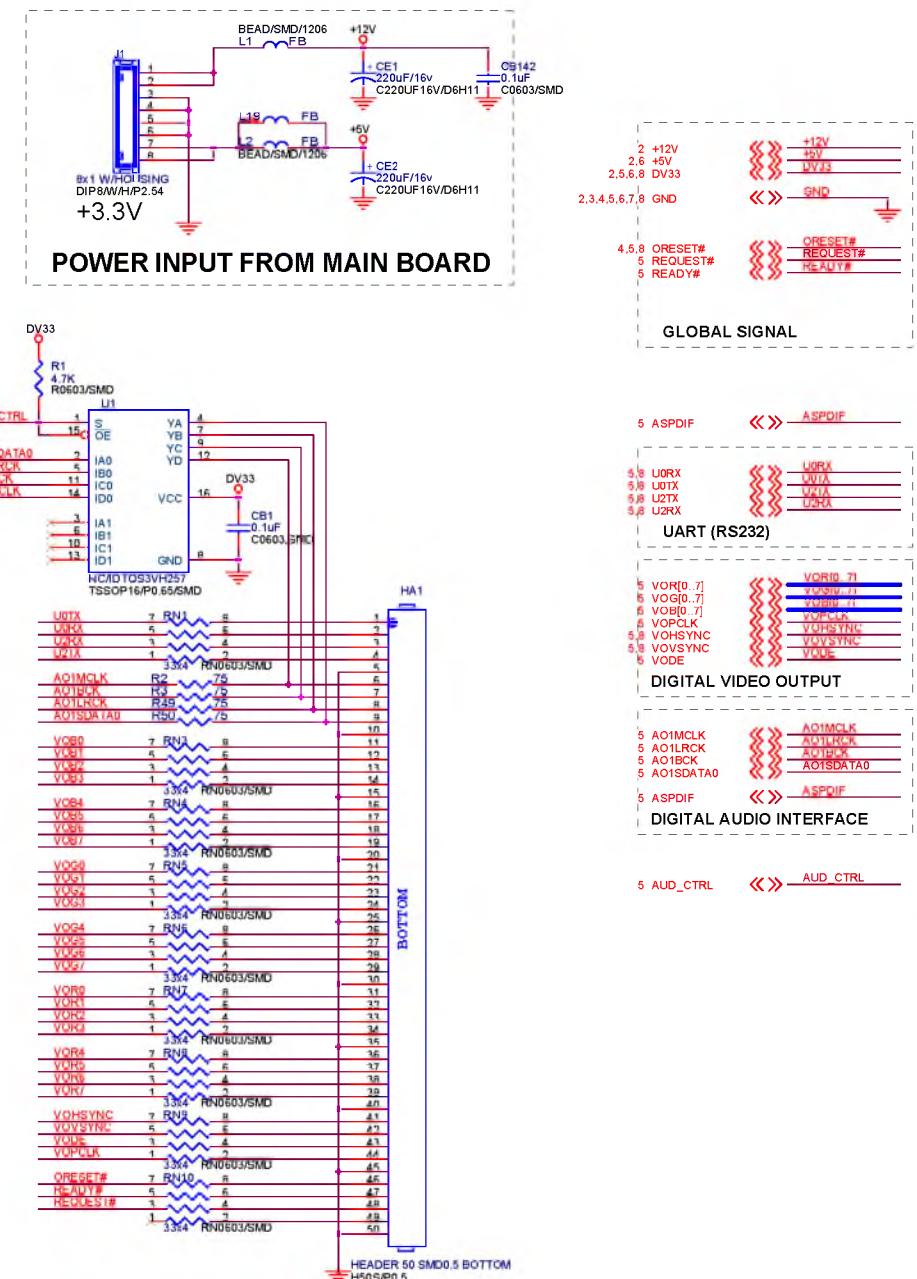
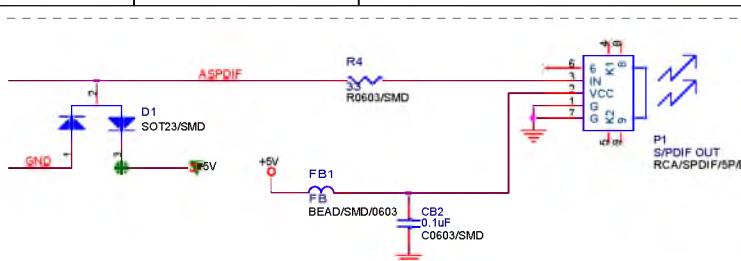
MT5111 / MT5351 REFERENCE DESIGN - 4 LAYERS

Rev	History	P#	DATE
RA-V1	INITIAL VERSION		2005/06/15
RA-V2	ADDED AUDIO SWITCH / REFINED POWER CIRCUIT		2005/07/14

- 01. INDEX AND INTERFACE
- 02. POWER
- 03. TUNER
- 04. MT5111 ASIC
- 05. MT5351 ASIC
- 06. MT5351 PERIPHERAL
- 07. DDR MEMORY
- 08. NOR FLASH / JTAG / UART

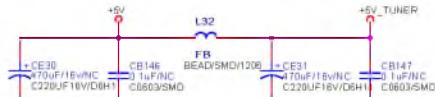
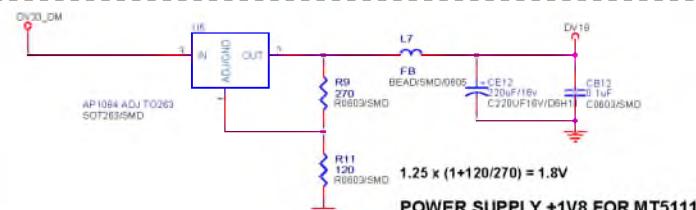
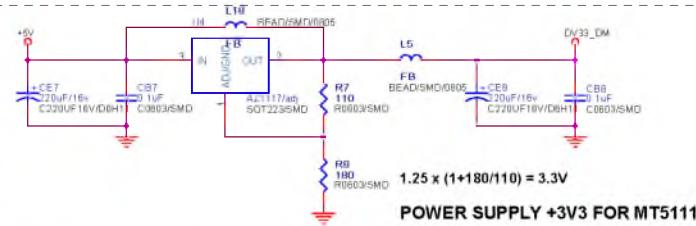
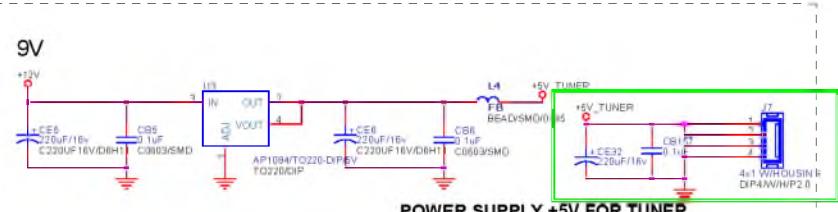
NS : NON-STUFF

NAME	TYPE	DEVICE
+12V	POWER +12V	POWER SUPPLY
+5V	POWER +5V	POWER SUPPLY
+5V_tuner	POWER +5V	TUNER POWER
DV33_DM	POWER +3V3	MT5111 POWER
DV18	POWER +1V8	MT5111 POWER
DV33	POWER +3V3	MT5351 POWER
AV33	POWER +3V3	MT5351 ANALOG POWER
DV25	POWER +2V5	MT5351 DDR POWER
DV12	POWER +1V2	MT5351 POWER
GND	GROUND	GROUND

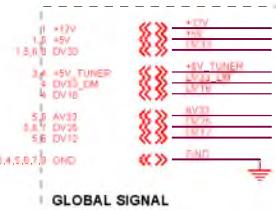
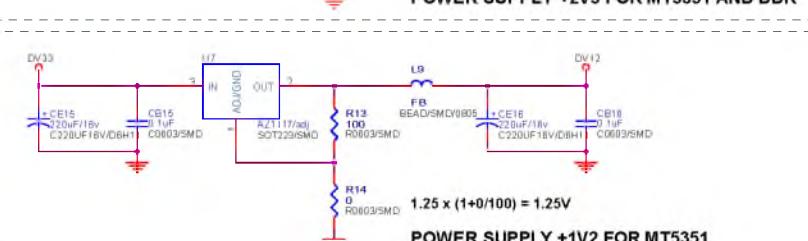
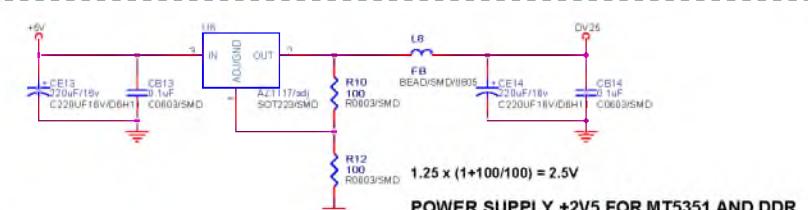
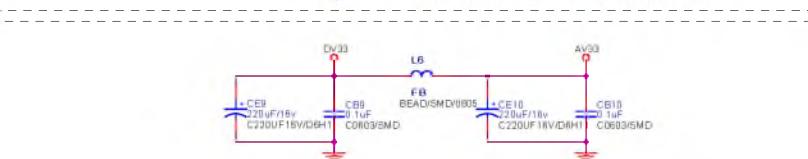
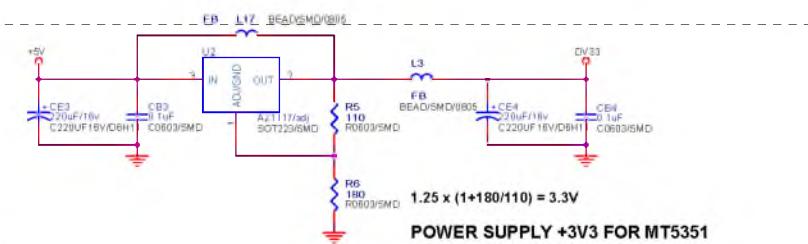
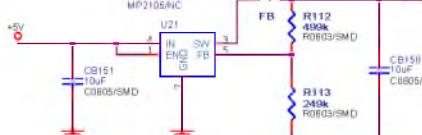


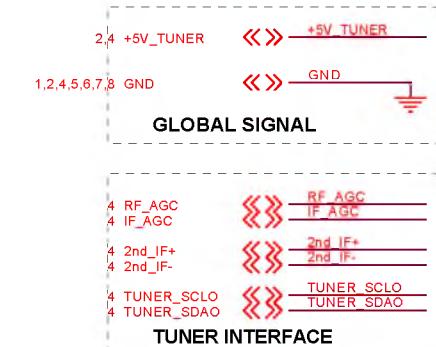
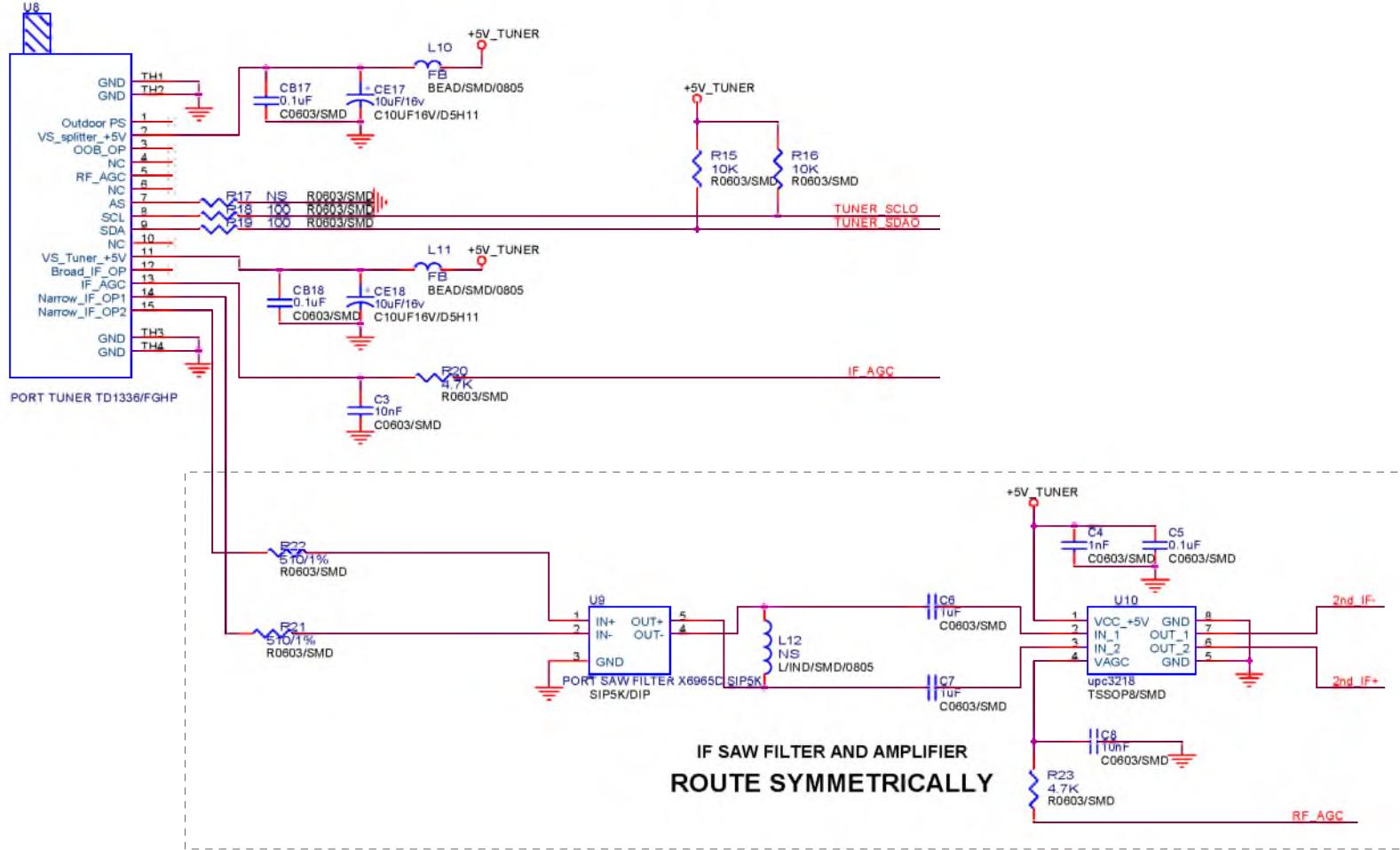
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INDEX		
Size	Document Number	Rev
Cuspm	MT5351RA-V2	1
Date:	Monday, February 20, 2006	Sheet 1 of 8



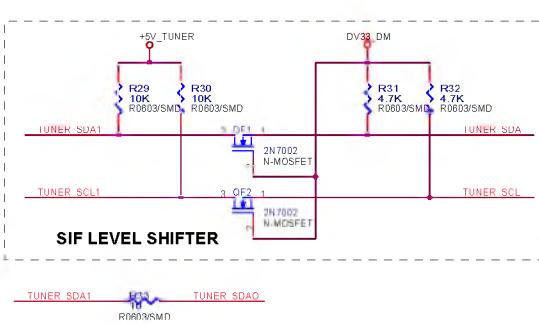
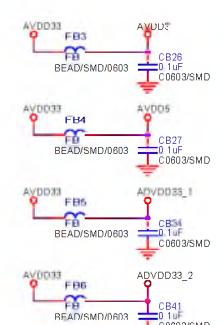
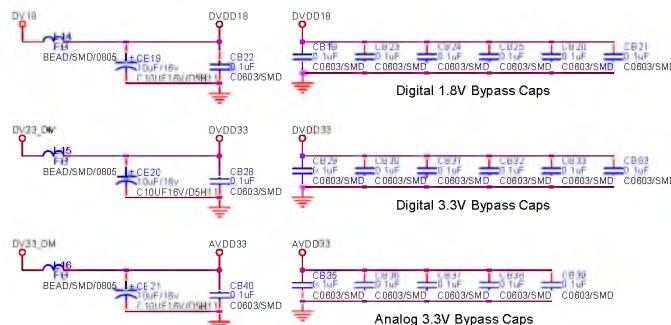
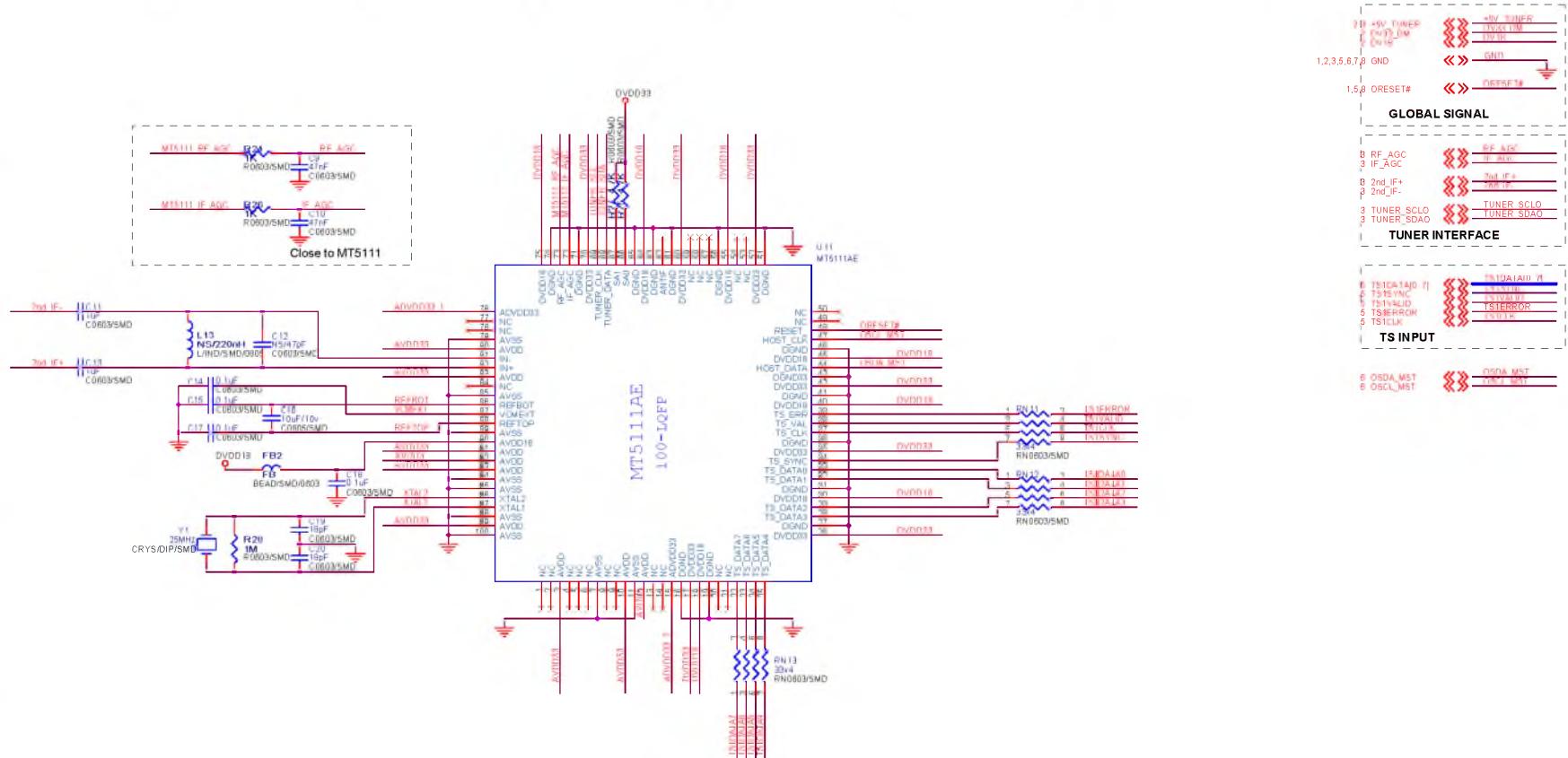
Compatible With U6





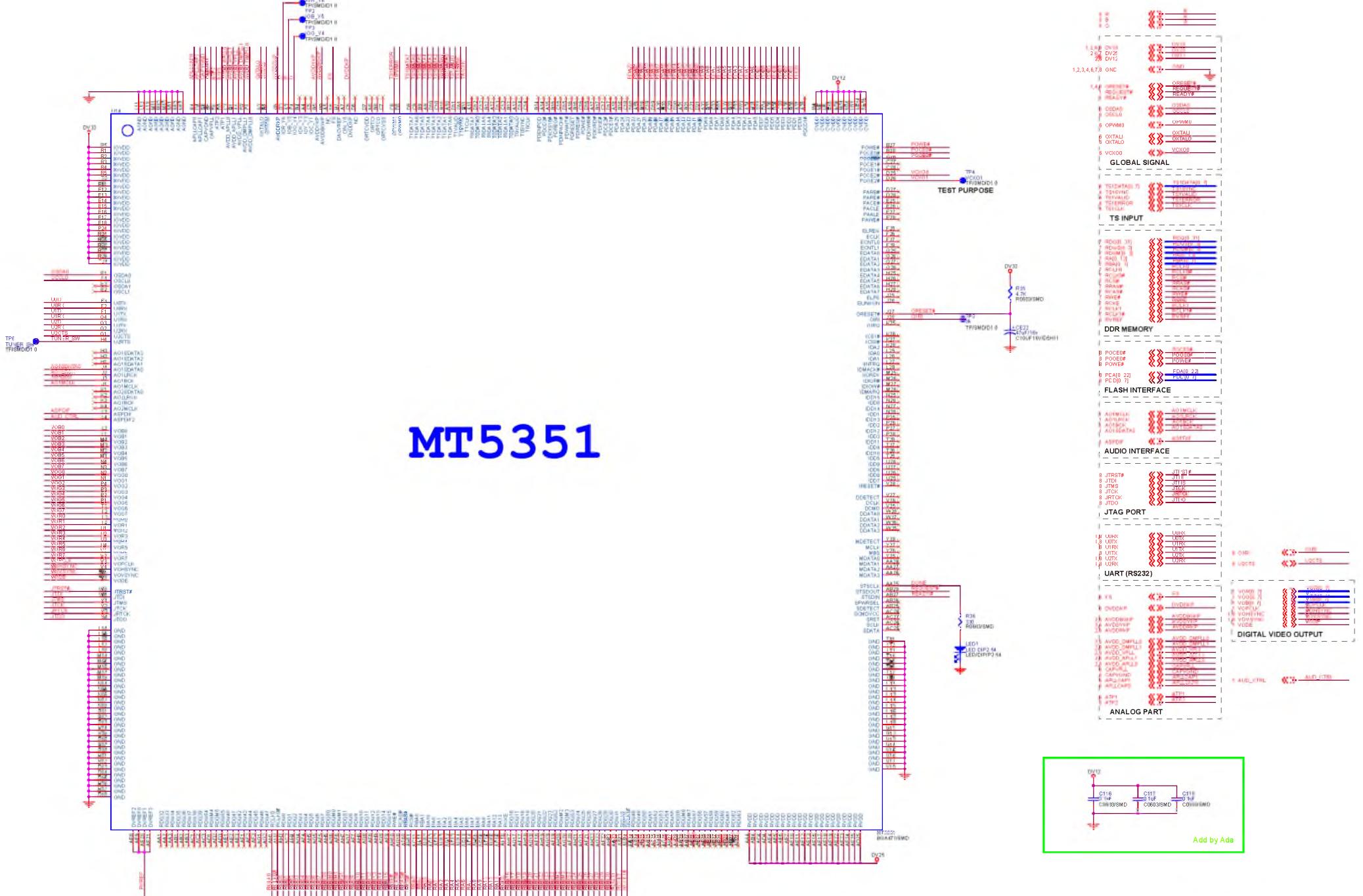
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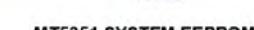
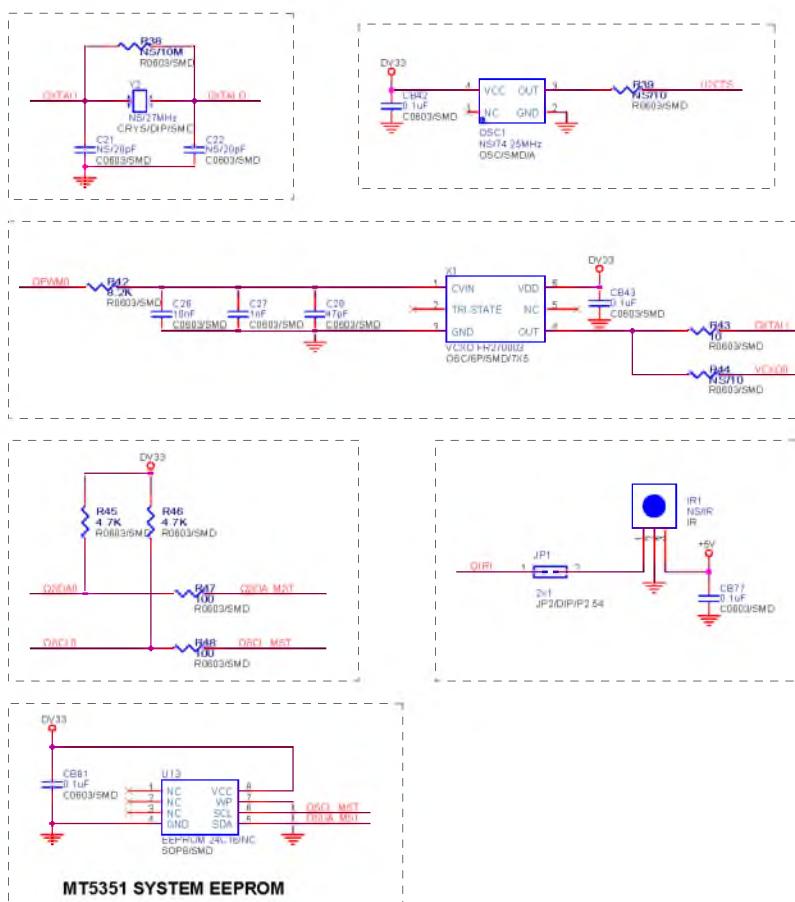
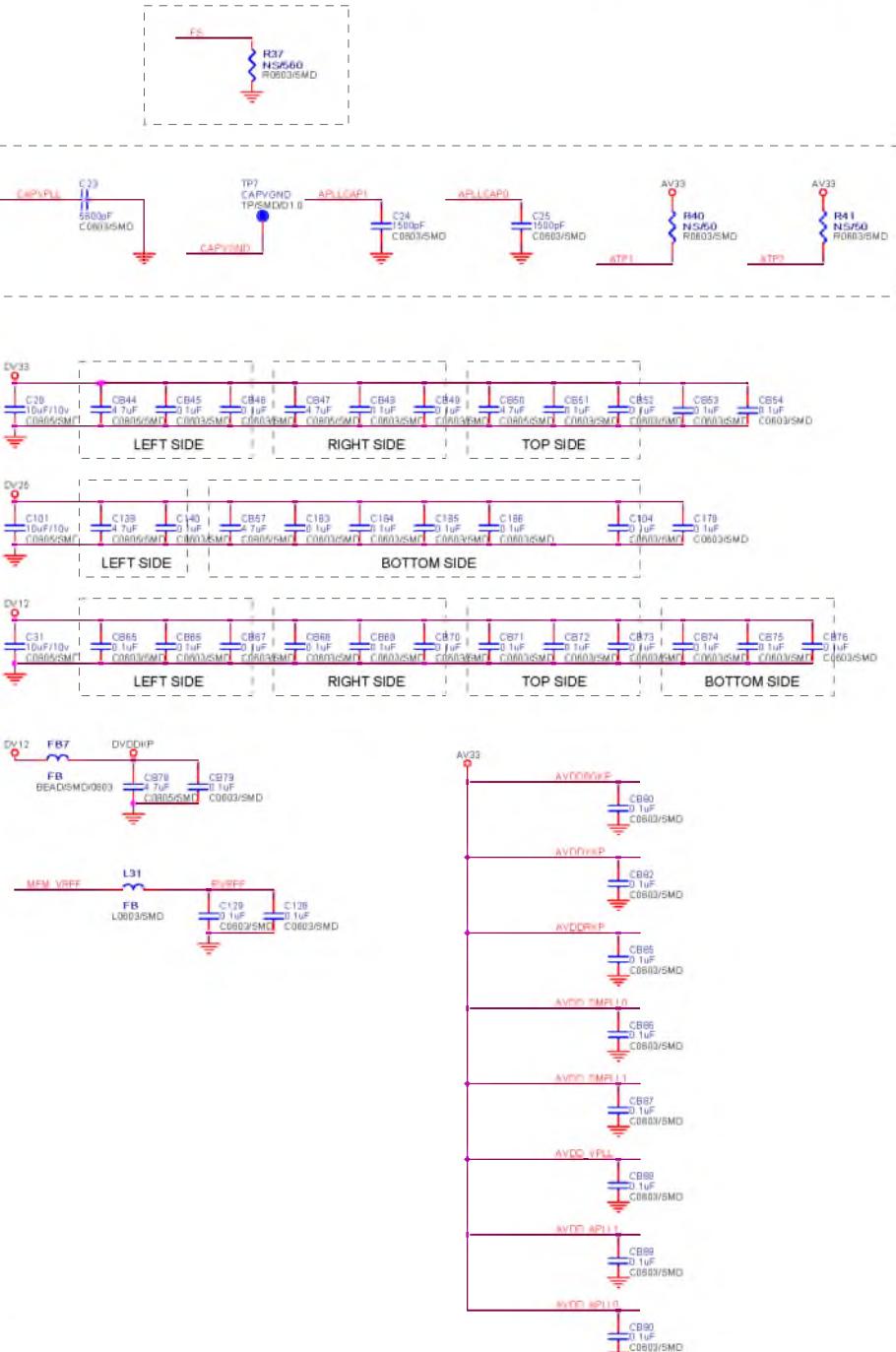
	Title	
	TUNER	
Size	Document Number	Rev
Custom	MT5351RA-V2	1
Date:	Monday, February 20, 2006	Sheet 3 of 8



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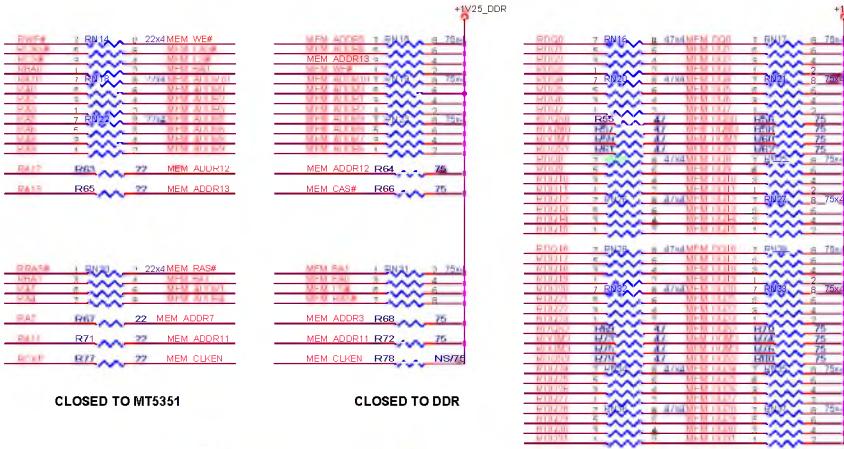
Title		MT5111 ASIC
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C	Date	Monday, February 20, 2006





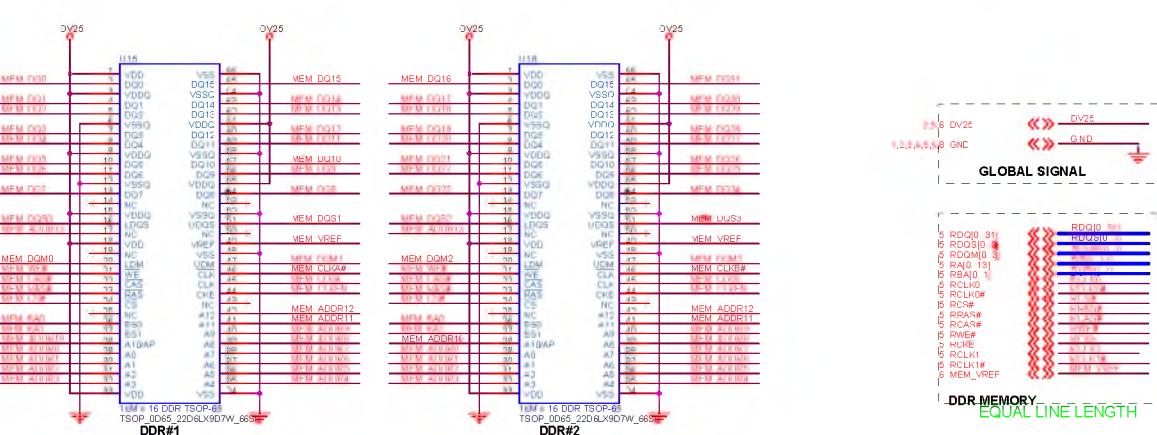
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MT5351 PERIPHERAL			
Size	Document Number	TwinSon Chan	
Custom	MT5351RA-V2	REV 1	
Date	March 1, February 20, 2008	Sheet	8 / 91 / 9



CLOSED TO MT535

CLOSED TO DDI



GLOBAL SIGNAL

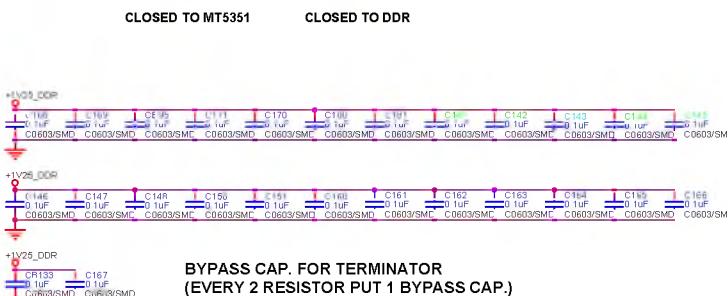
	RDQ0	RDQ1	RDQ2	RDQ3	RDQ4	RDQ5
1 RDQ0#	RDQ0#					
2 RDQ0\$#	RDQ0\$#					
3 RDQ0@	RDQ0@					
4 RDQ1#	RDQ1#					
5 RDQ1\$#	RDQ1\$#					
6 RDQ1@	RDQ1@					
7 RDQ2#	RDQ2#					
8 RDQ2\$#	RDQ2\$#					
9 RDQ2@	RDQ2@					
10 RDQ3#	RDQ3#					
11 RDQ3\$#	RDQ3\$#					
12 RDQ3@	RDQ3@					
13 RDQ4#	RDQ4#					
14 RDQ4\$#	RDQ4\$#					
15 RDQ4@	RDQ4@					
16 RDQ5#	RDQ5#					
17 RDQ5\$#	RDQ5\$#					
18 RDQ5@	RDQ5@					

DDR MEMORY EQUAL LINE LENGTH

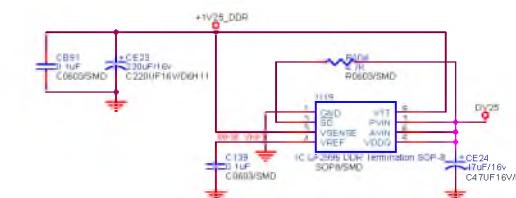
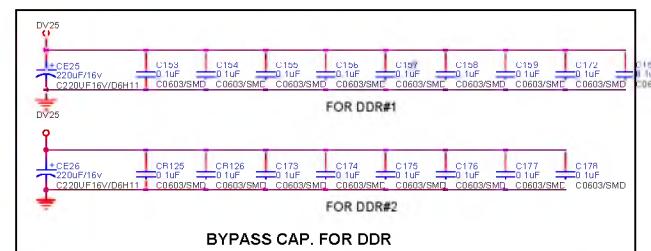


CLOSED TO MT535

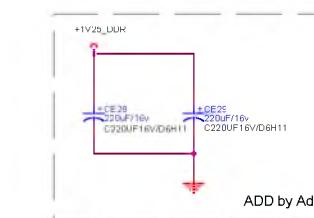
CLOSED TO DR

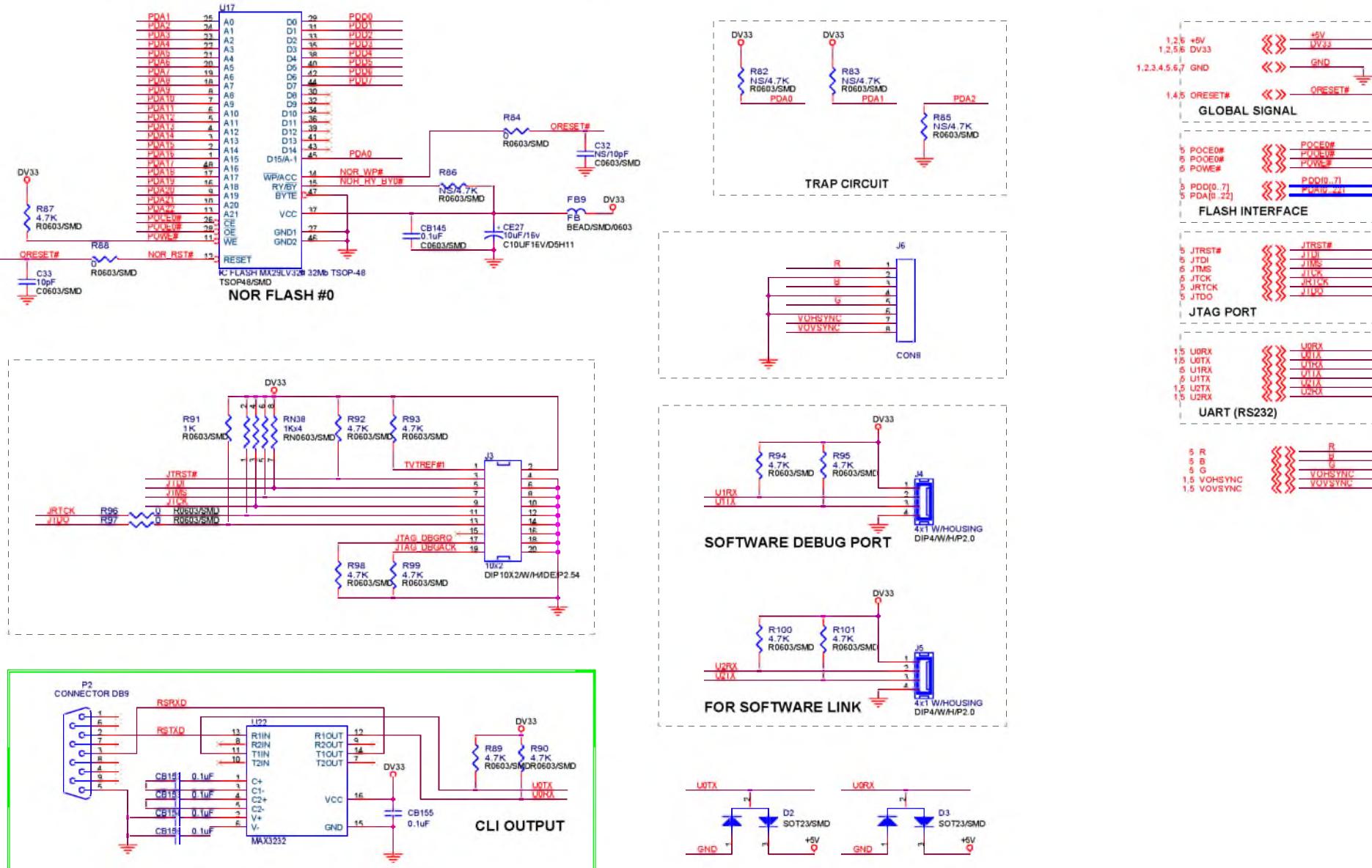


BYPASS CAP. FOR TERMINATOR
(EVERY 2 RESISTOR PUT 1 BYPASS CAP.)



BYPASS CAP. FOR DIMM
+1V25_DDR FOR DDR TERMINATOR
MEM_VREF FOR DDR AND MT5351 VREF





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	Title NOR FLASH / JTAG / UART	
Size	Document Number	Rev
Custom	MT5351RA-V2	<i>TwinSan Chan</i> 1
Date	Monday February 20, 2006	Sheet 8 of 8

Main IC Specifications

- M13S128168A (ESMT)
2M x 16 Bit x 4 Banks Double Data Rate SDRAW
- MT5111CE
Single-Chip HDTV/CATV Demodulator
- MT5351
MT5351 is a DTV Backend Decoder SOC which support flexible transport demux, HD MPEG-2 video decoder, MPEG1,2, MP3, AC3 audio decoder, HDTV encoder. MT5351 is powered by ARM 926EJ with 16K I-Cache and 16K D-Cache. It can support 64Mb to 1Gb DDR DRAM devices with configurable 32/64 bit data bus interface.
- MT8202
MT8202G is a highly integrated Single-Chip for LCD TV supporting video input and output format up to HDTV. It includes 3D comb filter TV decoder to retrieve the best image from popular composite signals.
- MT8293
HDMI PanelLink Cinema Receiver
- R2S15102NP
Digital Power Amplifier R2S15102NP
- WM8776
24-bit, 192kHz Stereo CODEC with 5 Channel I/P Multiplexer

TFT LCD Preliminary Specification

MODEL NO.: V270B1 - L01

LCD TV Head Division	
AVP	郭振隆

QRA Dept.	TVHD / PDD		
	DDIII	DDII	DDI
Approval	Approval	Approval	Approval
陳永一	李汪洋	藍文錦	林文聰

LCD TV Marketing and Product Management Division	
Product Manager	陳立宜 謝芳宜

- CONTENTS -

REVISION HISTORY	-----	3
1. GENERAL DESCRIPTION	-----	4
1.1 OVERVIEW		
1.2 FEATURES		
1.3 APPLICATION		
1.4 GENERAL SPECIFICATIONS		
1.5 MECHANICAL SPECIFICATIONS		
2. ABSOLUTE MAXIMUM RATINGS	-----	5
2.1 ABSOLUTE RATINGS OF ENVIRONMENT		
2.2 ELECTRICAL ABSOLUTE RATINGS		
2.2.1 TFT LCD MODULE		
2.2.2 BACKLIGHT UNIT		
3. ELECTRICAL CHARACTERISTICS	-----	7
3.1 TFT LCD MODULE		
3.2 BACKLIGHT INVERTER UNIT		
3.2.1 CCFL(Cold Cathode Fluorescent Lamp) CHARACTERISTICS		
3.2.2 INVERTER CHARACTERISTICS		
3.2.3 INVERTER INTERFACE CHARACTERISTICS		
4. BLOCK DIAGRAM	-----	12
4.1 TFT LCD MODULE		
5. INTERFACE PIN CONNECTION	-----	13
5.1 TFT LCD MODULE		
5.2 BACKLIGHT UNIT		
5.3 INVERTER UNIT		
5.4 BLOCK DIAGRAM OF INTERFACE		
5.5 LVDS INTERFACE		
5.6 COLOR DATA INPUT ASSIGNMENT		
6. INTERFACE TIMING	-----	19
6.1 INPUT SIGNAL TIMING SPECIFICATIONS		
6.2 POWER ON/OFF SEQUENCE		
7. OPTICAL CHARACTERISTICS	-----	22
7.1 TEST CONDITIONS		
7.2 OPTICAL SPECIFICATIONS		
8. DEFINITION OF LABELS	-----	26
8.1 CMO MODULE LABEL		
9. PACKAGING	-----	27
9.1 PACKING SPECIFICATIONS		
9.2 PACKING METHOD		
10. PRECAUTIONS	-----	29
10.1 ASSEMBLY AND HANDLING PRECAUTIONS		
10.2 SAFETY PRECAUTIONS		
11. MECHANICAL CHARACTERISTICS	-----	30

REVISION HISTORY

Version	Date	Page (New)	Section	Description
Ver 1.0	Jun. 15, '05	All	All	Preliminary Specification was first issued.

1. GENERAL DESCRIPTION

1.1 OVERVIEW

V270B1- L01 is a TFT Liquid Crystal Display module with 14-CCFL Backlight unit and 1ch-LVDS interface. The display diagonal is 27". This module supports 1366 x 768 WXGA format and can display true 16.7M colors(8-bits colors). The inverter module for backlight is built-in.

1.2 FEATURES

- Excellent brightness (550 nits)
- Ultra high contrast ratio (1000:1)
- Fast response time (8ms)
- High color saturation NTSC 75%
- WXGA (1366 x 768 pixels) resolution
- DE (Data Enable) only mode
- LVDS (Low Voltage Differential Signaling) interface
- Optimized response time for both 50/60 Hz frame rate
- Ultra wide viewing angle: 176(H)/176(V) (CR>20) Super MVA technology
- 180 degree rotation display option
- Low color shift function option
- Color reproduction (Nature color)

1.3 APPLICATION

- TFT LCD TVs
- High brightness, multi-media displays
-

1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Active Area	596.259 (H) x 335.232 (V) (27" diagonal)	mm	(1)
Bezel Opening Area	603.22 (H) x 341.98 (V)	mm	
Driver Element	a-si TFT active matrix	-	
Pixel Number	1366 x R.G.B. x 768	pixel	
Pixel Pitch (Sub Pixel)	0.1460 (H) x 0.4365 (V)	mm	
Pixel Arrangement	RGB vertical stripe	-	
Display Colors	16.7M	color	
Display Operation Mode	Transmissive mode / Normally black	-	
Surface Treatment	Hardness : 3H, Haze : 40% Anti-reflective coating < 2% reflection	-	

1.5 MECHANICAL SPECIFICATIONS

Item	Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal(H)	636.85	637.55	mm	
	Vertical(V)	379.1	379.8	mm	
	Depth(D)	33.9	35.4	mm	To PCB cover
	Depth(D)	39.2	40.7	mm	To inverter cover
Weight	3700	4000	4300	g	

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T_{ST}	-20	+60	°C	(1)
Operating Ambient Temperature	T_{OP}	0	+50	°C	(1), (2)
Shock (Non-Operating)	S_{NOP}	-	50	G	(3), (5)
Vibration (Non-Operating)	V_{NOP}	-	1.0	G	(4), (5)

Note (1) Temperature and relative humidity range is shown in the figure below.

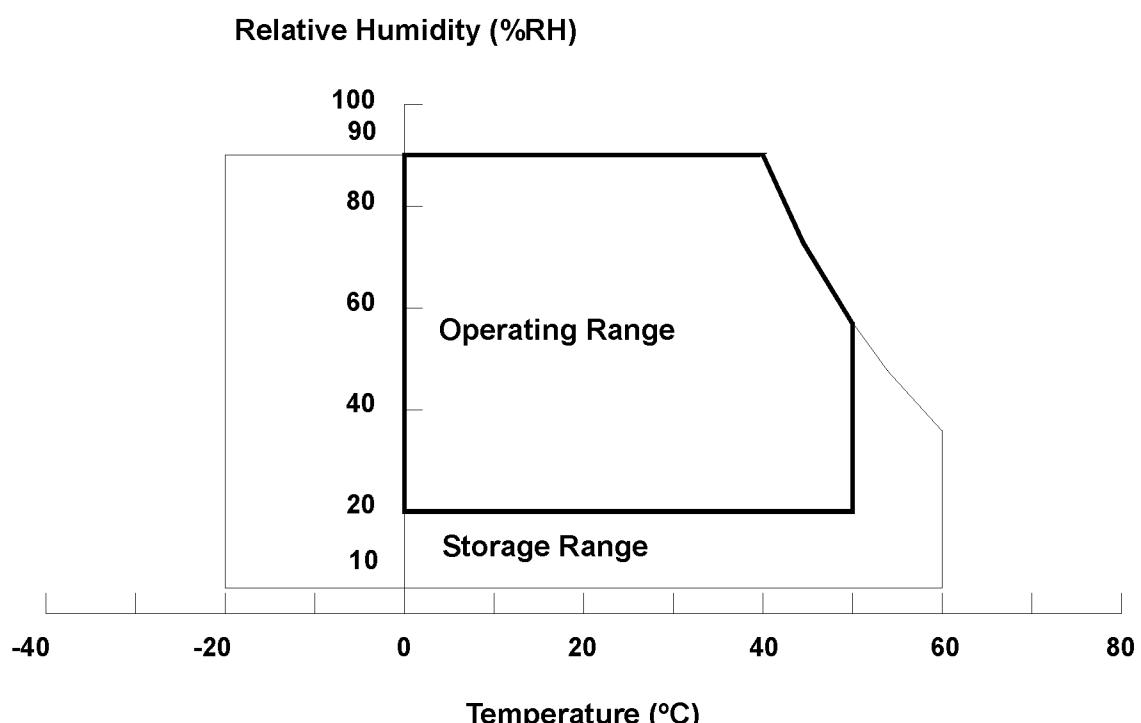
- (a) 90 %RH Max. ($T_a \leq 40$ °C).
- (b) Wet-bulb temperature should be 39 °C Max. ($T_a > 40$ °C).
- (c) No condensation.

Note (2) The maximum operating temperature is based on the test condition that the surface temperature of display area is less than or equal to 60 °C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 60 °C. The range of operating temperature may degrade in case of improper thermal management in final product design.

Note (3) 11 ms, half sine wave, 1 time for $\pm X$, $\pm Y$, $\pm Z$.

Note (4) 10 ~ 500 Hz, 10 min, 1 time each X, Y, Z.

Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.



2.2 ELECTRICAL ABSOLUTE RATINGS

2.2.1 TFT LCD MODULE

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	V _{CC}	-0.3	6.0	V	(1)
Input Signal Voltage	V _{IN}	-0.3	3.6	V	

2.2.2 BACKLIGHT UNIT

Item	Symbol	Test Condition	Min.	Type	Max.	Unit	Note
Lamp Voltage	V _W	T _a = 25 °C	—	—	3000	V _{RMS}	
Power Supply Voltage	V _{BL}	—	0	—	30	V	(1)
Control Signal Level	—	—	-0.3	—	7	V	(1), (3)

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Functional operation should be restricted to the conditions described under normal operating conditions.

Note (2) No moisture condensation or freezing.

Note (3) The control signals includes Backlight On/Off Control, Internal PWM Control, External PWM Control and Internal/External PWM Selection.

3. ELECTRICAL CHARACTERISTICS

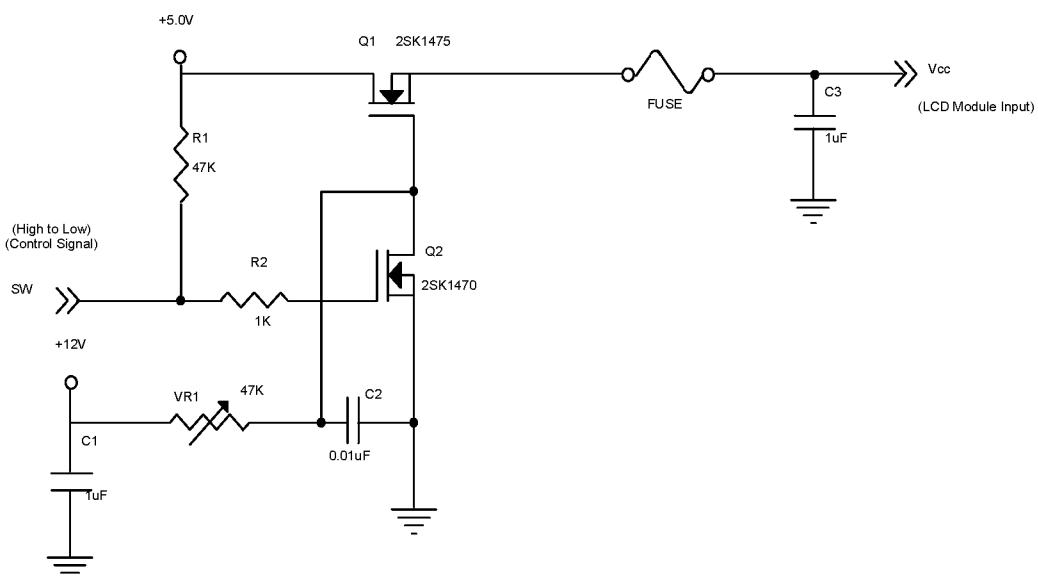
3.1 TFT LCD MODULE

T_a = 25 ± 2 °C

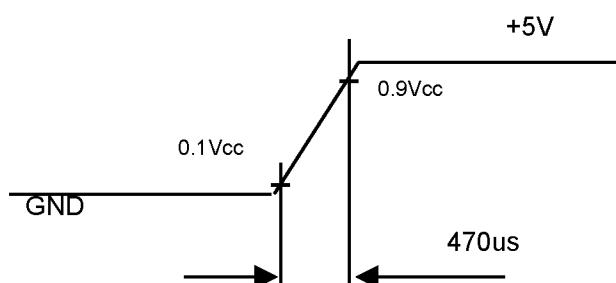
Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Supply Voltage	V _{CC}	4.5	5.0	5.5	V	(1)
Power Supply Ripple Voltage	V _{RP}	-	-	150	mV	
Rush Current	I _{RUSH}	-	-	3.0	A	(2)
Power Supply Current	I _{CC}	-	1.8	-	A	(3)
		-	1.2	-	A	
		-	1.65	-	A	
LVDS Interface	Differential Input High Threshold Voltage	V _{LVTH}	-	-	+100	mV
	Differential Input Low Threshold Voltage	V _{LVTL}	-100	-	-	mV
	Common Input Voltage	V _{LVC}	1.125	1.25	1.375	V
	Terminating Resistor	R _T		100		ohm
CMOS interface	Input High Threshold Voltage	V _{IH}	2.7	-	3.3	V
	Input Low Threshold Voltage	V _{IL}	0	-	0.7	V

Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:

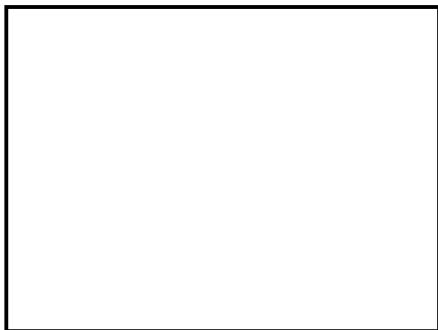


V_{cc} rising time is 470us



Note (3) The specified power supply current is under the conditions at $V_{CC} = 5$ V, $T_a = 25 \pm 2$ °C, $f_v = 60$ Hz, whereas a power dissipation check pattern below is displayed.

a. White Pattern



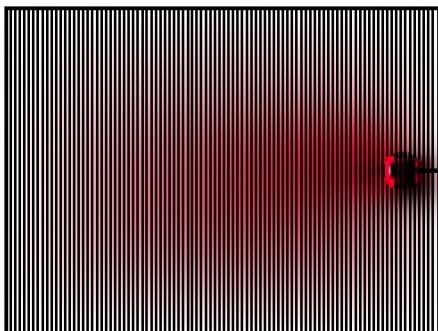
Active Area

b. Black Pattern



Active Area

c. Vertical Stripe Pattern



Active Area



3.2 BACKLIGHT INVERTER UNIT

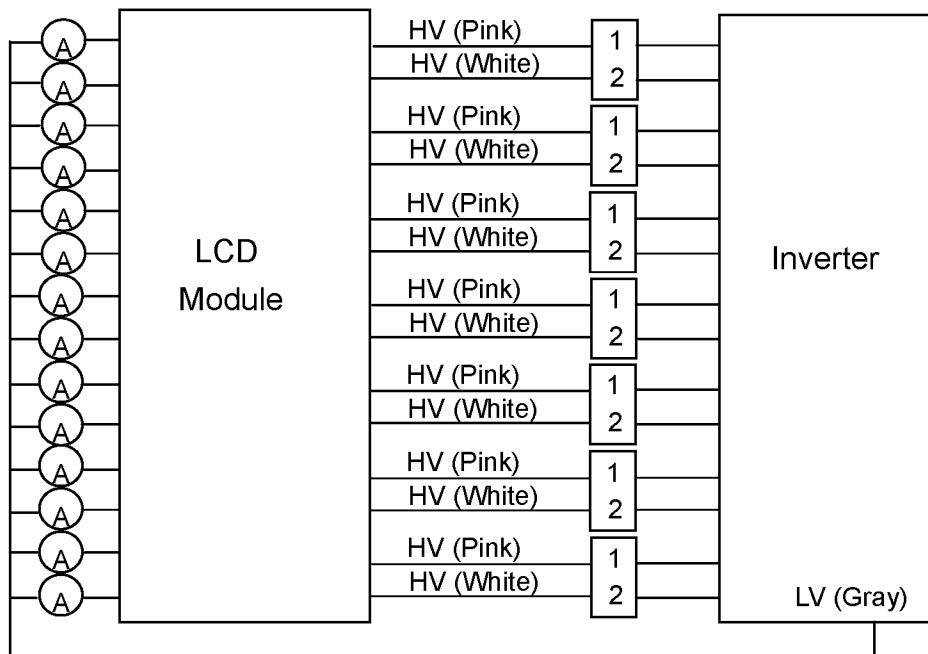
3.2.1 CCFL (Cold Cathode Fluorescent Lamp) CHARACTERISTICS ($T_a = 25 \pm 2$ °C)

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Lamp Voltage	V_W	-	1120	-	V_{RMS}	$I_L = 4.7\text{mA}$
Lamp Current	I_L	4.2	4.7	5.2	mA_{RMS}	(1)
Lamp Starting Voltage	V_S	-	-	1650	V_{RMS}	(2), $T_a = 0$ °C
		-	-	1500	V_{RMS}	(2), $T_a = 25$ °C
Operating Frequency	F_O	50	-	70	KHz	(3)
Lamp Life Time	L_{BL}	50,000	60,000	-	Hrs	(4)

3.2.2 INVERTER CHARACTERISTICS ($T_a = 25 \pm 2 {}^\circ C$)

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Consumption	P_{BL}	-	92	-	W	(5), $I_L = 4.7\text{mA}$
Power Supply Voltage	V_{BL}	22.8	24	25.2	V_{DC}	
Power Supply Current	I_{BL}	-	3.8	-	A	Non Dimming
Input Ripple Noise	-	-	-	500	mV_{P-P}	$V_{BL} = 22.8\text{V}$
Backlight Turn on Voltage	V_{BS}	1790	-	-	V_{RMS}	$T_a = 0 {}^\circ C$
		1200	-	-	V_{RMS}	$T_a = 25 {}^\circ C$
Oscillating Frequency	F_W	53	56	59	kHz	
Dimming Frequency	F_B	150	160	170	Hz	
Minimum Duty Ratio	D_{MIN}	-	10	-	%	

Note (1) Lamp current is measured by utilizing high frequency current meters as shown below:



Note (2) The lamp starting voltage V_S should be applied to the lamp for more than 1 second under starting up duration. Otherwise the lamp could not be lighted on completed.

Note (3) The lamp frequency may produce interference with horizontal synchronous frequency from the display, and this may cause line flow on the display. In order to avoid interference, the lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible.

Note (4) The life time of a lamp is defined as when the brightness is larger than 50% of its original value and the effective discharge length is longer than 80% of its original length (Effective discharge length is defined as an area that has equal to or more than 70% brightness compared to the brightness at the center point.) as the time in which it continues to operate under the condition $T_a = 25 \pm 2^\circ C$ and $I_L = 4.2 \sim 5.2 \text{ mA}_{\text{RMS}}$.

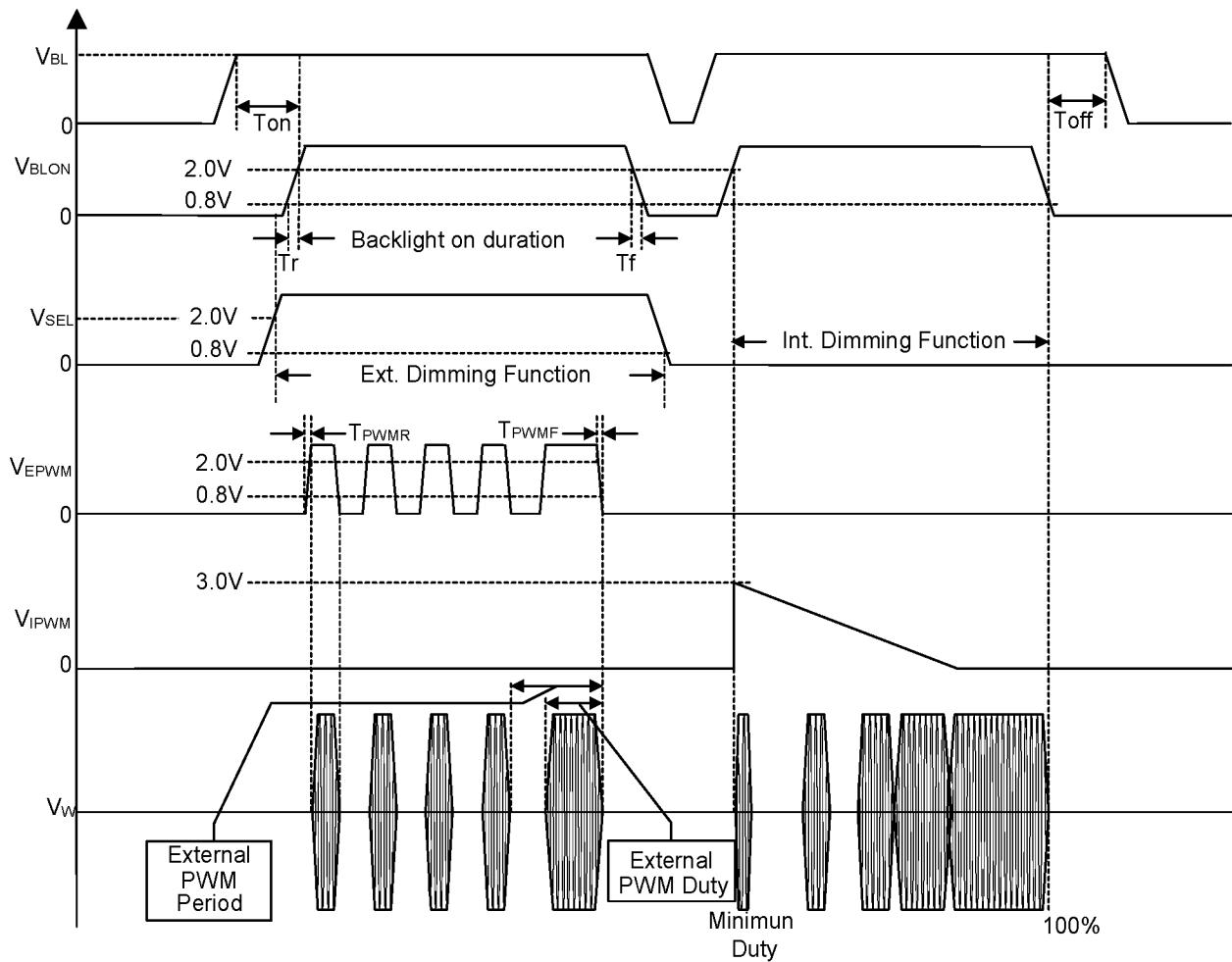
Note (5) The power supply capacity should be higher than the total inverter power consumption P_{BL} . Since the pulse width modulation (PWM) mode was applied for backlight dimming, the driving current changed as PWM duty on and off. The transient response of power supply should be considered for the changing loading when inverter dimming.

3.2.3 INVERTER INTERFACE CHARACTERISTICS

Item		Symbol	Test Condition	Min.	Typ.	Max.	Unit	Note
On/Off Control Voltage	ON	V_{BLON}	—	2.0	—	5.0	V	
	OFF		—	0	—	0.8	V	
Internal/External PWM Select Voltage	HI	V_{SEL}	—	2.0	—	5.0	V	
	LO		—	0	—	0.8	V	
Internal PWM Control Voltage	MAX	V_{IPWM}	$V_{SEL} = L$	—	—	3.0	V	minimum duty ratio
	MIN			—	0	—	V	maximum duty ratio
External PWM Control Voltage	HI	V_{EPWM}	$V_{SEL} = H$	2.0	—	5.0	V	duty on
	LO			0	—	0.8	V	duty off
Control Signal Rising Time	T_r	—	—	—	100	ms		
Control Signal Falling Time	T_f	—	—	—	100	ms		
PWM Signal Rising Time	T_{PWMR}	—	—	—	50	us		
PWM Signal Falling Time	T_{PWMF}	—	—	—	50	us		
Input impedance	R_{IN}	—	1	—	—	$M\Omega$		
BLON Delay Time	T_{on}	—	1	—	—	ms		
BLON Off Time	T_{off}	—	1	—	—	ms		

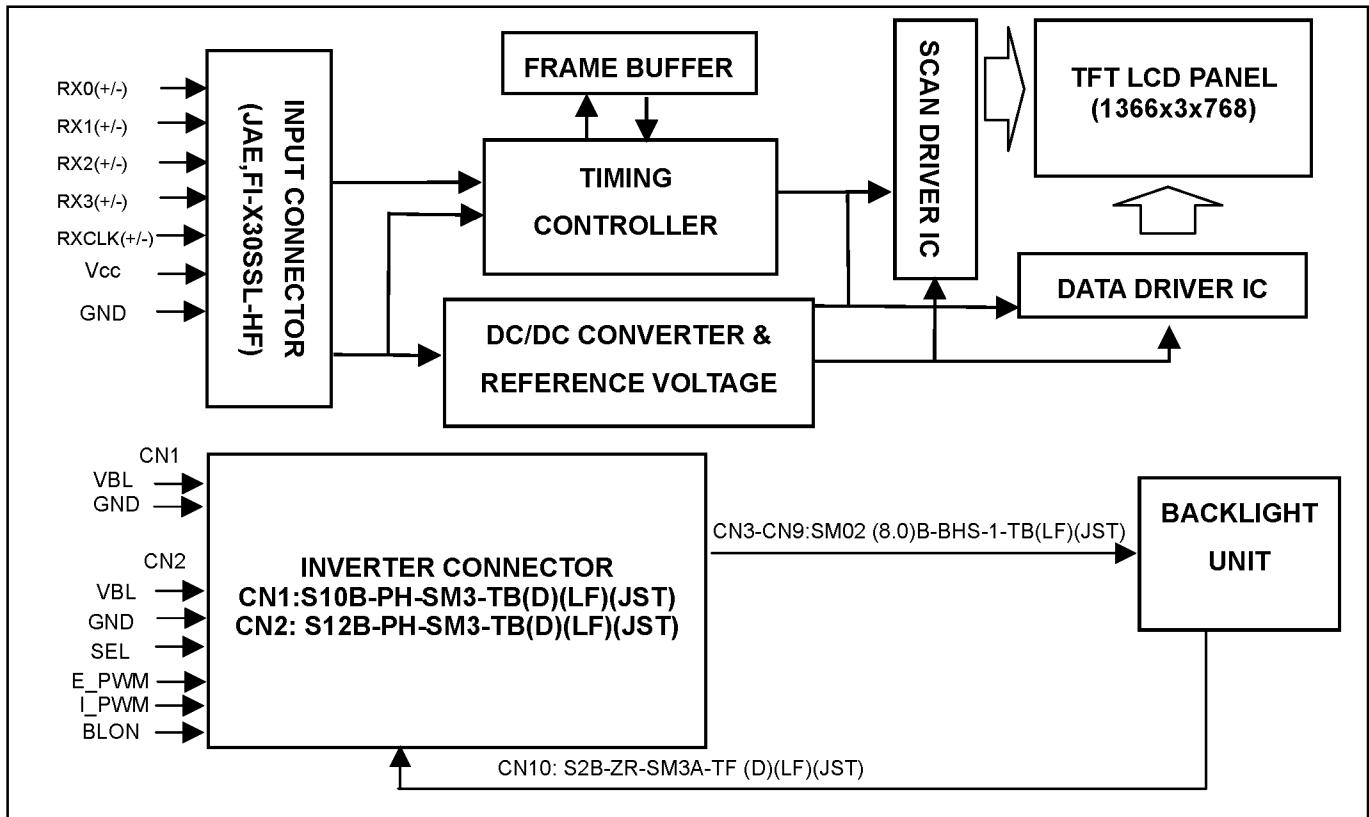
Note (1) The SEL signal should be valid before backlight turns on by BLON signal. It is inhibited to change the internal/external PWM selection (SEL) during backlight turn on period.

Note (2) The power sequence and control signal timing are shown as the following figure.



4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



5. INTERFACE PIN CONNECTION

5.1 TFT LCD MODULE

CNF1 Connector Pin Assignment

Pin No.	Symbol	Description	Note
1	GND	Ground	
2	RPF	Display Rotation	(3)
3	SELLVDS	Select LVDS data format	(5)
4	NC	No Connection	
5	NC	No Connection	(2)
6	ODSEL	Overdrive Lookup Table Selection	(4)
7	EN LCS	Low Color Shift	(6)
8	GND	Ground	
9	RX0-	Negative transmission data of pixel 0	
10	RX0+	Positive transmission data of pixel 0	
11	RX1-	Negative transmission data of pixel 1	
12	RX1+	Positive transmission data of pixel 1	
13	RX2-	Negative transmission data of pixel 2	
14	RX2+	Positive transmission data of pixel 2	
15	RXCLK-	Negative of clock	
16	RXCLK+	Positive of clock	
17	RX3-	Negative transmission data of pixel 3	
18	RX3+	Positive transmission data of pixel 3	
19	GND	Ground	
20	GND	Ground	
21	GND	Ground	
22	GND	Ground	
23	GND	Ground	
24	GND	Ground	
25	GND	Ground	
26	VCC	Power supply: +5V	
27	VCC	Power supply: +5V	
28	VCC	Power supply: +5V	
29	VCC	Power supply: +5V	
30	VCC	Power supply: +5V	

Note (1) Connector Part No.: FI-X30SSL-HF(JAE) or compatible

Note (2) Reserved for internal use. Left it open.

Note (3) Low : normal display (default), High : display with 180 degree rotation

Note (4) Overdrive lookup table selection. The Overdrive lookup table should be selected in accordance to the frame rate to optimize image quality.

ODSEL	Note
L	Lookup table was optimized for 60 Hz frame rate.
H	Lookup table was optimized for 50 Hz frame rate.

Note (5) Please refer to 5.5 LVDS INTERFACE (Page 17)

Note (6) Enable Low color shift function.

EN LCS	Note
L	Low color shift off
H	Low color shift on

5.2 BACKLIGHT UNIT

The pin configuration for the housing and leader wire is shown in the table below.

CN3-CN9 (Housing): BHR-03VS-1 (JST)

Pin No.	Symbol	Description	Wire Color
1	HV	High Voltage	Pink
2	HV	High Voltage	White

Note (1) The backlight interface housing for high voltage side is a model BHR-03VS-1, manufactured by JST.

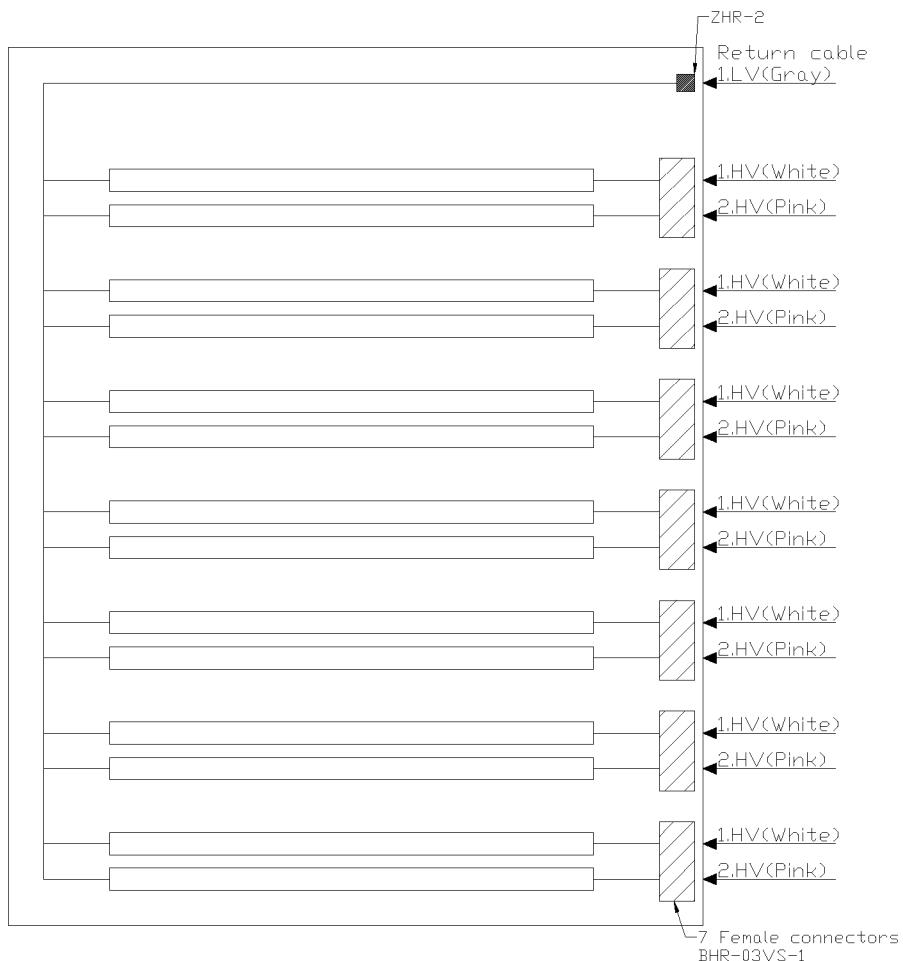
The mating header on inverter part number is SM02(8.0)B-BHS-1-TB(LF) or equivalent.

CN10 (Housing): ZHR-2 (JST) or equivalent

Pin No.	Symbol	Description	Wire Color
1	LV	Low Voltage (+)	Gray
2	NC	No Connection	-

Note (2) The backlight interface housing and return cable for low voltage side is a model ZHR-2 , manufactured

by JST or equivalent. The mating header on inverter part number is S2B-ZR-SM3A-TF(D)(LF) or equivalent.



5.3 INVERTER UNIT

CN1(Header): S10B-PH-SM3-TB(D)(LF)(JST) or equivalent.

Pin	Name	Description
1	VBL	+24V Power input
2		
3		
4		
5		
6	GND	Ground
7		
8		
9		
10		

CN2(Header): S12B-PH-SM3-TB(D)(LF)(JST) or equivalent.

Pin	Name	Description
1	VBL	+24V Power input
2		
3		
4		
5		
6	GND	Ground
7		
8		
9	SEL	Internal/external PWM selection High : external dimming Low : internal dimming
10	E_PWM	External PWM control signal E_PWM should be connected to low when internal PWM was selected (SEL = low).
11	I_PWM	Internal PWM control signal I_PWM should be connected to ground when external PWM was selected (SEL = high).
12	BLON	Backlight on/off control

CN3-CN9(Header): SM02(8.0)B-BHS-1-TB(LF)(JST) or equivalent

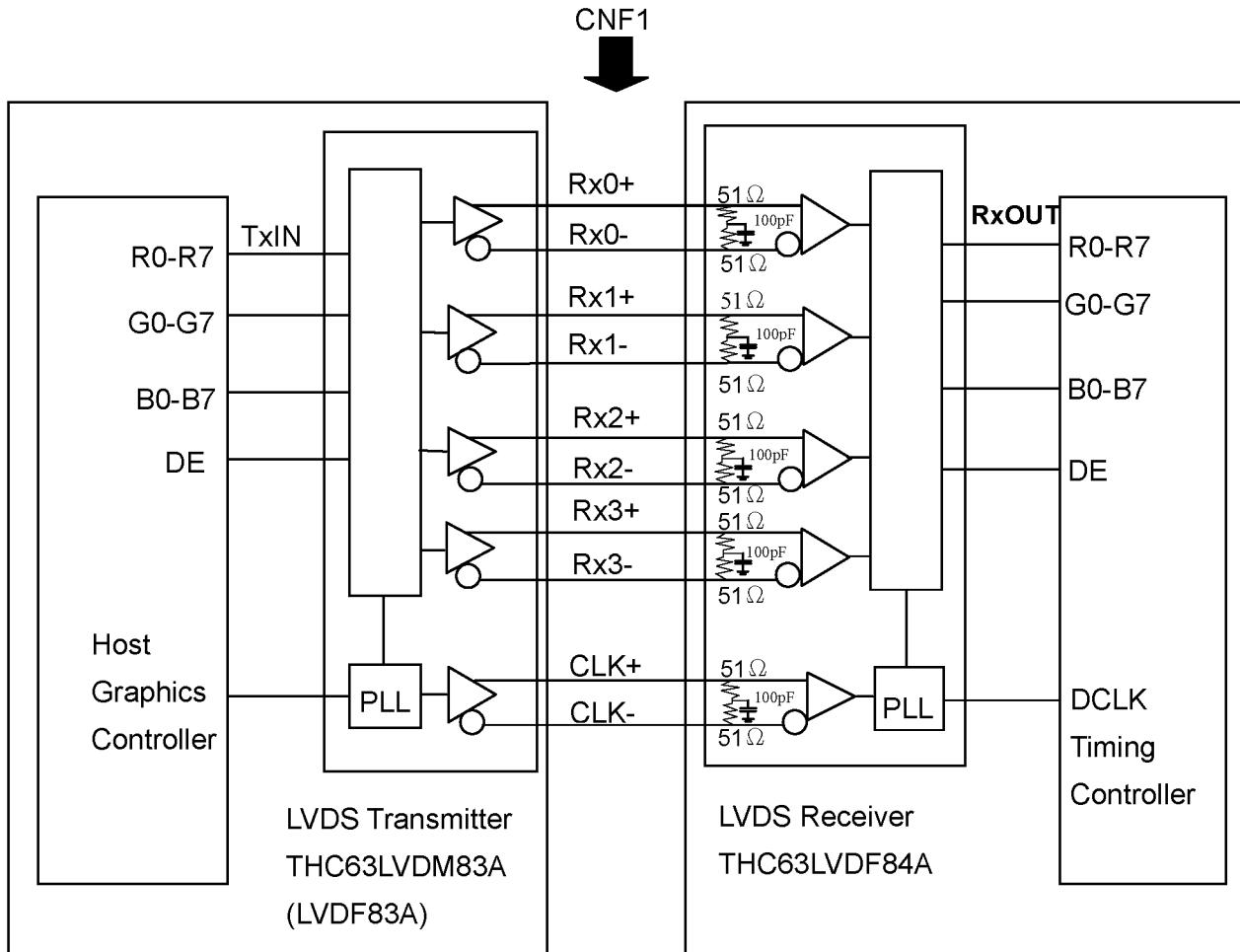
Pin	Name	Description
1	CCFL HOT	CCFL high voltage
2	CCFL HOT	CCFL high voltage

CN10(Header): S2B-ZR-SM3A-TF(D)(LF)(JST) or equivalent

Pin	Name	Description
1	CCFL COLD	CCFL low voltage
2	NC	-

Note (1) Floating of any control signal is not allowed.

5.4 BLOCK DIAGRAM OF INTERFACE



R0~R7 : Pixel R Data ,

G0~G7 : Pixel G Data ,

B0~B7 : Pixel B Data ,

DE : Data enable signal

Note (1) The system must have the transmitter to drive the module.

Note (2) LVDS cable impedance shall be 50 ohms per signal line or about 100 ohms per twist-pair line when it is used differentially.

5.5 LVDS INTERFACE

SIGNAL		TRANSMITTER THC63LVDM83A		INTERFACE CONNECTOR		RECEIVER THC63LVDF84A		TFT CONTROL INPUT		
SELLVDS =L	SELLVDS =H	PIN	INPUT	Host	TFT-LCD	PIN	OUTPUT	SELLVDS =L	SELLVDS =H	
24 bit	R0	R2	51	TxIN0	TA OUT0+	Rx 0+	27	Rx OUT0	R0	R2
	R1	R3	52	TxIN1			29	Rx OUT1	R1	R3
	R2	R4	54	TxIN2			30	Rx OUT2	R2	R4
	R3	R5	55	TxIN3			32	Rx OUT3	R3	R5
	R4	R6	56	TxIN4			33	Rx OUT4	R4	R6
	R5	R7	3	TxIN6			35	Rx OUT6	R5	R7
	G0	G2	4	TxIN7	TA OUT0-	Rx 0-	37	Rx OUT7	G0	G2
	G1	G3	6	TxIN8			38	Rx OUT8	G1	G3
	G2	G4	7	TxIN9			39	Rx OUT9	G2	G4
	G3	G5	11	TxIN12	TA OUT1+	Rx 1+	43	Rx OUT12	G3	G5
	G4	G6	12	TxIN13			45	Rx OUT13	G4	G6
	G5	G7	14	TxIN14			46	Rx OUT14	G5	G7
	B0	B2	15	TxIN15			47	Rx OUT15	B0	B2
	B1	B3	19	TxIN18	TA OUT1-	Rx 1-	51	Rx OUT18	B1	B3
	B2	B4	20	TxIN19			53	Rx OUT19	B2	B4
	B3	B5	22	TxIN20			54	Rx OUT20	B3	B5
	B4	B6	23	TxIN21	TA OUT2+	Rx 2+	55	Rx OUT21	B4	B6
	B5	B7	24	TxIN22			1	Rx OUT22	B5	B7
	DE	DE	30	TxIN26			6	Rx OUT26	DE	DE
	R6	R0	50	TxIN27			7	Rx OUT27	R6	R0
	R7	R1	2	TxIN5	TA OUT2-	Rx 2-	34	Rx OUT5	R7	R1
	G6	G0	8	TxIN10			41	Rx OUT10	G6	G0
	G7	G1	10	TxIN11			42	Rx OUT11	G7	G1
	B6	B0	16	TxIN16	TA OUT3+	Rx 3+	49	Rx OUT16	B6	B0
	B7	B1	18	TxIN17			50	Rx OUT17	B7	B1
	RSVD 1	RSVD 1	25	TxIN23			2	Rx OUT23	NC	NC
	RSVD 2	RSVD 2	27	TxIN24			3	Rx OUT24	NC	NC
	RSVD 3	RSVD 3	28	TxIN25	TA OUT3-	Rx 3-	5	Rx OUT25	NC	NC
		DCLK	31	TxCLK IN			26	RxCLK OUT	DCLK	

R0~R7: Pixel R Data (7; MSB, 0; LSB)

G0~G7: Pixel G Data (7; MSB, 0; LSB)

B0~B7: Pixel B Data (7; MSB, 0; LSB)

DE: Data enable signal

Notes(1) RSVD(reserved)pins on the transmitter shall be "H" or "L".

5.6 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																						
		Red					Green					Blue												
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Green(253)	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale Of Blue	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

6. INTERFACE TIMING

6.1 INPUT SIGNAL TIMING SPECIFICATIONS

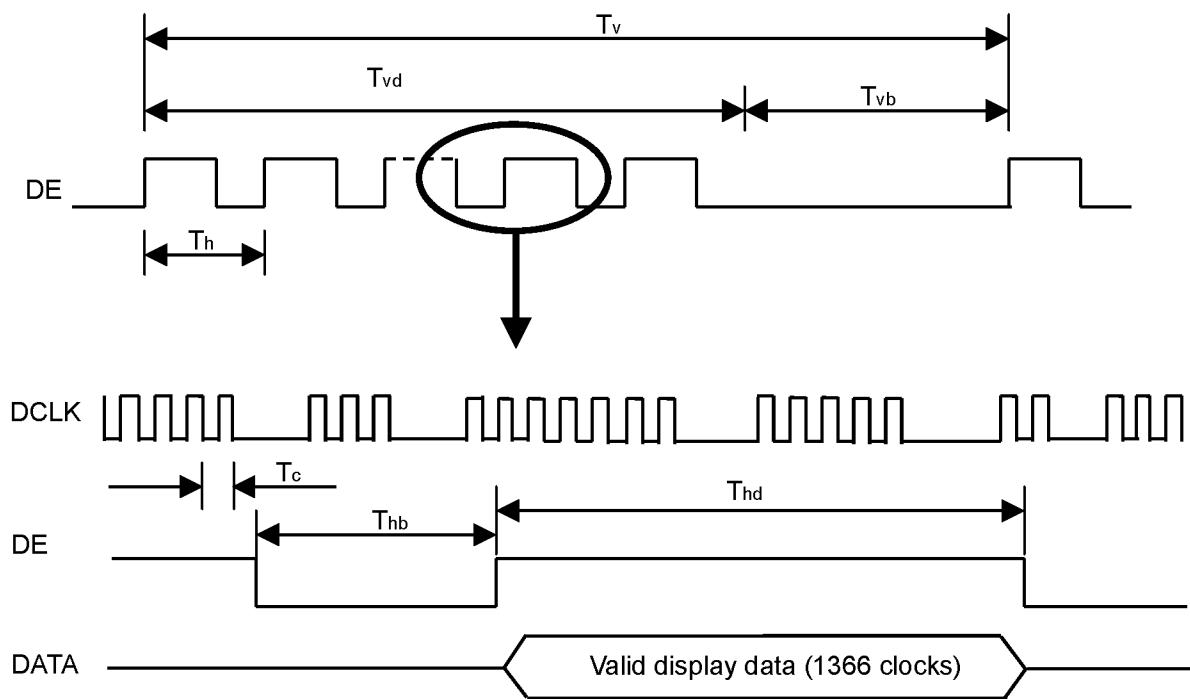
The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Receiver Clock	Frequency	1/Tc	60	86	88	MHz	
	Input cycle to cycle jitter	Trcl	-	-	200	ps	
LVDS Receiver Data	Setup Time	Tlvsu	600	-	-	ps	
	Hold Time	Tlvhd	600	-	-	ps	
Vertical Active Display Term	Frame Rate	Fr5	47	50	53	Hz	(2)
		Fr6	57	60	63	Hz	
	Total	Tv	770	795	888	Th	Tv=Tvd+Tvb
	Display	Tvd	768	768	768	Th	-
	Blank	Tvb	2	27	120	Th	-
	Total	Th	1436	1798	1936	Tc	Th=Thd+Thb
Horizontal Active Display Term	Display	Thd	1366	1366	1366	Tc	-
	Blank	Thb	70	432	570	Tc	-

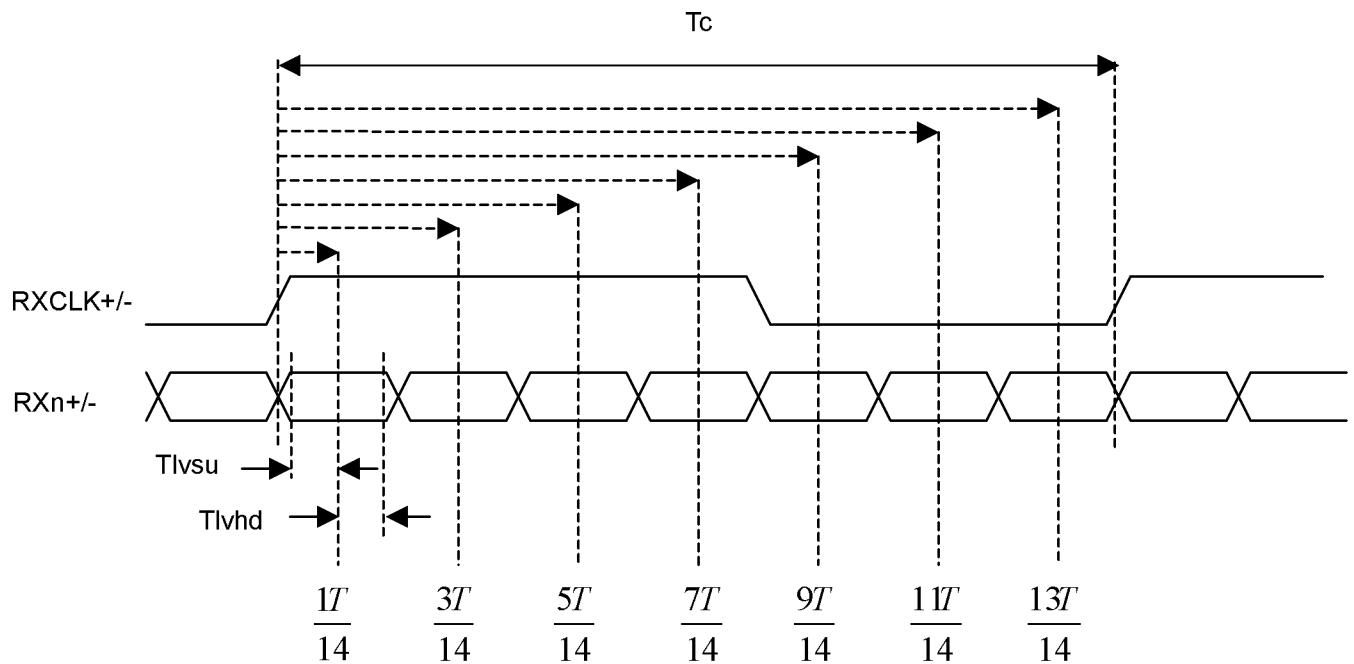
Note (1) Since this module is operated in DE only mode, Hsync and Vsync input signals should be set to low logic level. Otherwise, this module would operate abnormally.

(2) Please refer to 5.1 for detail information.

INPUT SIGNAL TIMING DIAGRAM

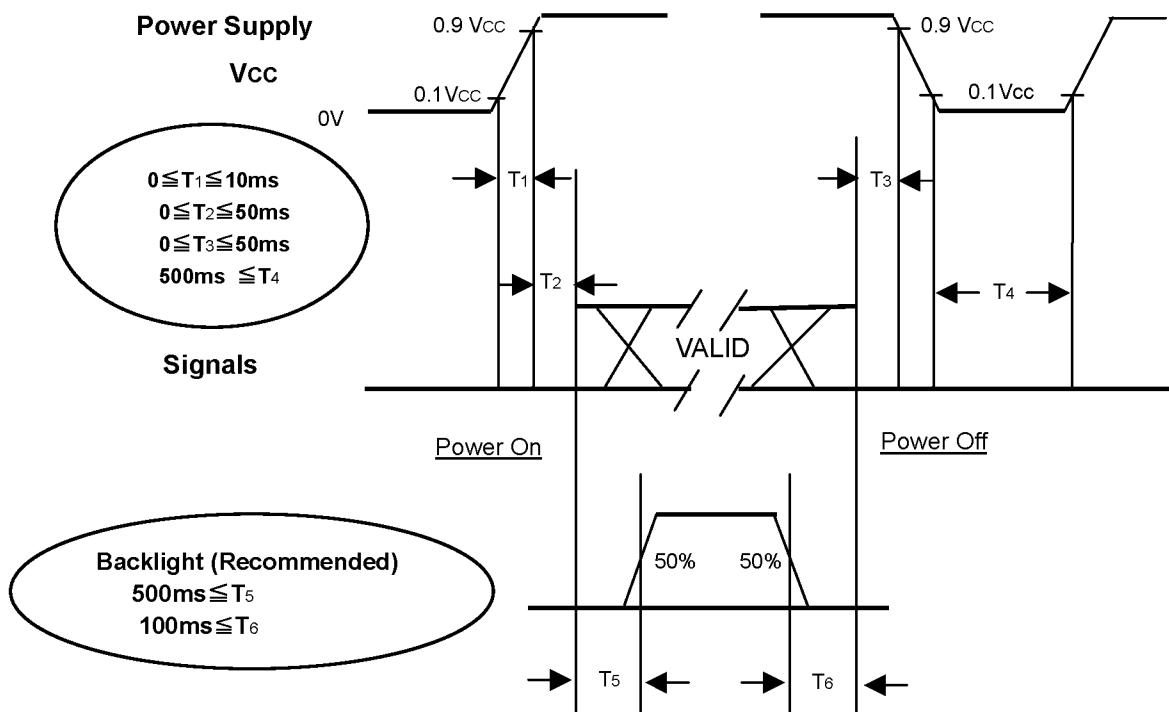


LVDS RECEIVER INTERFACE TIMING DIAGRAM



6.2 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should be as the diagram below.



Power ON/OFF Sequence

Note (1) The supply voltage of the external system for the module input should follow the definition of Vcc.

Note (2) Apply the lamp voltage within the LCD operation range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal screen.

Note (3) In case of Vcc is in off level, please keep the level of input signals on the low or high impedance.

Note (4) T4 should be measured after the module has been fully discharged between power off and on period.

Note (5) Interface signal shall not be kept at high impedance when the power is on.

7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	T _a	25±2	°C
Ambient Humidity	H _a	50±10	%RH
Supply Voltage	V _{CC}	5.0	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Lamp Current	I _L	4.7 ± 0.5	mA
Oscillating Frequency (Inverter)	F _W	56 ± 3	KHz

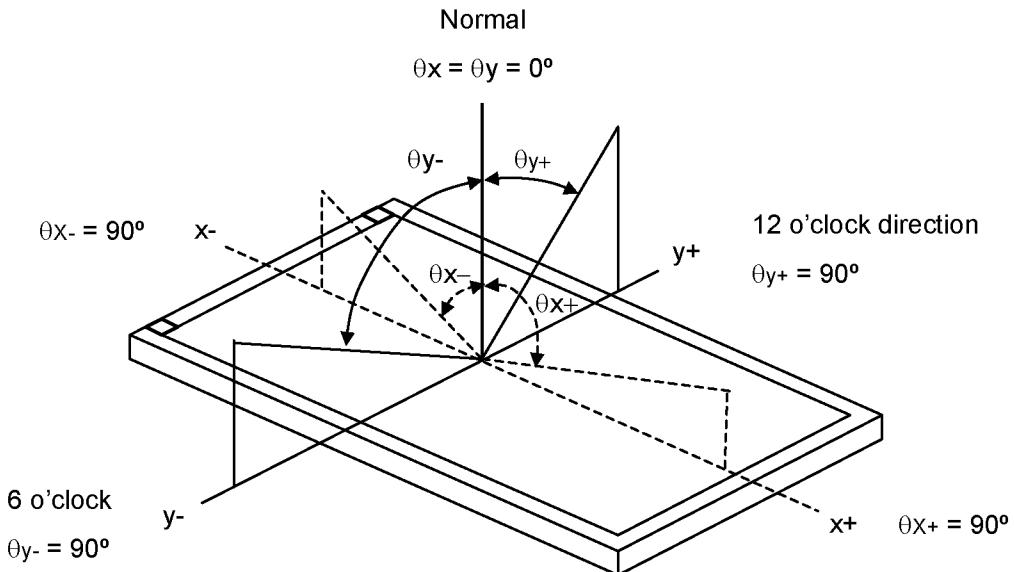
7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 7.2. The following items should be measured under the test conditions described in 7.1 and stable environment shown in Note (6).

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio	CR	$\theta_x=0^\circ, \theta_y=0^\circ$ Viewing Normal Angle	(1000)			-	(2)
Response Time	Gray to gray average		(8)			ms	(3)
Center Luminance of White	L _c		(550)			cd/m ²	(4)
White Variation	δW			(1.3)		-	(7)
Cross Talk	CT			(4)		%	(5)
Color Chromaticity	Red		(0.652)			-	(6)
	Ry		(0.331)			-	
	Green		(0.275)			-	
	Gy		(0.597)			-	
	Blue		(0.143)			-	
	Bx		(0.063)			-	
Viewing Angle	White		(0.285)			Target	(1)
	Wy		(0.293)				
	Color Gamut		(75)			%	
	Horizontal		(88)			Deg.	
	θ _x +		(88)				
	θ _x -		(88)				
	Vertical		(88)				
	θ _y +		(88)				
	θ _y -		(88)				
	CR≥20						

Note (1) Definition of Viewing Angle (θ_x , θ_y):

Viewing angles are measured by EZ-Contrast 160R (Eldim)



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{255} / L_0$$

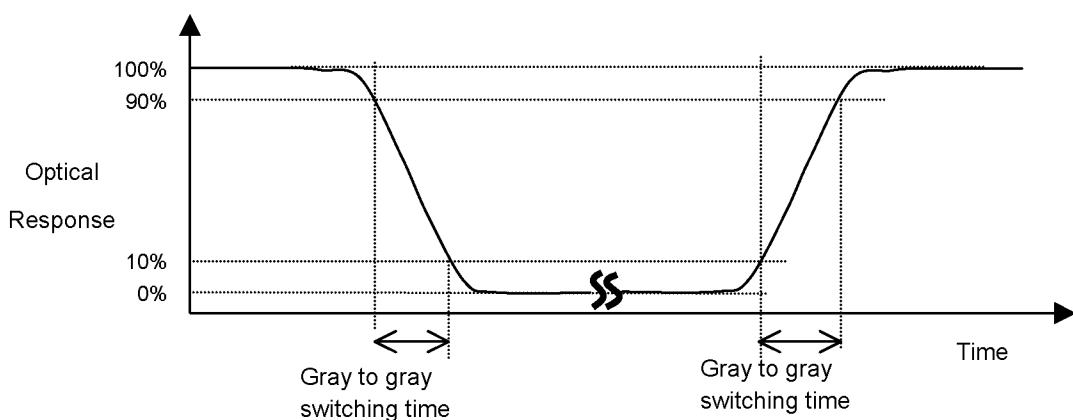
L_{255} : Luminance of gray level 255

L_0 : Luminance of gray level 0

$$CR = CR(5)$$

CR (X) is corresponding to the Contrast Ratio of the point X at the figure in Note (7).

Note (3) Definition of Gray to Gray Switching Time :



The driving signal means the signal of gray level 0, 63, 127, 191, 255.

Gray to gray average time means the average switching time of gray level 0, 63, 127, 191, 255 to each other .

Note (4) Definition of Luminance of White (L_C , L_{AVE}):

Measure the luminance of gray level 255 at center point and 5 points

$$L_C = L(5)$$

$$L_{AVE} = [L(1) + L(2) + L(3) + L(4) + L(5)] / 5$$

$L(x)$ is corresponding to the luminance of the point X at the figure in Note (7).

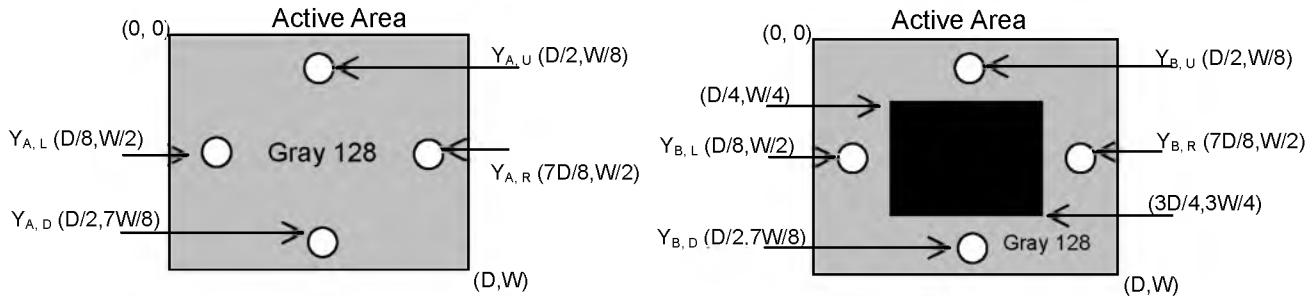
Note (5) Definition of Cross Talk (CT):

$$CT = |Y_B - Y_A| / Y_A \times 100 (\%)$$

Where:

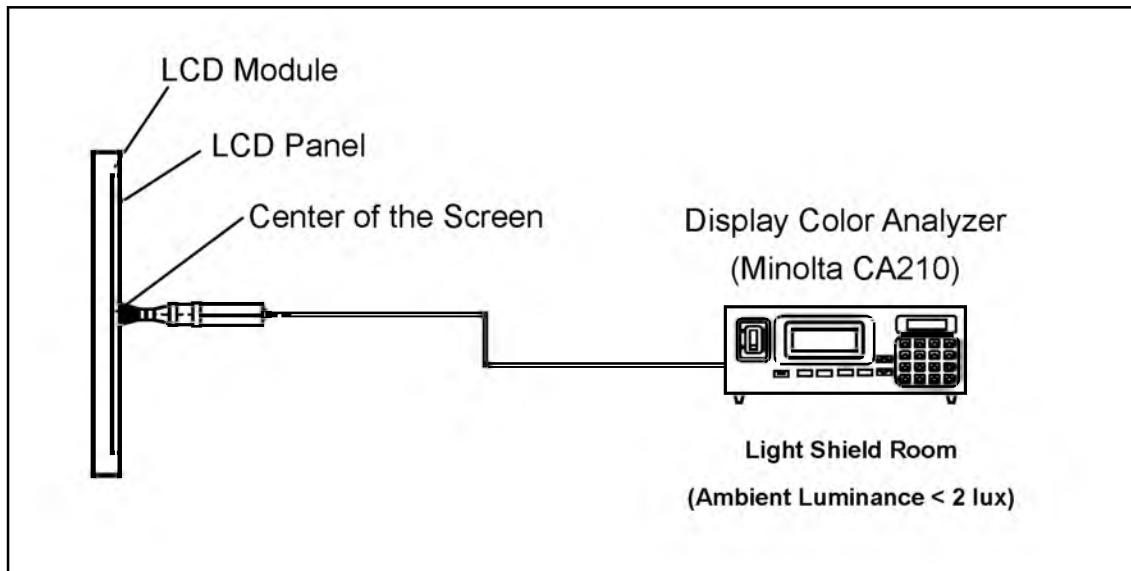
Y_A = Luminance of measured location without gray level 0 pattern (cd/m^2)

Y_B = Luminance of measured location with gray level 0 pattern (cd/m^2)



Note (6) Measurement Setup:

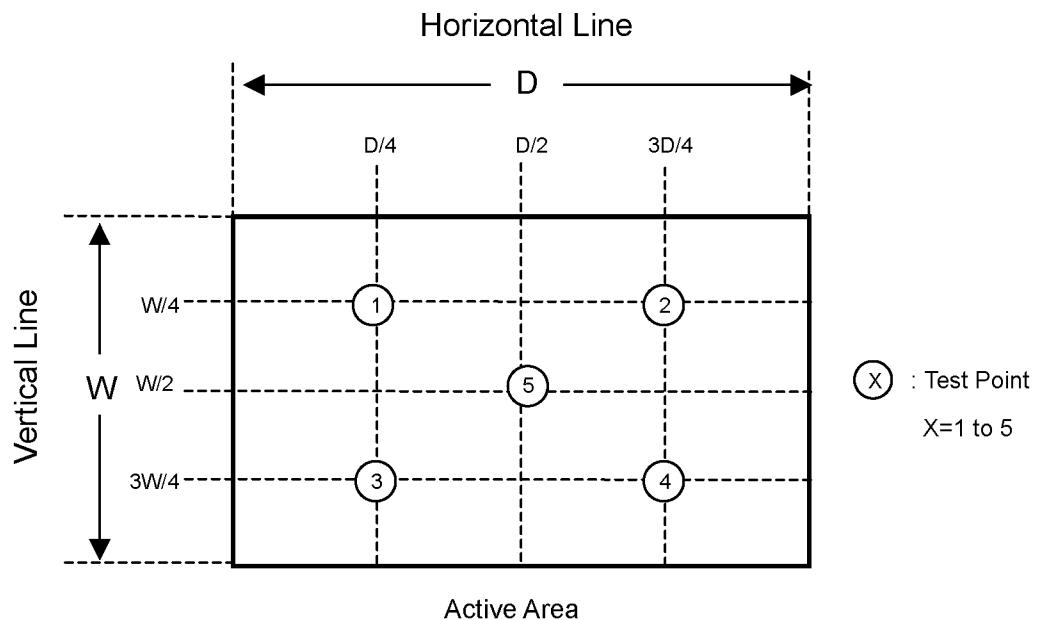
The LCD module should be stabilized at given temperature for 1 hour to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 1 hour in a windless room.



Note (7) Definition of White Variation (δW):

Measure the luminance of gray level 255 at 5 points

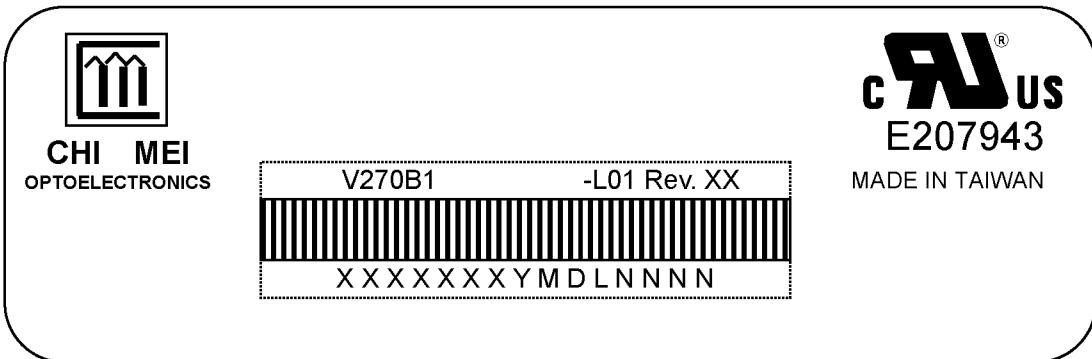
$\delta W = \text{Maximum } [L(1), L(2), L(3), L(4), L(5)] / \text{Minimum } [L(1), L(2), L(3), L(4), L(5)]$



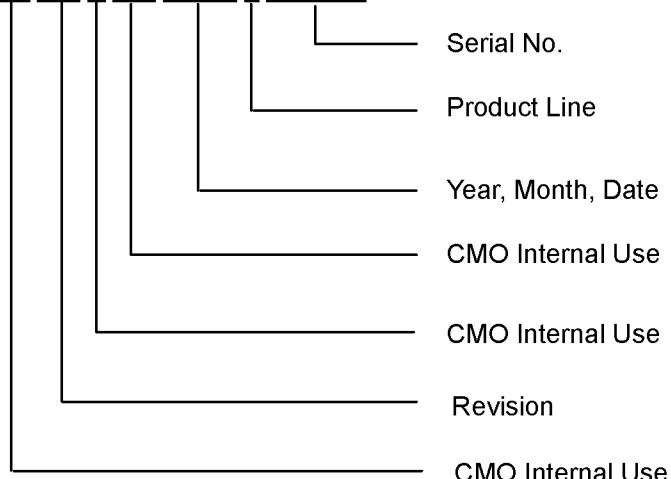
8. DEFINITION OF LABELS

8.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



- (a) Model Name: V270B1-L01
- (b) Revision: Rev. XX, for example: A0, A1... B1, B2... or C1, C2...etc.
- (c) Serial ID: XXXXXX XX Y M D L NNNN



Serial ID includes the information as below:

- (a) Manufactured Date: Year: 1~9, for 2001~2009
Month: 1~9, A~C, for Jan. ~ Dec.
Day: 1~9, A~Y, for 1st to 31st, exclude I ,O, and U.
- (b) Revision Code: Cover all the change
- (c) Serial No.: Manufacturing sequence of product
- (d) Product Line: 1 -> Line1, 2 -> Line 2, ...etc.

9. PACKAGING

9.1 PACKING SPECIFICATIONS

- (1) 4 LCD TV modules / 1 Box
- (2) Box dimensions : 742(L) X 327 (W) X 510 (H)
- (3) Weight : approximately 19Kg (4 modules per box)

9.2 PACKING METHOD

Figures 9-1 and 9-2 are the packing method

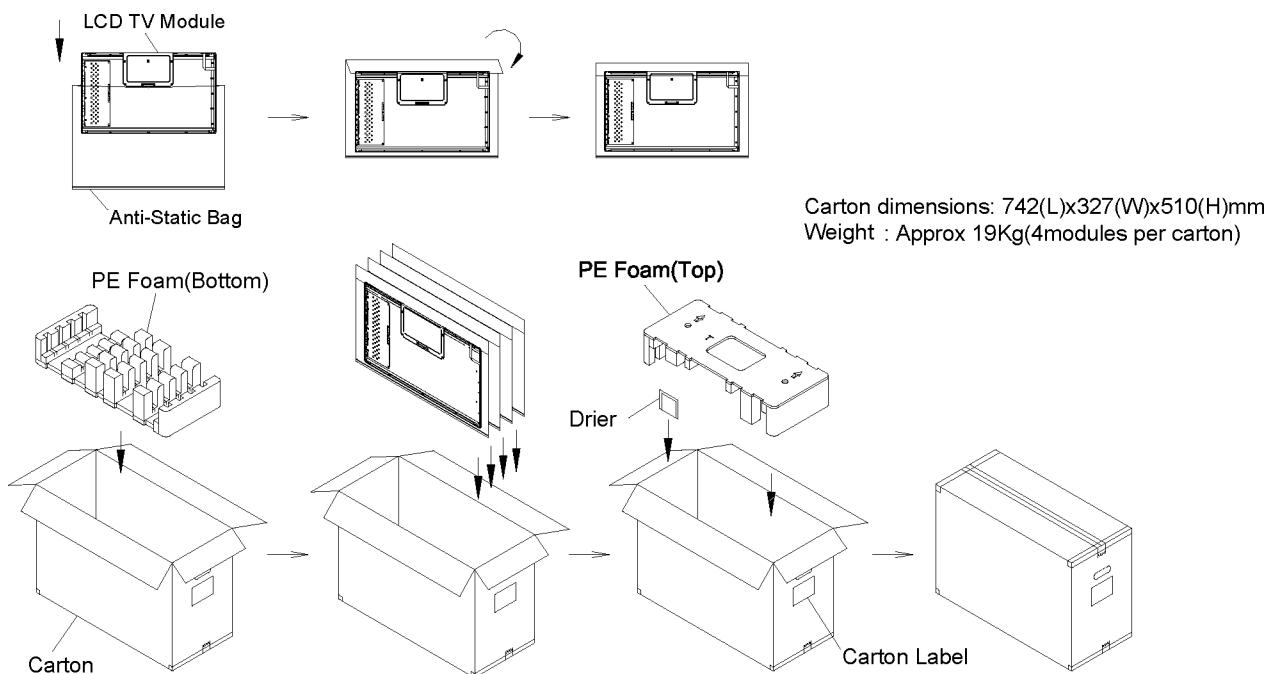


Figure.9-1 packing method

Corner Protector:L1020*50mm*50mm
Pallet:L1100*W1100*H135mm
Corrugated Fiberboard:L1100*W1100mm
Pallet Stack:L1100*W1100*H1160mm
Gross:168kg

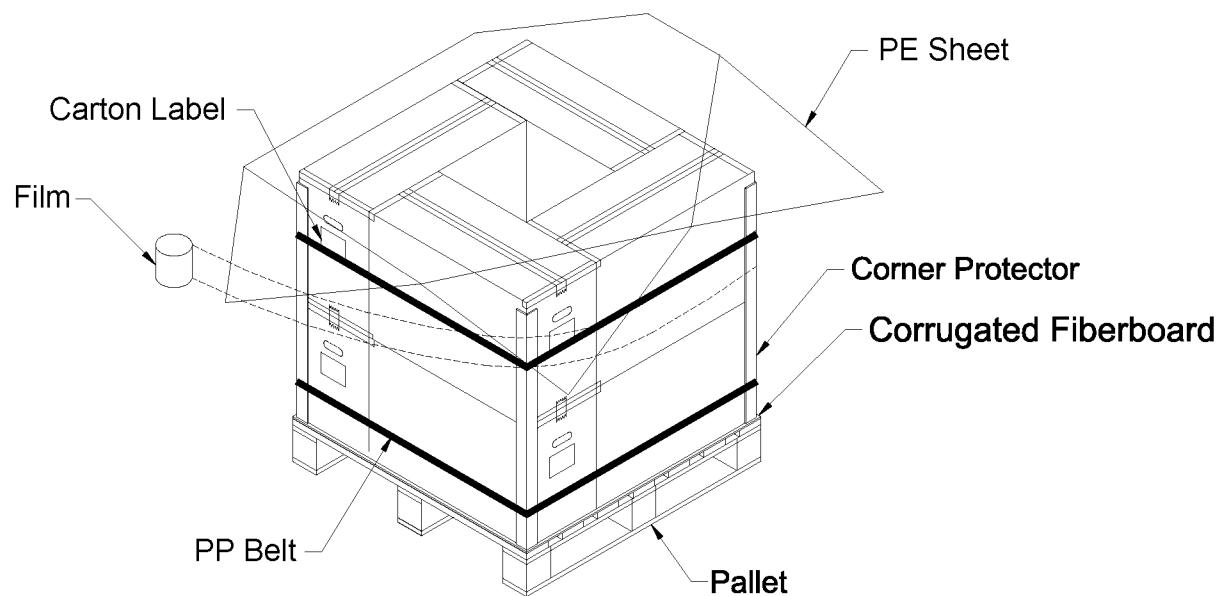


Figure. 9-2 packing method

10. PRECAUTIONS

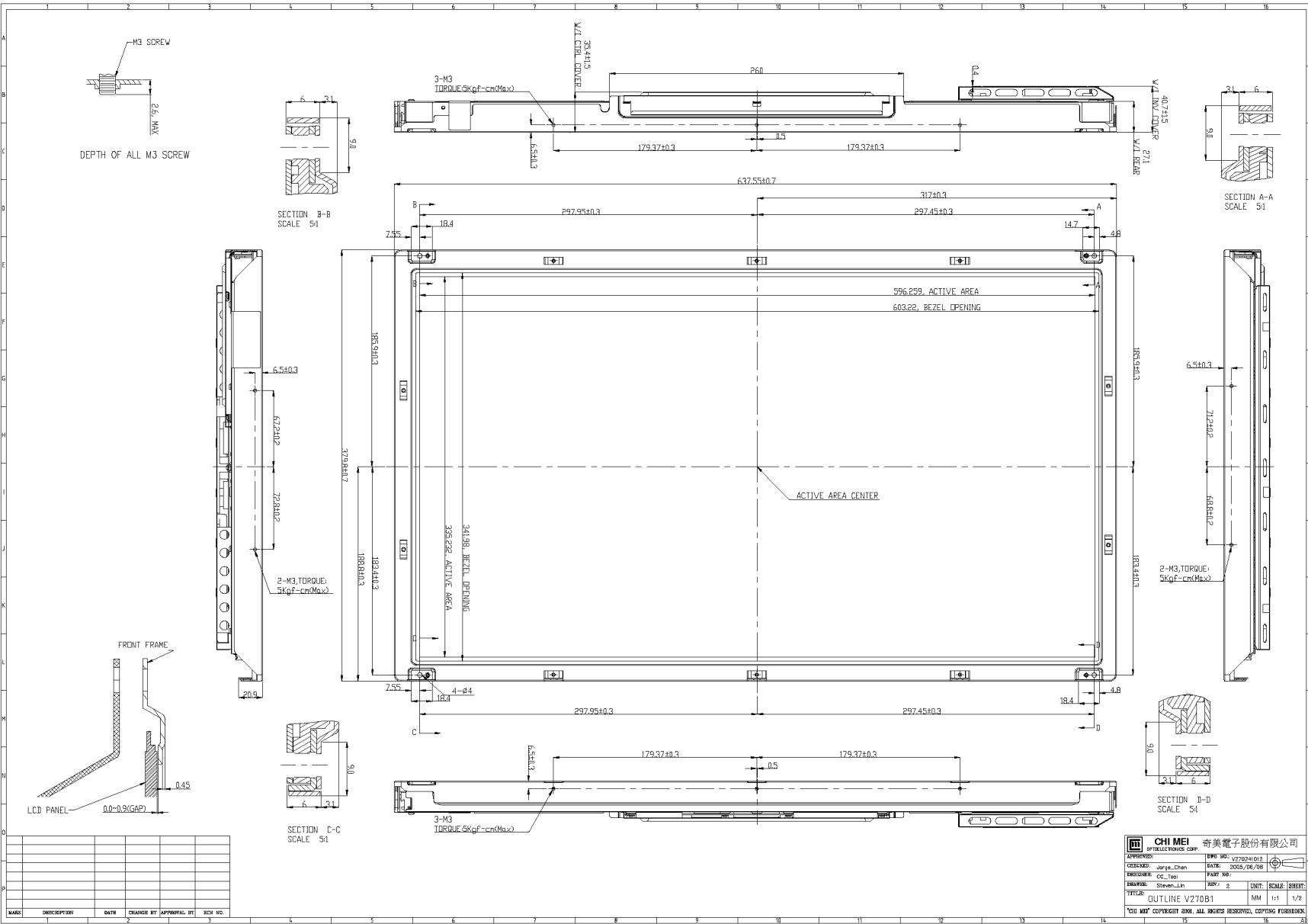
10.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) It is recommended to assemble or to install a module into the user's system in clean working areas.
The dust and oil may cause electrical short or worsen the polarizer.
- (3) Do not apply pressure or impulse to the module to prevent the damage of LCD panel and backlight.
- (4) Always follow the correct power-on sequence when the LCD module is turned on. This can prevent the damage and latch-up of the CMOS LSI chips.
- (5) Do not plug in or pull out the I/F connector while the module is in operation.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) Moisture can easily penetrate into LCD module and may cause the damage during operation.
- (9) High temperature or humidity may deteriorate the performance of LCD module. Please store LCD modules in the specified storage conditions.
- (10) When ambient temperature is lower than 10°C, the display quality might be reduced. For example, the response time will become slow, and the starting voltage of CCFL will be higher than that of room temperature.

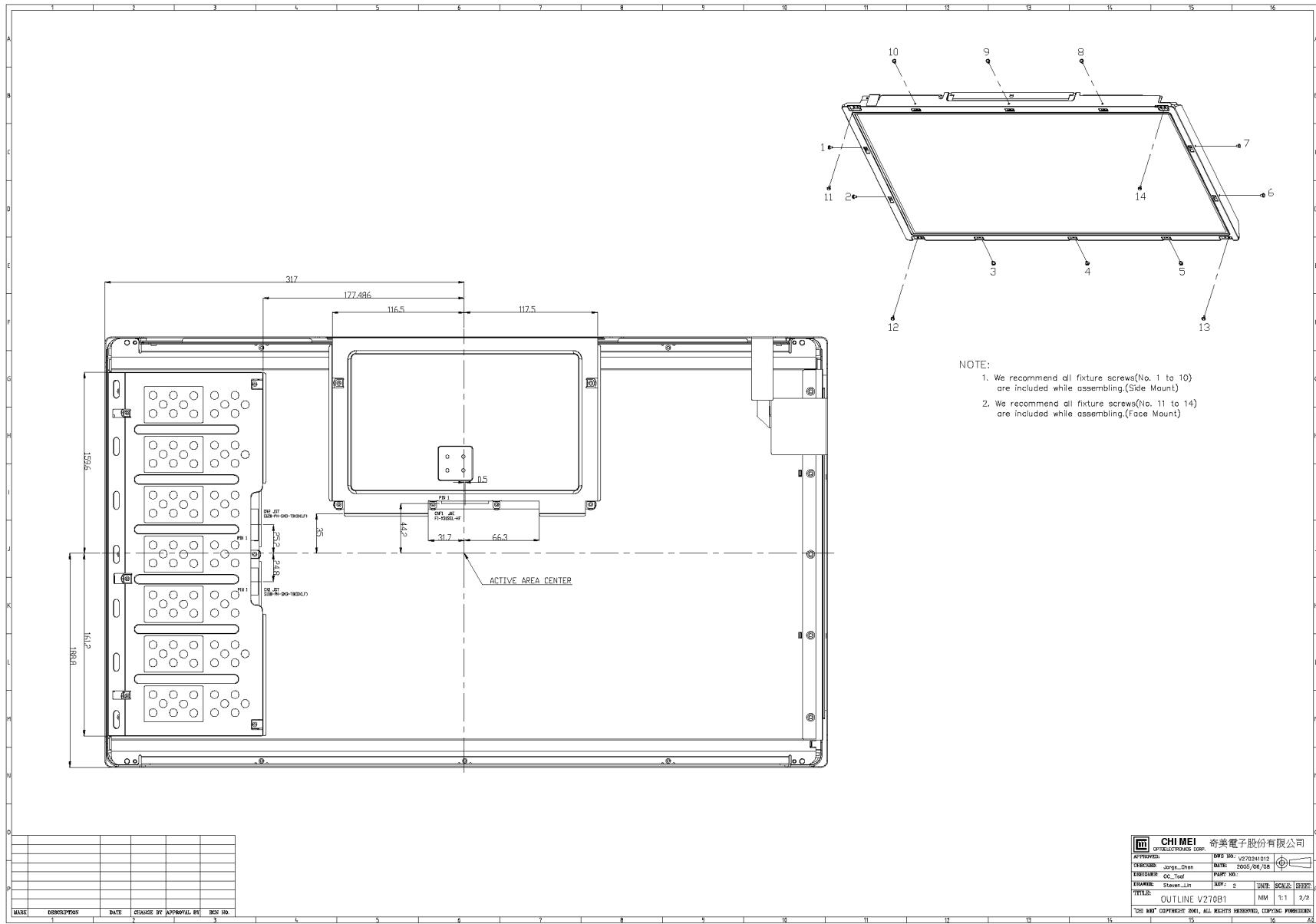
10.2 SAFETY PRECAUTIONS

- (1) The startup voltage of a backlight is over 1000 Volts. It may cause an electrical shock while assembling with the inverter. Do not disassemble the module or insert anything into the backlight unit.
- (2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (3) After the module's end of life, it is not harmful in case of normal operation and storage.

11. MECHANICAL CHARACTERISTICS

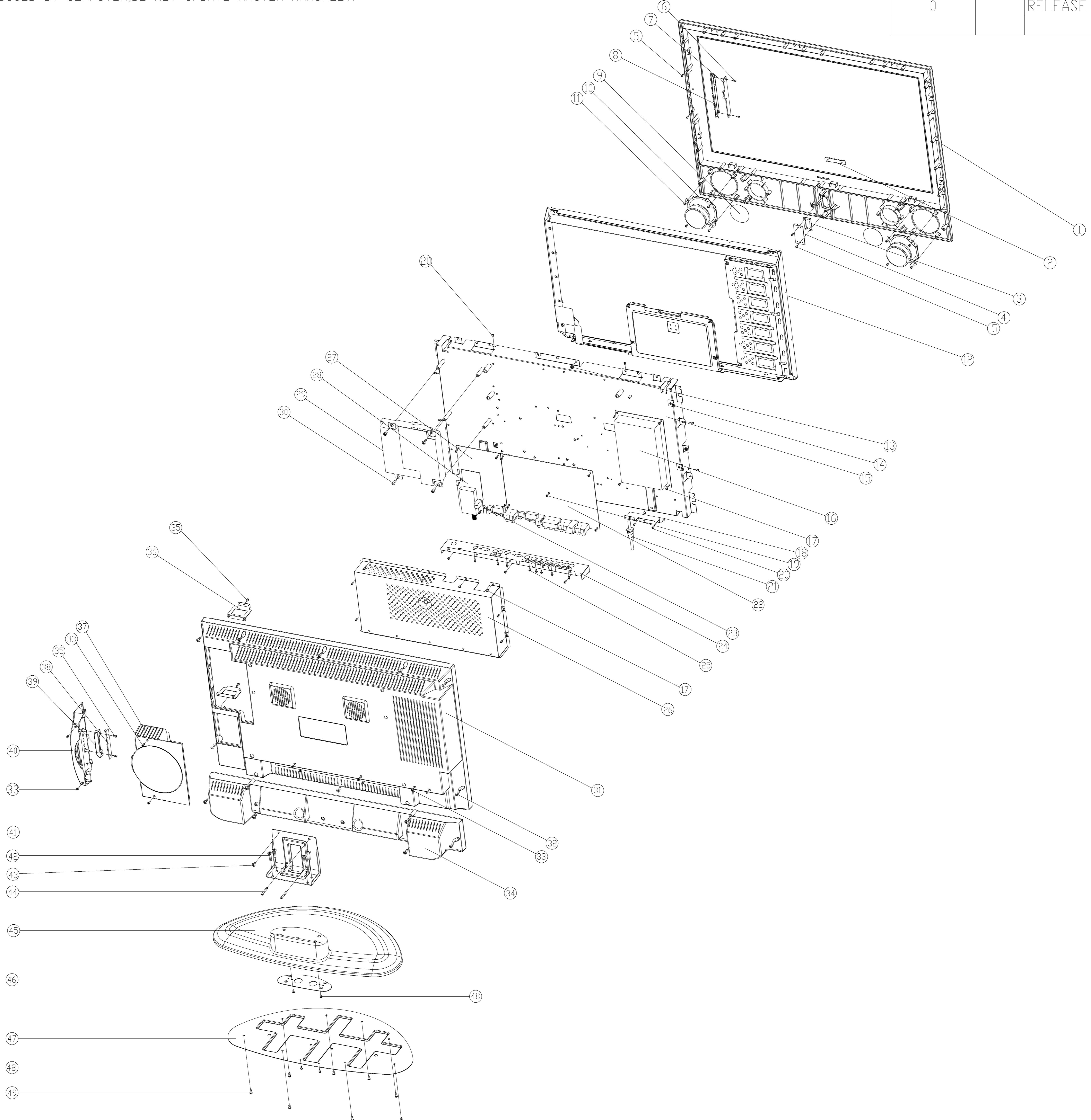


CHI MEI
ELECTRONICS CORP. 奇美電子股份有限公司
APPROVED: FWD NO: V270241012
2013/06/08 Jorge Chen
TECHNICAL CC_101 PART NO:
DRAWING Sheet 1/1 REV: Z UNIT: MM SCALE: 1:1
TITLE: OUTLINE V270B1 1/2
*CHI MEI COPYRIGHT 2004. ALL RIGHTS RESERVED. COPYING FORBIDDEN



NOTE : THIS RELEASED DRAWING WAS PRODUCED BY COMPUTER,DO NOT UPDATE MASTER MANUALLY.

DWG.Rev.	ZONE	DESCRIPTION	DATE	REVISOR
0		RELEASE	15-May-06	tzl



ITEM	PART No.	DESCRIPTION	QTY
49	614-400412-10	S-TAP. SCREW BID 4X12	6
48	614-300108-10	S-TAP. SCREW BID 3X8	4
47	449-27LA01-01	METAL PLATE FOR STAND BASE	1
46	423-27LA08-01	MIDDLE PLATE	1
45	230-26LA11-02RV	STAND COVER	1
44	614-400440-00	S-TAP. SCREW BID 4X40	2
43	604-407010-00	MACH. SCREW BID M4X10	2
42	604-508022-00	MACH. SCREW BID M5X22	4
41	423-27LA0A-01	STAND SUPPORT PLATE	1
40	258-L27AD11-01RV	DVD FUNCTION KNOB COVER	1
39	277-L27AD11-01S	DVD FUNCTION KNOB	1
38	771KL27AD02-01	DVD KEY PCB ASSY	1
37	236-L27AD11-01RV	DVD COVER	1
36	429-L27AD02-01S	DVD COVER BRACKET	2
35	614-300108-10	S-TAP. SCREW BID 3X8	4
34	206-L27AD11-01RV	SPEAKER BACK CABINET	1
33	601-3005008-00	MACH. SCREW CTS M3X8	10
32	614-400416-00	S-TAP. SCREW BID 4X16	12
31	202-L27AD31-01AV	BACK CABINET	1
30	609-L32AD01-01	SPECIAL MACH. SCREW M4X15	4
29	E7802-005006	DVD ASSY	1
28	771L37AD01-01	NTSC TUNER PCB ASSY	1
27	771S42D102-01	ATSC TUNER PCB ASSY	1
26	483-L27AD01-01S	SHIELD COVER -MAIN PCB	1
25	610-300210-10	S-TAP. SCREW BID 3X10	8
24	436-L27AD08-01S	TERMINAL SHEET	1
23	649-42AA02-01	CONNECTION BOSS	2
22	771EL27AD04-01	MAIN PCB ASSY	1
21	E3404-157009	POWER CABLE	1
20	602-305004-10	MACH. SCREW BID M3X4	8
19	604-305005-10	MACH. SCREW BID M3X5	8
18	426-L27AD03-01S	POWER CABLE BRACKET	1
17	604-305005-10	MACH. SCREW BID M3X5	17
16	E7801-P02001	PCB ASSY PSU BOARD MEGMEET MT168	1
15	428-27LA0C-01S	PANEL BRACKET FOR MT8202 W DVD	1
14	424-L27AD01-01S	POWER PCB BRACKET	2
13	614-400412-10	S-TAP. SCREW BID 4X12	7
12	E6203-27CD02	LCD PANEL CM	1
11	614-300210-10	S-TAP. SCREW BID 3X10	8
10	E4801-124001	SPEAKER	2
9	389-L32AB01-01	PVC PLATE	2
8	277-L32AD11-01S	FUNCTION KEY LCT32AD SILVER	1
7	771KL27AD01-01	KEY PCB ASSY	1
6	614-220206-10	S-TAP. SCREW BID 2.2X6	2
5	614-260208-10	S-TAP. SCREW BID 2.6X8	4
4	771BL27AD01-01	IR RECEIVE PCB ASSY	1
3	269-42SD01-01L	REMOTE RECEIVE LENS	1
2	486-M32111-01	NAME PLATE	1
1	200-L27AD11-STD01AV	FRONT CABINET	1

DRAWN.					TOLERANCE UNLESS OTHERWISE SPECIFIED	KAWA ELECTRONIC R & D CENTRE	TITLE EXPLODE VIEW		
CHECKED					0 ~ 30 ± 0.10	MATL.	MODEL NO. LCT2701AD		
APPR'D.					>30~100 ± 0.20		PART NO. EXP-L27AD05-01		
					>100 ± 0.30		DWG. NO. L27ADEXP5		
3rd ANGLE PROJECTION					ANGULAR: $\pm 0.3^\circ$		SCALE 1:1		
					UNIT : mm	SHEET 1 OF 1			

Item	Part Number	Part Description	Usage / unit	Unit	Key/Spare
	LCT27ADNDA1CS-C01	AKAI LCD 27" COMBO (LCT2701AD) S-MT8202G CMO(V270B1-L01) AC120V USA BLACK			
1>	510-L27AD03-02AKA	CARTON BOX AKAI LCT2701AD (MTK-8202+CMO PANEL) K	1	Piece	K
2>	580-L27ADHS-TU14L	IB E FOR AKAI LCT2701AD USA CM DTV+DVD S-MT8202 (w/o power switch)	1	Piece	K
	580-L27ADHS-TU04L	IB E FOR AKAI LCT2701AD USA CM DTV+DVD S-MT8202 (w power switch)	1	Piece	K
3>	E7501-061001	REMOTE CONTROL K002 AKAI FOR MT8202 COMBO 60KEYS SIL/BLK	1	SET	K
4>	771EL27AD04-01	MAIN PCB ASSY S-MT8202G ATSC & DVD FOR 27LCD CMO	1	SET	K
5>	771L37AD01-01	NTSC TUNER PCB ASSY FOR LCD37	1	SET	K
6>	771S42D102-01	ATSC TUNER PCB ASSY (MT5111CE)	1	SET	K
7>	200-L27AD11-STD01AV	CABINET FRONT SILVER/BLACK AKAI LCT2701TD MT8205 A	1	Piece	S
8>	202-L27AD41-01AV	BACK CABINET W/DVD W/O CARD READER LCT27AD	1	Piece	S
9>	206-L27AD11-01RV	SPEAKER CABINET AKAI LCT2701TD(MT8205) R	1	Piece	S
10>	236-L27AD11-01RV	DVD COVER BLACK LCT2701TD R	1	Piece	S
11>	258-L27AD11-01RV	DVD FUNCTION KNOB BLACK COVER LCT2701TD R	1	Piece	S
12>	269-42SD01-01L	REMOTE RECEIVE LENS	1	Piece	S
13>	277-L27AD11-01S	DVD FUNCTION KNOB BLK LCT2701TD S	1	Piece	S
14>	277-L32AD11-01S	FUNCTION KEY SIL(MATERIAL:BLACK) LCT32SD	1	Piece	S
15>	300-L27AD05-02C	POLYFOAM BOTTOM	1	Piece	S
16>	300-L27AD06-02C	POLYFOAM TOP	1	Piece	S
17>	310-041204-01V	POLYBAG 4"X12"X0.04 AV	1	Piece	S
18>	310-111404-07V	POLYBAG 11"X14"X0.04 FV	1	Piece	S
19>	310-383550-07V	POLYBAG LAMIFILM 38"X35"X0.5MM	1	Piece	S
20>	370-42D102-01	PAD CORD SPONG FOR SPK	1	Piece	S
21>	384-L32AB01-04AHA	PVC SHEET FOR TERMINAL (MTK-8202) W/DVD	1	Piece	S
22>	387-L27AD01-02AHA	MODEL PLATE AKAI LCT2701AD (MTK8202+CMO PANEL) H	1	Piece	S
23>	389-L32AB01-01	PVC SHEET L32AB	2	Piece	S
24>	426-L27AD03-01S	POWER CABLE BRACKET W/O SWITCH LCT27AD	1	Piece	S
25>	436-L27AD08-01S	TERMINAL SHEET MT8202 W/DVD ONLY COMPONET 1	1	Piece	S
26>	483-L27AD01-01S	SHIELD COVER-MAIN PCB	1	Piece	S
27>	486-M32111-01	NAME PLATE M AKAI	1	Piece	S
28>	522-421D01-01	MASKING PAPER	1	Piece	S

Item	Part Number	Part Description	Usage / unit	Unit	Key/Spare
29>	530-080032-10	FBP WHR 3.2X8.0X1.0	1	Piece	S
30>	530-120045-05	FIBER WASHER 12X4.5X0.5MM	4	Piece	S
31>	563-119-	SERIAL NO. LABEL	1	Piece	S
32>	568-P46T02-02	WARNING LB ENG 42SF NIL	1	Piece	S
33>	579-42D102-09	SERIAL NO/BAR CODE LABEL 42D1	1	Piece	S
34>	579-42D105-01	PROTECTIVE EARTH LABEL FOR ESA 42TD1	1	Piece	S
35>	579-L27AD02-03APA	UPC LABEL OF C/B AKAI LCT2701AD P	2	Piece	S
36>	579-L27AD09-01	CAUTION LABEL ENG AKAI	1	Piece	S
37>	579-L32AD03-02	CLASS I LASER PRODUCT LOGO	1	Piece	S
38>	579-L32AD04-01	LASER WARNING LABEL AKAI LC32AD	1	Piece	S
39>	590-L27AD01-05	WARRANTY CARD AKAI LCT2701AD	1	Piece	S
40>	593-L27AD01-06	INSERTION CARD AKAI LCT2701AD	1	Piece	S
41>	E3404-157009	AC CORD UL 1.88M FOR LCD32 MT8202	1	Piece	S
42>	E3421-925118	WIRE ASSY 8P2.5/7P2.0 L170MM 5V 12V SIGNAL POWER MT8202	1	Piece	S
43>	E3421-925119	WIRE ASSY P2.5 11P/11P L400MM 5V SIGNAL POWER MT8202	1	Piece	S
44>	E3421-925127	WIRE ASSY TJC3-2Y L860 SPK-R MT8202	1	Piece	S
45>	E3421-925133	WIRE ASSY TJC3-3Y L650 SPK-L MT8202	1	Piece	S
46>	E3421-926119	WIRE ASSY P2.0 8P L=215 TV/SIF	1	Piece	S
47>	E3421-926125	WIRE ASSY P2.5 4P/4P L400MM AMP24V EMI MT8202	1	Piece	S
48>	E3461-064036	WIRE ASSY INVERTER 12P2.0+8P2.5+3P2.0 L450MM L650MM MT8202	1	Piece	S
49>	E3461-064037	WIRE ASSY P2.5 10P/10P+4P/2.0 L400MM L680MM 12V 9V MT8202	1	Piece	S
50>	E3461-064038	WIRE ASSY P2.5 7P/7P L400MM 5V STANBY POWER MT8202 FOR 27"/32" LCD	1	Piece	S
51>	E3461-064039	WIRE ASSY 5P2.5 L560MM 5V 3.3V SIGNAL WIRE EMI MT8202	1	Piece	S
52>	E3471-000044	WIRE WS SHIELD WIRE FOR 32LCD COMBO MICO KEY 13P/8P+5P	1	Piece	S
53>	E3471-000048	WIRE WS SHIELD WIRE FOR 32LCD TV+COMBO KEY WIRE FOR DVD	1	Piece	S
54>	E3471-000057	WIRE WS SHIELD WIRE 27" L300MM MICO CMO MT8202 LVDS NEW	1	Piece	S
55>	E3471-002005	WIRE WS SHIELD 6P2.0/+2P2.5+8P2.0 COMBO DVD SIGNAL WIRE MT8202	1	Piece	S
56>	E3471-002006	WIRE WS SHIELD WIRE 27LCD TV+COMBO DVD SIGNAL WIRE MT8202	1	Piece	S
57>	E4801-124001	SPEAKER 8 OHM 10W D3" YD78-1	2	Piece	S
58>	E6203-27CD02	DISPLAY LCD 27" CMO V270B1-L01 1366X768 1000:1 HIGH CONTRAST	1	Piece	S

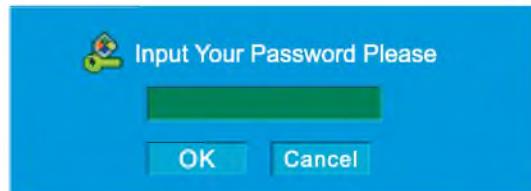
Item	Part Number	Part Description	Usage / unit	Unit	Key/Spare
59>	E7301-010002	BATTERY AAA R03P1.5V <2>	2	Piece	S
60>	E7801-D01001	DVD PCB ASSY MICO FOR MT8202	1	SET	S
61>	E7801-P02001	PCB ASSY PSU BOARD MEGMEET MT168 FOR 27LCD AC110-240V OUTPUT 12V/8V/24V 200W	1	SET	S
62>	734-L27AD03-01	ELLIPSE PLASTIC BASE ASSY W/O LOGO W/O PACKING SILVER	1	SET	S
63>	771BL27AD01-01	IR RECEIVE PCB ASSY FOR LCT27AD ATSC & DVD S-MT8202G	1	SET	S
64>	771KL27AD01-01	KEY PCB ASSY FOR TV S-MT8202G ATSC & DVD	1	SET	S
65>	771KL27AD02-01	KEY PCB ASSY FOR DVD LCT27AD ATSC & DVD S-MT8202G	1	SET	S

If you forget your V-Chip Password

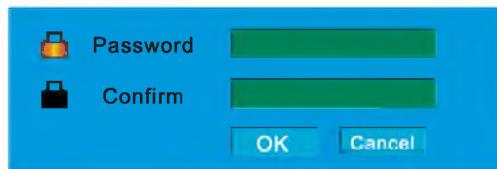
- Omnipotence V-Chip Password: 8202.

Using the "Change Password" item

- ① When enter the "V-Chip" menu, select "Change Password".
- ② Press ▲ or ▼ button to highlight the "Change Password" item.
- ③ Press **Enter** button to confirm and pop up a menu.



- ④ Use 0~9 buttons input the omnipotence password(8202), then Press **Enter** button to enter and pop up a menu.



- ⑤ Use 0~9 buttons input your new password.
- ⑥ Press ▼ button to move to confirm blank.
- ⑦ Use 0~9 buttons input your new password again.
- ⑧ Press **Enter** button to confirm

-Suggest: Change to your familiar Password again.

Software Upgrade

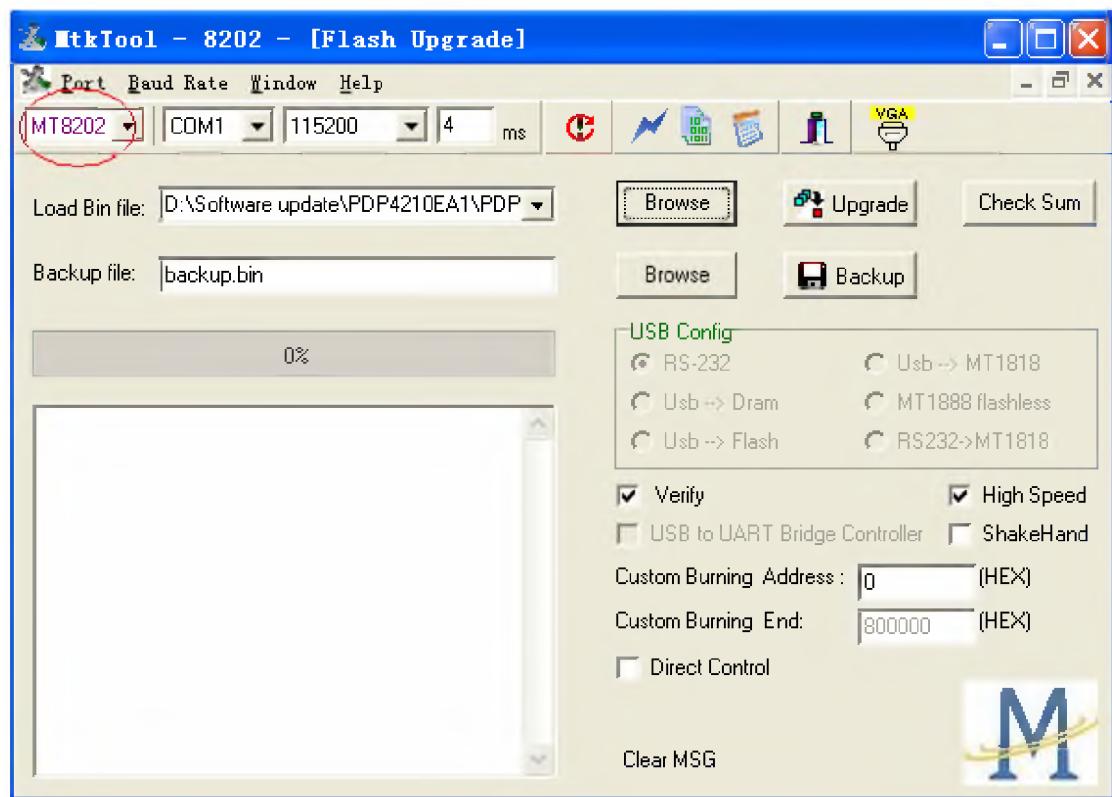
Process of update MT8202

Preparing :

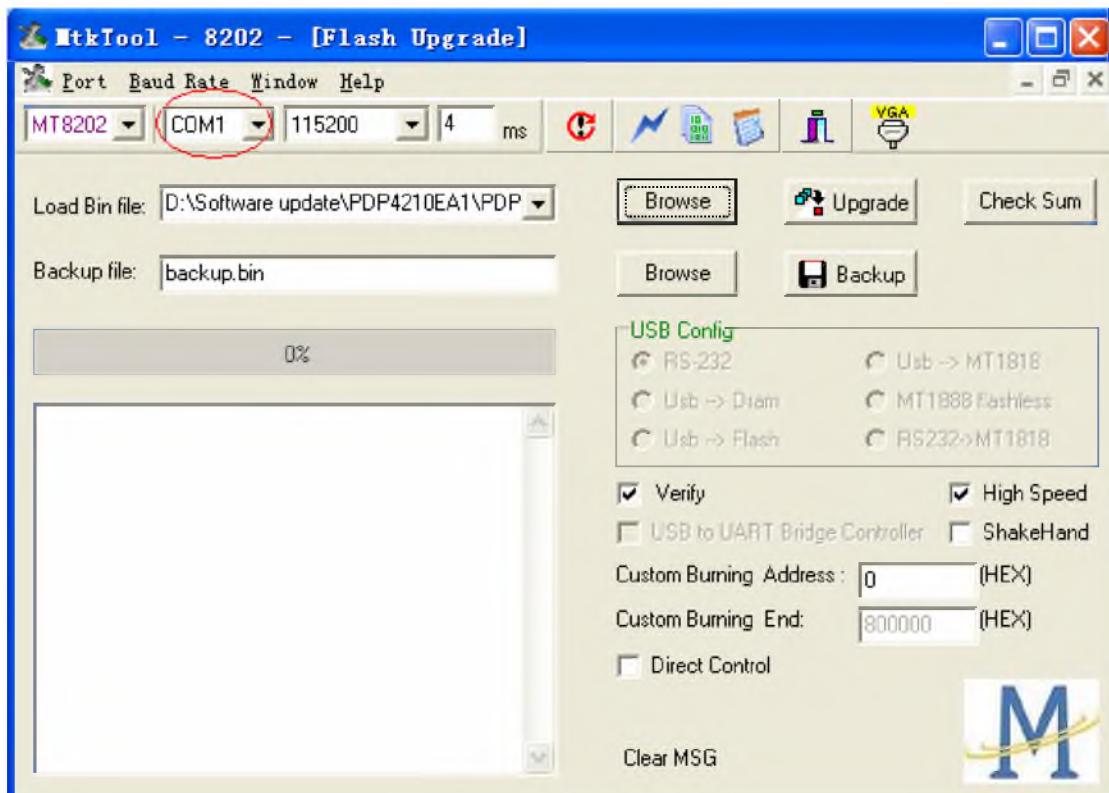
1. Connect **RS232-VGA download line**, One connector is connected to **VGA connect port of Plasma TV** ,while another side is connected to PC COM port.
2. Store the MtkTool into the PC .

Downloading :

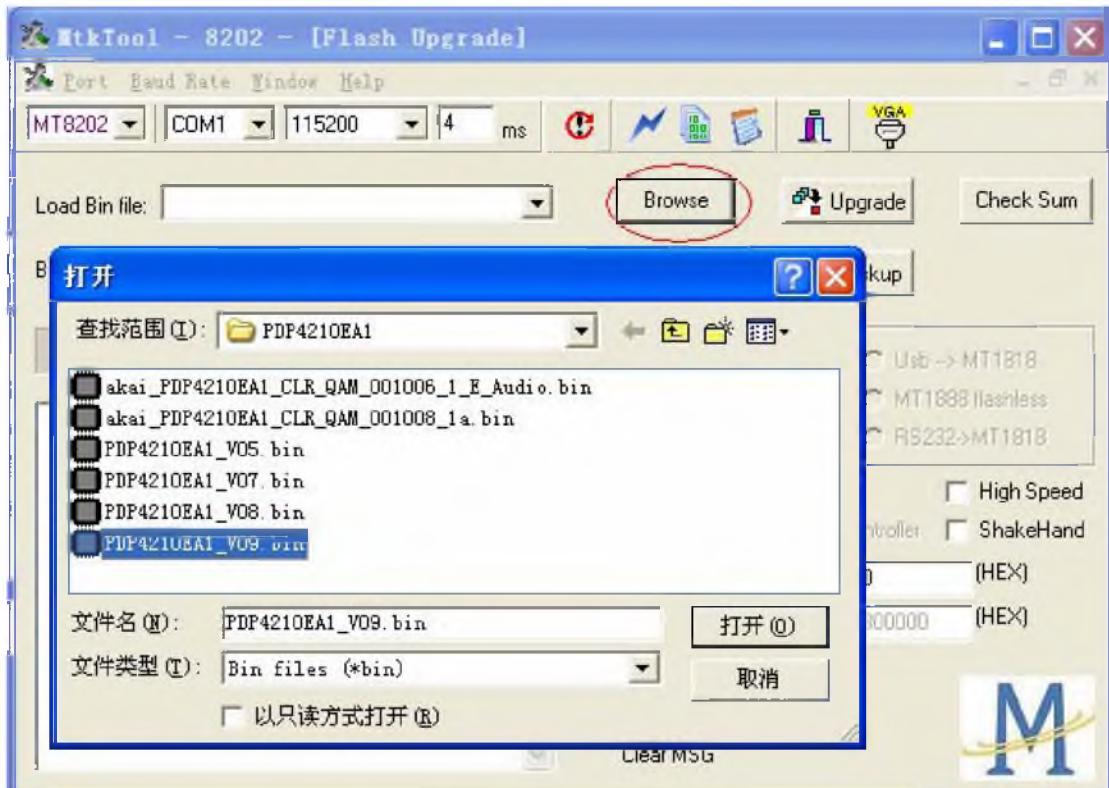
3. Turn on AC power switch of the Plasma TV and then press the button “standby” of the remote control . The image could be found on the screen of the Plasma TV while the color of the power indicator is green . (the mode of the Plasma TV will be standby mode if after turn on the main power switch only .)
4. Execute MTKtool and select the chipset as MT8202. (the software of MTKtool will be sent to your side)



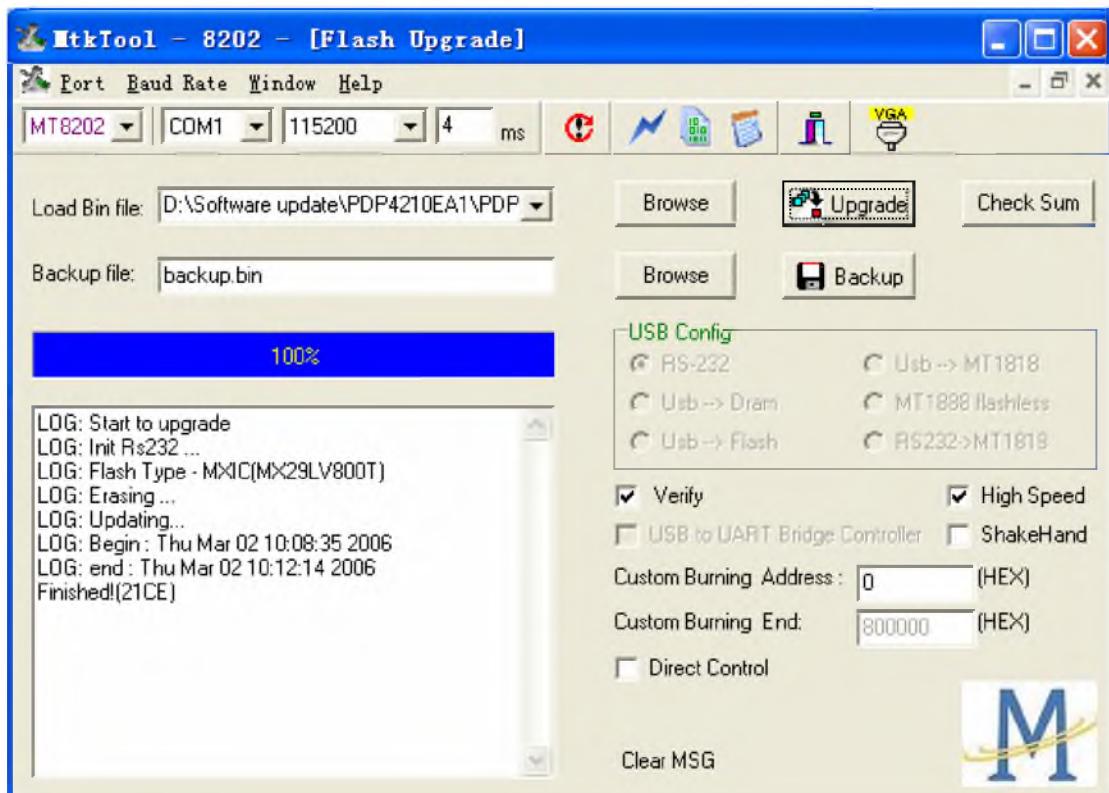
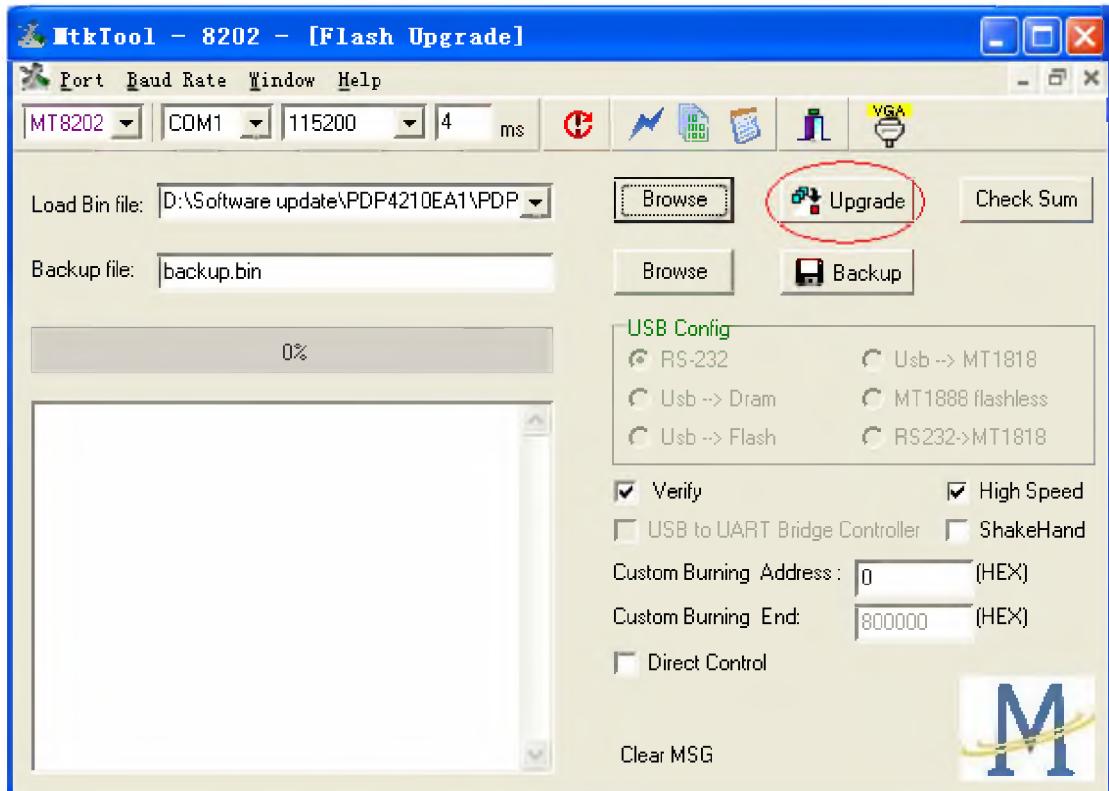
5. Select current COM port. (please try to check the COM port of your PC).



6. Choose the bit rate as 115200.
7. Select the update binary by pressing browse button. For example, the binary file name is PDP4210EA1_V09.bin. (this update firmware will be sent to your side)



8. Press Upgrade button and start update process.



9. The update process is successful as the progress bar is 100%. After the update process is ok,

turn off power and wait indicator light is off. Turn on power and TV can work.

Checking

It is needed to check the version of the firmware for MT8202 which has been download into the Plasma TV .

Press Menu button of the remote control, following input “8202” of the remote control and OSD menu for Factory Setting is appeared on the screen .

Use the remote control and select the mode of Firmware Version and then enter the mode of Firmware Version . It is easy to be found the version of the current firmware for MT8202 is as the following : “Factory ID : PDP4210EA1_VXX ”

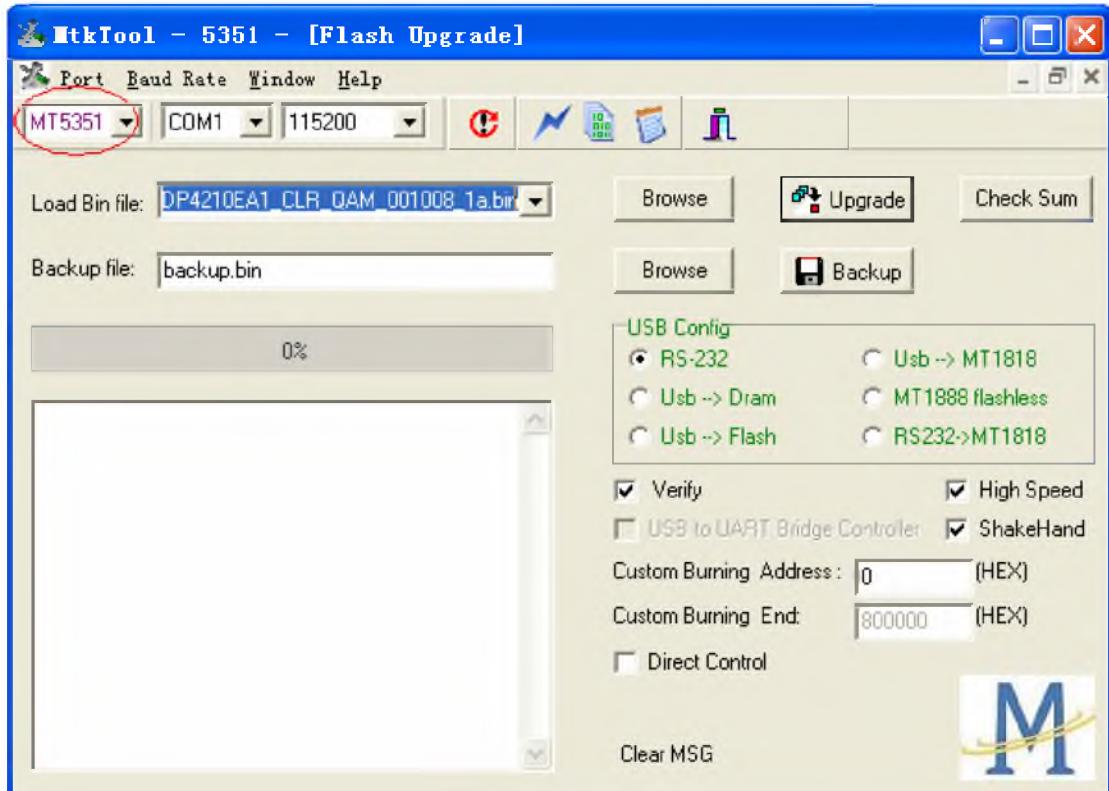
Process of update MT5351AG

Preparing :

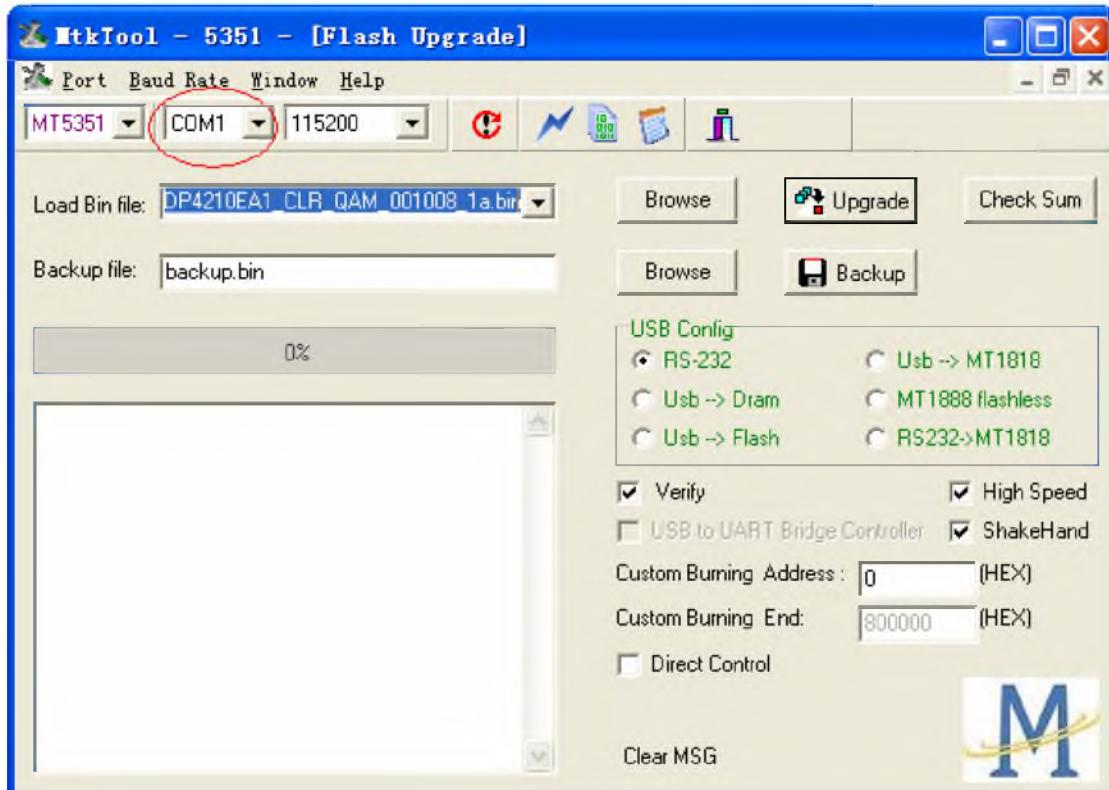
1. Connect **RS232 download line**, One connector is connected to **RS232 connect port of Plasma TV** , while another side is connected to PC COM port.
2. Store the MtkTool into the PC

Downloading :

3. Turn on AC power switch of the Plasma TV and then press the button “standby” of the remote control . The image could be found on the screen of the Plasma TV while the color of the power indicator is green . (the mode of the Plasma TV will be standby mode if after turn on the main power switch only .)
4. Execute MTKtool and select the chipset as MT5351AG. (the software of MTKtool will be sent to your side)

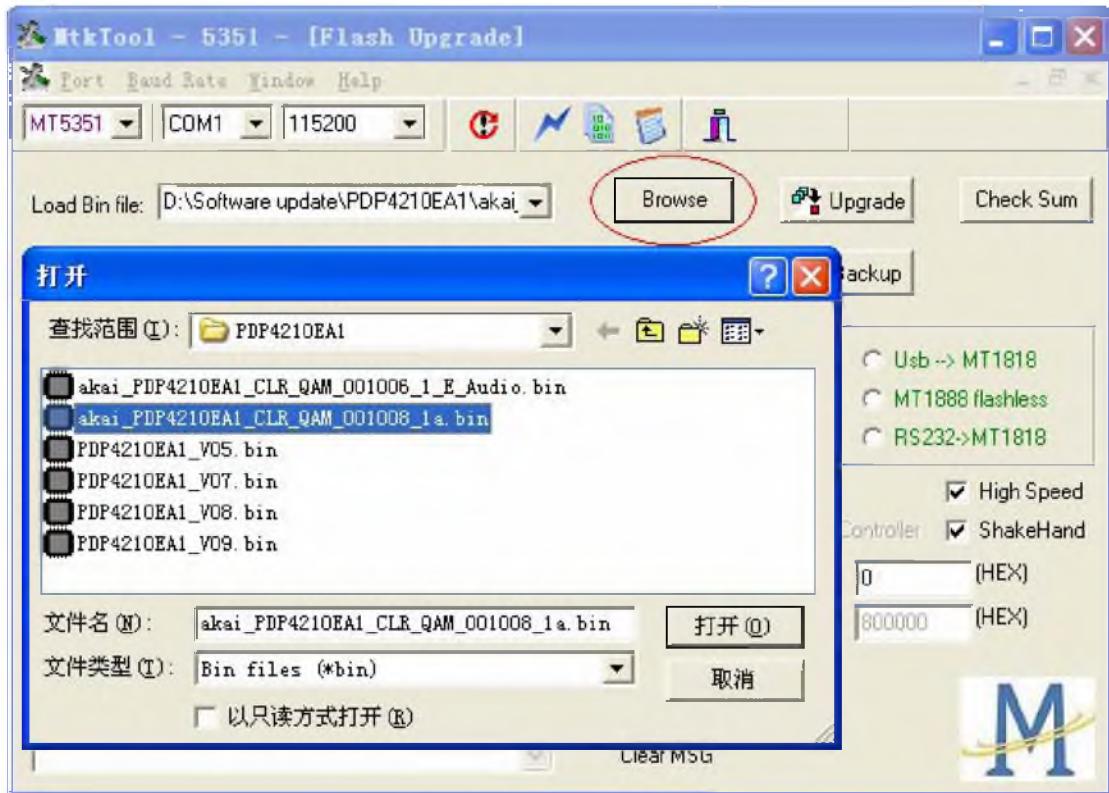


5. Select current COM port. (please try to check the COM port of your PC).

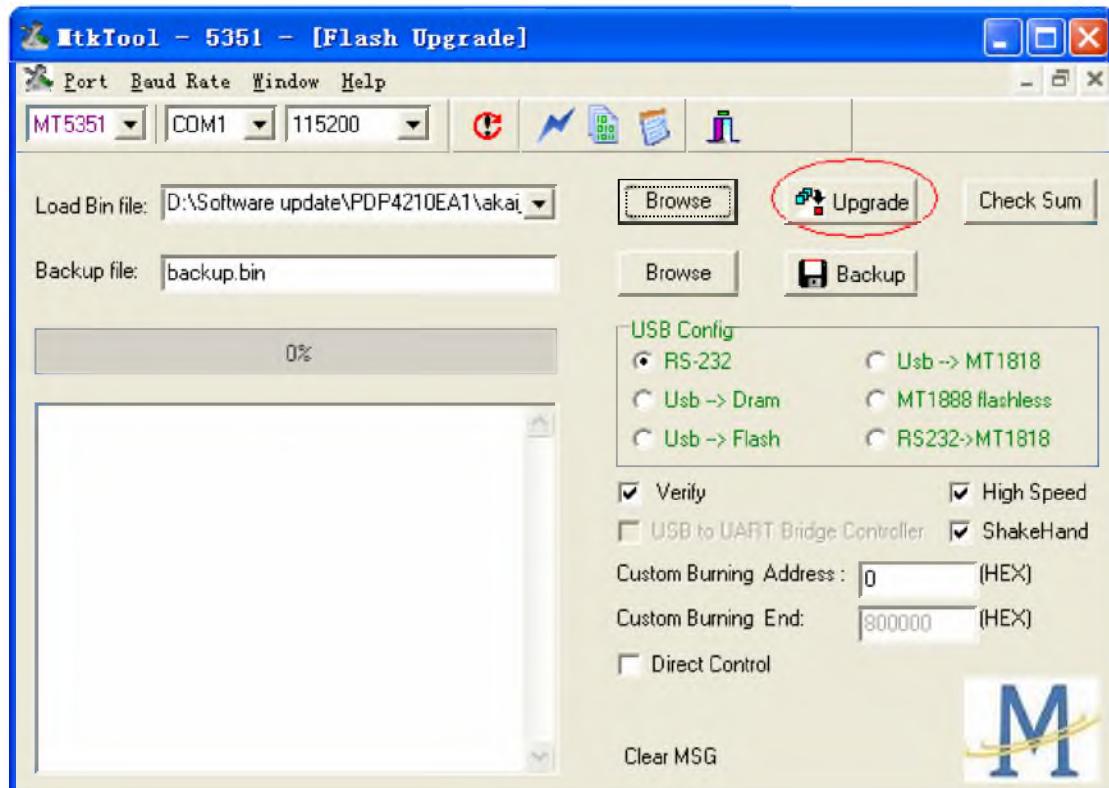


6. Choose the bit rate as 115200.
7. Select the update binary by pressing browse button. For example, the binary file name is

XXXX_PDP4210EA1_000000XX_X_P.bin. (this update firmware will be sent to your side)



8. Press Upgrade button and start update process.



9. The update process is successful as the progress bar is 100%. After the update process is ok, turn off power and wait indicator light is off. Turn on power and TV can work.

Checking :

It is needed to check the version of the firmware for MT5351AG which has been download into the Plasma TV .

Press Menu button of the remote control and the main OSD menu is appeared on the screen .

Use the remote control and select the DTV menu . following input “0000” (zero , zero , zero , zero) of the remote control .Then enter the mode of factory after input the digits .

It is easy to be found the version of the current firmware for MT5351AG is “PDP4210EA1 CLA_QAM_XXXXXX_XX”under the mode of factory .

