# 2246 1Y, $2 R$ and 2246 MOD A PORTABLE OSCILLOSCOPE 

 SERVICESN B700100 and ABOVE ( 2246 1Y 2R) SN B100100 and ABOVE (2246 MOD A)

# 2246 1Y, 2R and 2246 MOD A PORTABLE OSCILLOSCOPE 

## SERVICE

## SN B700100 and ABOVE (2246 1Y, 2R) SN B100100 and ABOVE (2246 MOD A)

The 2246 2R Is a rackmount version of the 2246 1Y. This service manual and the 2240F1R Instructlon sheet (Tektronlx part number 070-6332-00) are required to fully service the $22462 R$ oscIIloscope.

## WARNING

The following servicing instructions are for use by qualified service personnel only. To avoid personal injury, do not perform any servicing unless you are qualified to do so. Refer to the Safety Summary prior to performing any service.

Please check for CHANGE INFORMATION at the rear of thls manual.

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## INSTRUMENT SERIAL NUMBERS

Each instrument has a serial number on a panel insert, tag, or stamped on the chassis. The first two digits designate the country of manufacture. The last five digits of the serial number are unique to each instrument. The country of manufacture is identified as follows:

B000000 Tektronix, Inc., Beaverton, Oregon, U.S.A.<br>E200000 Tektronix United Kingdom, Ltd., Marlow<br>G100000 Tektronix Guernsey, Ltd., Channel Islands<br>HK00000 Hong Kong<br>H700000 Tektronix Holland, NV, Heerenveen, The Netherlands<br>JP00000 Sony/Tektronix, Japan

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## OPERATORS SAFETY SUMMARY

The safety information in this summary is for operating personnel. Warnings and cautions will also be found throughout the manual where they apply.

## Terms in this Manual

CAUTION statements identify conditions or practices that could result in damage to the equipment or other property.

WARNING statements identify conditions or practices that could result in personal injury or loss of life.

## Terms as Marked on Equipment

CAUTION indicates a personal injury hazard not immediately accessible as one reads the markings, or a hazard to property, including the equipment itself.

DANGER indicates a personal injury hazard immediately accessible as one reads the marking.

## Symbols in this Manual

This symbol indicates where applicable cautionary or other information is to be found. For maximum input voltage see Table 1-1.

## Symbols as Marked on Equipment



DANGER-High voltage.

Protective ground (earth) terminal.
$\therefore$ ATTENTION-Refer to manual.

## Power Source

This product is intended to operate from a power source that does not apply more than 250 V rms between the supply conductors or between either supply conductor and ground. A protective ground connection, by way of the grounding conductor in the power cord, is essential for safe operation.

## Grounding the Product

This product is grounded through the grounding conductor of the power cord. To avoid electrical shock, plug the power cord into a properly wired receptacle before making any connections to the product input or output terminals. A protective ground connection, by way of the grounding conductor in the power cord, is essential for safe operation.

## Danger Arising From Loss of Ground

Upon loss of the protective-ground connection, all accessible conductive parts, including knobs and controls that may appear to be insulating, can render an electric shock.

## Use the Proper Power Cord

Use only the power cord and connector specified for your product.

Use only a power cord that is in good condition.
For detailed information on power cords and connectors, see Figure 2-2.

## Use the Proper Fuse

To avoid fire hazard, use only a fuse of the correct type, voltage rating and current rating as specified in the parts list for your product.

## Do Not Operate in an Explosive Atmosphere

To avoid explosion, do not operate this instrument in an explosive atmosphere unless it has been specifically certified for such operation.

## Do Not Remove Covers or Panels

To avoid personal injury, do not remove the product covers or panels. Do not operate the product without the covers and panels properly installed.

## SERVICING SAFETY SUMMARY

## FOR QUALIFIED SERVICE PERSONNEL ONLY

Refer also to the preceding Operators Safety Summary

## Do Not Service Alone

Do not perform internal service or adjustment of this product unless another person capable of rendering first aid and resuscitation is present.

## Use Care When Servicing With Power On

Dangerous voltages exist at several points in this product. To avoid personal injury, do not touch exposed connections or components while power is on.

Disconnect power before removing protective panels, soldering, or replacing components.

## Power Source

This product is intended to operate from a power source that does not apply more than 250 volts rms between the supply conductors or between either supply conductor and ground. A protective ground connection by way of the grounding connector in the power cord is essential for safe operation.


## SPECIFICATION

## INTRODUCTION

The 2246 1Y and 2246 Mod A are 100 MHz , fourchannel, dual-sweep, portable oscilloscopes for general-purpose use. A microprocessor-based operating system controls most of the functions in each instrument, including a fully integrated menudriven voltage and time measurement system with SmartCursors ${ }^{(\pi) \pi}$. A menu-driven service mode provides for configuring of certain menu and readout displays, internal calibration, and servicing diagnostics. The instruments include additional EMI shielding. (See Table 1-2, Environmental Characteristics, for more information.)

The vertical deflection system has four input channels. Two channels have 11 basic deflection factors from 2 mV to 5 V per division, and two channels have two basic deflection factors of 0.1 V and 0.5 V per division. Basic deflection factors can be extended with attenuator probes. VOLTS/DIV readouts are switched to display the correct vertical scale factors when properly coded probes are connected to the vertical input connectors.

The horizontal deflection system provides single, dual, or delayed sweeps from 0.5 s to 20 ns per division (delayed sweep, 5 ms to 20 ns per division). The trigger system provides stable triggering over the full bandwidth of the vertical deflection system.

Alphanumeric crt readouts of the vertical and horizontal scale factors are displayed at the bottom of the screen. On-screen vertical and horizontal cursors provide accurate voltage, time, frequency, and phase measurements; measurement values are displayed at the top of the crt.

The measurement features include voltage measurements for +Peak, -Peak, Peak-to-Peak, and average DC, or positionable cursors for measuring voltage difference, time difference, frequency, and phase. SmartCursors ${ }^{(\pi)}$ that visually track voltage measurements, trigger levels, and ground can be placed on displayed waveforms. Delay-time and delta-delay measurements for time, frequency, and phase are available in ALT and B Horizontal Modes.

## STANDARD ACCESSORIES

The following items are standard accessories shipped with the 22461 Y and 2246 Mod A instrument:

2 Probes, 10X, 2 meter, with accessories
1 Probe, $1 \times, 2$ meter, with accessories
1 Power cord
1 Power cord clamp
1 CRT implosion shield, blue plastic (installed)
1 Fuse, 2A, 250 V, slow-blow
1 Attaching accessory pouch
1 Accessory pouch, ziploc
1 Front cover
The following items are standard accessories shipped with the $22461 Y$ instrument:

2 Operators manuals
2 Service manuals
2 Reference guides
The following items are standard accessories shipped with the 2246 Mod A instrument:

1 Operators manual
1 Reference guide

## RECOMMENDED RECALIBRATION SCHEDULE

To ensure accurate measurements, check the performance of this instrument every 2000 hours of operation, or, if used infrequently, once each year. Replacement of components in the instrument may also necessitate readjustment of the affected circuits.

NOTE
The silver-oxide batteries used in the 22461 Y (instruments serial numbered B714999 and below) MUST BE REPLACED AT LEAST EVERY 12 MONTHS to ensure proper operation of the instrument.

## PERFORMANCE CONDITIONS

The electrical characteristics given in Table 1-1 are valid when the instrument has been adjusted at an ambient temperature between $+20^{\circ} \mathrm{C}$ and $+30^{\circ} \mathrm{C}$, has had a warm-up period of at least 20 minutes, and is operating at an ambient temperature between $-10^{\circ} \mathrm{C}$ and $+55^{\circ} \mathrm{C}$ (unless otherwise noted).

Items listed in the Performance Requirements column are verifiable qualitative or quantitative limits
that define the measurement capabilities of the instrument.

Environmental characteristics are given in Table 1-2. This instrument meets the requirements of MIL-T-28800D, for Type III, Class 3, Style D equipment, as described in this section.

Physical characteristics of the instrument are listed in Table 1-3.

Table 1-1
Electrical Characteristics

| Characteristics | Performance Requirements |
| :---: | :---: |
| VERTICAL DEFLECTION SYSTEM - CH 1 AND CH 2 |  |
| Deflection Factor Range | $2 \mathrm{mV} / \mathrm{div}$ to $5 \mathrm{~V} / \mathrm{div}$ in $1-2-5$ sequence. ${ }^{\text {a }}$ |
| Accuracy (includes ADD MODE and CH 2 INVERT) $15^{\circ} \mathrm{C} \text { to } 35^{\circ} \mathrm{C}$ | Within $\pm 2 \%$. |
| $\begin{aligned} & -10^{\circ} \mathrm{C} \text { to } 15^{\circ} \mathrm{C} \\ & \text { and } 35^{\circ} \mathrm{C} \text { to } 55^{\circ} \mathrm{C} \end{aligned}$ | Within $\pm 3 \% .^{\text {a }}$ |
| Variable Range | Increases deflection factor by at least 2.5:1. |
| Frequency Response (-3 dB bandwidth) $2 \mathrm{mV} / \mathrm{div}$ to $5 \mathrm{~V} / \mathrm{div}$ | Dc to 100 MHz (at the input BNC and with a 10 X probe). |
| AC Coupled Lower -3 dB Point <br> 1X Probe | 10 Hz or less. |
| 10X Probe | 1 Hz or less. |
| Step Response (5-division step) <br> Rise Time <br> $2 \mathrm{mV} / \mathrm{div}$ to $5 \mathrm{~V} / \mathrm{div}$ | 3.5 ns or less (calculated) ${ }^{\text {a }}$ |
| Aberrations <br> Positive-Going Step <br> $5 \mathrm{mV} / \mathrm{div}$ to $0.5 \mathrm{~V} / \mathrm{div}$ | +5\%, -5\%, 5\% p-p or less. |
| $1 \mathrm{~V} /$ div to $5 \mathrm{~V} / \mathrm{div}$ | +10\%, $-10 \%, 10 \% p-p$ or less. |
| Negative-Going Step | Add $2 \%$ to all positive-going step specifications. |
| Position Effect | Aberrations measured anywhere on screen do not exceed those measured at center screen by more than $1 \%$. |
| Temperature Effect | Add $0.15 \%$ per ${ }^{\circ} \mathrm{C}$ above or below $25^{\circ} \mathrm{C}$. |

[^0]Table 1-1 (cont)
Electrical Characteristics

| Characteristics | Performance Requirements |
| :---: | :---: |
| Delay Match ( CH 1 to CH 2 ) | Less than 400 ps difference. |
| Common Mode Rejection Ratio (CMRR) | At least $25: 1$ up to 10 MHz and at least $10: 1$ up to 100 MHz for signals of eight divisions or less with VOLTS/DIV VAR adjusted for best CMRR at 50 kHz . |
| Channel Isolation (attenuation of deselected channel) <br> $2 \mathrm{mV} /$ Div to $0.5 \mathrm{~V} / \mathrm{Div}$ | 10 MHz 100 MHz <br> $50 \mathrm{~dB}(\approx 316: 1)$ <br> or more $34 \mathrm{~dB}(\approx 50: 1)$ <br> or more <br> Charnel isolation tested with eight-division input signal. |
| Trace Shift as VAR VOLTSIDIV is Turned | 1 division or less. |
| Invert Trace Shift | 1 division or less. |
| Trace Shift Between VOLTS/DIV Switch Positions | 0.2 division or less. |
| Trace Shift Between GND and DC input Coupling $-10^{\circ} \mathrm{C} \text { to } 35^{\circ} \mathrm{C}$ | Less than 0.5 mV . |
| $35^{\circ} \mathrm{C}$ to $55^{\circ} \mathrm{C}$ | Less than 2 mV . ${ }^{\text {a }}$ |
| Input Characteristics <br> Resistance | $1 \mathrm{M} \Omega \pm 2 \%{ }^{\text {a }}$ |
| Capacitance | $20 \mathrm{pF} \pm 2 \mathrm{pF}$. |
| Capacitance Match Between Any Two VOLTS/DIV Settings | $\pm 0.5 \mathrm{pF}$. |
| Maximum Input Volts $\square$ 4 | $400 \vee(d c+p e a k ~ a c) ; 800 \vee p-p$ at 10 kHz or less; $800 \vee \mathrm{p}-\mathrm{p}$ at 10 kHz or less; 5 V (dc + peak ac$)$ at 100 MHz or less. ${ }^{\text {a }}$ |

[^1]Table 1-1 (cont)
Electrical Characteristics

| Characteristics | Performance Requirements |
| :---: | :---: |
| VERTICAL DEFLECTION SYSTEM - CH 3 AND CH 4 |  |
| Deflection Factor |  |
| Range | 0.1 $V$ per division and $0.5 \vee$ per division. ${ }^{\text {a }}$ |
| Accuracy |  |
| $15^{\circ} \mathrm{C}$ to $35^{\circ} \mathrm{C}$ | Within $\pm 2 \%$. |
| $-10^{\circ} \mathrm{C}$ to $55^{\circ} \mathrm{C}$ | Within $\pm 3 \%$. |
| Frequency Response ( -3 dB bandwidth) | Dc to 100 MHz (at the input BNC and with a $10 \times$ probe). |
| Step Response (5-division step) Rise Time | 3.5 ns or less (calculated). ${ }^{\text {a }}$ |
| Delay Match ( CH 3 to CH 4 ) | Less than 400 ps difference. |
| Trace Shift Between VOLTS/DIV Settings | 1 division or less. |
| Channel Isolation (attenuation of deselected channel) | 34 dB or more at 100 MHz . <br> Channel isolation tested with eight-division input signa!. |
| Input Characteristics <br> Resistance | $1 \mathrm{M} \Omega \pm 2 \%$. ${ }^{\text {a }}$ |
| Capacitance | $20 \mathrm{pF} \pm 2 \mathrm{pF} .^{\text {a }}$ |
| Maximum Input Volts 4 | 400 V (dc + peak ac); $800 \mathrm{Vp-p}$ at 10 kHz or less; $800 \mathrm{Vp}-\mathrm{p}$ at 10 kHz or less; 5 V (dc + peak ac) at 100 MHz or less. ${ }^{\text {a }}$ |

[^2]Table 1-1 (cont)

## Electrical Characteristics

| Characteristics | Performance Requirements |
| :--- | :--- |
|  | VERTICAL DEFLECTION SYSTEM - ALL CHANNELS |
| Bandwidth Limit <br> $(-3 \mathrm{~dB}$ bandwidth $)$ | $20 \mathrm{MHz} \pm 25 \%$. |
| Low Frequency Linearity <br> (Relative to <br> center screen) | Within $\pm 5 \%$. <br> Linearity is measured by positioning a two-division test signal <br> anywhere on screen and noting the amplitude change. |
| Position Range | At least $\pm 10$ divisions form graticule center. |
| TRACE SEP Control <br> Position Range | At least $\pm 4$ divisions. |
| CHOP Mode Clock Rate | $625 \mathrm{kHz} \pm 10 \%$ a |
| Delay Match (CH 1 or CH 2 <br> to CH 3 or CH 4) | Less than 800 ps difference. |

HORIZONTAL DEFLECTION SYSTEM


Sweep Accuracy applies over the center eight divisions. Excludes the first $1 / 4$ division or 25 ns from the start of the magnified sweep and anything beyond the 100th magnified division.

[^3]Table 1-1 (cont)

## Electrical Characteristics

| Characteristics | Performance Requirements |
| :---: | :---: |
| Sweep Linearity | $\pm 5 \%$ <br> Sweep Linearity applies over the center eight divisions of an unmagnified sweep. Excludes the first $1 / 4$ division or 25 ns from the start of the sweep. <br> Sweep linearity is determined by the error of a single time mark relative to two adjacent time marks spaced at one time mark per division. |
| POSITION Control Range Normal Displays | Able to move the start of the sweep to the right of the center vertical graticule; able to move a time mark corresponding to the end of the tenth division of an unmagnified sweep to the left of the center graticule. |
| X-Y Displays | At least $\pm 13$ divisions. ${ }^{\text {a }}$ |
| X10 Magnifier <br> Registration (X10 to X1) | Expands the normal sweep by ten times around that portion of the sweep positioned at the center vertical graticule line. ${ }^{\text {a }}$ <br> 2 divisions or less shift. |
| Variable Control Range | Continuously variable between calibrated SEC/DIV settings. Extends both the $A$ and $B$ sweep time per division by at least a factor of 2.5 . |
| Sweep Length | Greater than 10 divisions. |
| Delay Time <br> Delay Control Range | Less than 0.1 division to 10 times the A SEC/DIV switch setting. Maximum value does not exceed end of the A sweep. |
| Jitter | 1 part in 20,000, or less, peak-to-peak, during a twosecond time interval. |
| Delta Time <br> Delta Control Range | From 0.5 division or less to the right of setting of DELAY control to within 1.5 divisions of the end of the A sweep. |

[^4]Table 1-1 (cont)

## Electrical Characteristics

| Characteristics | Performance Requirements |
| :---: | :---: |
| A AND B TRIGGER |  |
| Sensitivity-CH 1 through CH 4 : AUTO LEVEL, NORM AND SINGLE SEQUENCE | Trigger sensitivity is defined as the minimum peak-to-peak sine-wave trigger signal amplitude required to show the test signal with horizontal jitter of less than $3.0 \%$ of one period ( $p-p$ viewed over two seconds). |
| COUPLING <br> DC | 0.35 division from dc to 25 MHz , increasing to 1.0 division at 100 MHz . |
| NOISE REJECT | 1.4 division from dc to 25 MHz ; increasing to 2.2 division at 100 MHz . 0.5 division or less will not trigger. |
| HF REJECT | 0.35 division from dc to 50 kHz ; attenuates signals above upper -3 dB cutoff frequency of 70 kHz . |
| LF REJECT | 0.35 division from 100 kHz to 25 MHz , increasing to 1.0 division at 100 MHz attenuates signals below the lower -3 dB cutoff frequency of 50 kHz . |
| AC | 0.35 division from 50 Hz to 25 MHz , increasing to 1.0 division at 100 MHz ; attenuates signals below the lower -3 dB cutoff frequency of 10 Hz . |
| TV LINE, TV FIELD | 0.5 division of composite sync will achieve a stable display. |
| AUTO LEVEL and AUTO MODE Trigger Low-Frequency Limit | 10 Hz . |
| LEVEL Control Range | $\pm 20$ divisions referred to the appropriate vertical input. <br> This range is sufficient to allow triggering at any point on a displayed waveform for all modes except "ADD". In ADD, the combined range of the two position controls exceeds the trigger level range, making it possible (though unlikely) to pull a signal on screen for display but fail to trigger to it due to insufficient trigger level range. |

Table 1-1 (cont) Electrical Characteristics

| Characteristics | Performance Requirements |
| :--- | :---: |
| TRIGGER LEVEL <br> READOUT Accuracy | $\pm(0.3 \%$ of reading $+10 \%$ of one vertical division $)$. |
| HOLDOFF Control Range | Increases A Sweep holdoff time by at least a factor of $2 .{ }^{\mathrm{a}}$ |

Specifications for functions with digital readout are valid only when the ambient temperature is within $\pm 10^{\circ} \mathrm{C}$ of the temperature at the time of the last SELF CAL. For maximum performance, a recent SELF CAL is recommended.

| VOLTMETER FUNCTIONS DC VOLTS Accuracy | $\pm(0.5 \%$ of reading $+2 \%$ of the VOLTS/DIV setting $+250 \mu \mathrm{~V})$. |
| :---: | :---: |
| Normal Mode Rejection Ratio | Greater than 50 dB at 50 or 60 Hz . |
| PLUS or MINUS Peak <br> Accuracy-Full Bandwidth <br> 25 Hz to 25 MHz | $\pm(2.0 \%$ of reading $+15 \%$ of one vertical division $+1 \mathrm{mV})$. |
| Greater Than 25 MHz to 100 MHz ( 90 MHz at $35^{\circ} \mathrm{C}$ to $55^{\circ} \mathrm{C}$ ) | $+0.5 \mathrm{~dB} /-3 \mathrm{~dB} \pm 1 \mathrm{mV}$. Follows the trigger system frequency response curve. |
| Accuracy-Bandwidth Limited ( 25 Hz to 10 MHz ) | $\pm(2.0 \%$ of reading $+10 \%$ of one vertical division $+0.3 \mathrm{mV})$. |
| Gated Region Minimum Width (when gated) | ( 0.2 division +50 ns ) or less. |

[^5]Table 1-1 (cont)
Electrical Characteristics

| Characteristics | Performance Requirements |
| :---: | :---: |
| PK-PK VOLTS |  |
| Accuracy - Full Bandwidth |  |
| 25 Hz to 25 MHz | $\pm(2.0 \%$ of reading $+15 \%$ of one vertical division $+1.5 \mathrm{mV})$. |
| Greater Than 25 MHz to 100 MHz ( 90 MHz at $35^{\circ} \mathrm{C}$ to $55^{\circ} \mathrm{C}$ ) | $+0.5 \mathrm{~dB} /-3 \mathrm{~dB} \pm 1.5 \mathrm{mV}$. Follows the trigger system frequency response curve. |
| Accuracy - Bandwidth Limited |  |
| 25 Hz to 10 MHz | $\pm(2.0 \%$ of reading $+10 \%$ of one vertical division $+0.5 \mathrm{mV})$. |
| Gated Region Minimum Width (when gated) | ( 0.2 division +50 ns ) or less. |
| CURSOR FUNCTIONS |  |
| $K$ SEC $\rightarrow$ (manually position cursors) |  |
| Accuracy |  |
| $0.5 \mathrm{~s} / \mathrm{div}$ to $0.1 \mu \mathrm{~s} / \mathrm{div}$ | $\pm(0.5 \%$ of reading $+2 \%$ of the SEC/DIV setting $)$. |
| $50 \mathrm{~ns} / \mathrm{div}$ to $20 \mathrm{~ns} / \mathrm{div}$ | $\pm(1.2 \%$ of reading $+2 \%$ of the SEC/DIV setting $)$. |
| $K-1 /$ SEC $\rightarrow$ (manually position cursors) |  |
| Accuracy | Readout calculated from $k$ SEC $\rightarrow$ cursor positions. |
| $K$ VOLTS $\rightarrow$ (manually position cursors) |  |
| Accuracy | $\pm(0.5 \%$ of reading $+2 \%$ of the VOLTS/DIV setting + high-frequency display errors). |
| $\rightarrow$ VOLTS $\rightarrow$ (manually position cursors) |  |
| Accuracy | $\pm(0.5 \%$ of reading $+2 \%$ of the VOLTS/DIV setting + high-frequency display errors). |

Table 1-1 (cont)
Electrical Characteristics

| Characteristics | Performance Requirements |
| :---: | :---: |
| $\leftarrow$ PHASE $\rightarrow$ (manually positioned cursors) <br> Accuracy | Readout calculated from $K \leftarrow$ SEC $\rightarrow$ cursor positions. |
| TRACK MEASUREMENT <br> Position Accuracy (Cursor position on waveform versus digitally displayed measurement value) | Within $\pm 0.05$ vertical division. |
| TRACK TRIG LEVEL <br> Position Accuracy (Cursor position on waveform versus digitally displayed measurement value) | Within $\pm 0.05$ vertical division. |
| TRACK GROUND <br> Position Accuracy (Cursor position on waveform versus baseline displayed with grounded input) | Within $\pm 0.05$ vertical division. |
| DELTA TIME FUNCTIONS DELTA TIME Accuracy |  |
| $\begin{aligned} & -10^{\circ} \mathrm{C} \text { to } 15^{\circ} \mathrm{C} \\ & \text { and } 35^{\circ} \mathrm{C} \text { to } 55^{\circ} \mathrm{C} \end{aligned}$ | $\pm(2.0 \%$ of reading $+1.0 \%$ of one division of the A Sweep). |
| DELTA 1/TIME Accuracy | Readouts calculated using DELTA TIME difference. |
| DELTA Phase Accuracy | Readouts calculated using DELTA TIME difference. |
| Delay Accuracy, A Sweep Trigger Point to start of B Sweep | $\pm(0.5 \%$ of reading $+5.0 \%$ of one division of the A Sweep +25 ns ). |

Table 1-1 (cont)
Electrical Characteristics

| Characteristics | Performance Requirements |
| :---: | :---: |
| X-Y OPERATION |  |
| Deflection Factors <br> Accuracy <br> Y Axis $15^{\circ} \mathrm{C}$ to $35^{\circ} \mathrm{C}$ | Same as Vertical deflection system with the VOLTS/DIV variable controls in calibrated detent position. ${ }^{\text {a }}$ <br> Within $\pm 3 \%$. |
| $\begin{aligned} & -10^{\circ} \mathrm{C} \text { to } 15^{\circ} \mathrm{C} \\ & \text { and } 35^{\circ} \mathrm{C} \text { to } 55^{\circ} \mathrm{C} \end{aligned}$ | Within $\pm 4 \% .{ }^{\text {a }}$ |
| $\times$ Axis <br> $15^{\circ} \mathrm{C}$ to $35^{\circ} \mathrm{C}$ | Within $\pm 3 \%$. |
| $-10^{\circ} \mathrm{C}$ to $15^{\circ} \mathrm{C}$ and $35^{\circ} \mathrm{C}$ to $55^{\circ} \mathrm{C}$ | Within $\pm 3 \% .^{\text {a }}$ |
| Horizontal (X-Axis) -3 dB Bandwidth | 3 MHz or more. |
| Phase Match (DC Coupled) | $\pm 3$ degrees from dc to 100 kHz . |

EXTERNAL Z-AXIS INPUT

| Active Region Lower Threshold (intensity decreases above this voltage) | +1.8 volts or less. |
| :---: | :---: |
| Signal Required to Modulate an A or B Trace of Normal Intensity | +3.8 volts or less (usable frequency: DC -10 MHz ). <br> External Z-Axis signal does not affect the readout or the intensified zone intensity. |
| Maximum Input Voltage $\triangle$ | $30 \mathrm{~V}(\mathrm{dc}+$ peak ac); $30 \mathrm{Vp-p}$ ac at 1 kHz or less. ${ }^{\text {a }}$ |
| Input Loading | Represents less than one LSTTL load. ${ }^{\text {a }}$ |

## PROBE ADJUST OUTPUT

| Overshoot (rising and falling edge) | $0.1 \%$ or less. |
| :--- | :--- |
| Output Voltage on PROBE <br> ADJUST Jack | $0.5 \mathrm{~V} \pm 1 \%$ into $1 \mathrm{M} \Omega$ load. |
| Repetition Rate | $1 \mathrm{kHz} \pm 25 \%$. |

[^6]Table 1-1 (cont)

## Electrical Characteristics

| Characteristics | Performance Requirements |
| :---: | :---: |
| FRONT PANEL SETUP MEMORY |  |
| Battery Type 2246 1Y, 2R SN B700100-B714999 | Two $1.5 \mathrm{~V}, 43 \mathrm{mAH}$, silver-oxide button cells in series. ${ }^{\text {a }}$ |
| SN B715000 and above | One, dry, 3.0 V, Lithium. ${ }^{\text {a }}$ |
| 2246 Mod A | One, dry, 3.0 V, Lithium. ${ }^{\text {a }}$ |
| Battery Life Silver-oxide | At least one year. ${ }^{\text {a }}$ |
| Lithium | At least five years. ${ }^{\text {a }}$ <br> WARNING - To avoid injury, have battery replaced by a qualified service person for proper handling and disposal of Lithium batteries. |
| POWER SOURCE |  |
| Line Voltage Range | 90 Vac to $250 \mathrm{Vac} .^{\text {a }}$ |
| Line Frequency | 48 Hz to $445 \mathrm{~Hz}{ }^{\text {a }}$ |
| Line Fuse | $2 \mathrm{~A}, 250 \mathrm{~V}$, slow blow. ${ }^{\text {a }}$ |
| Maximum Power Consumption | 100 Watts (155 VA). ${ }^{\text {a }}$ |
| CRT DISPLAY |  |
| Display Area | $8 \mathrm{by} 10 \mathrm{~cm} .^{\text {a }}$ |
| Geometry Vertical | $\pm 1 / 2$ minor ( 0.1 div ) at 8 by 8 cm centered area. $\pm 1$ minor ( 0.2 div ) at 8 by 10 cm centered area. |
| Horizontal | $\pm 1 / 2$ minor ( 0.1 div ) at $6 \mathrm{by}, 10 \mathrm{~cm}$ centered area. $\pm 1$ minor ( 0.2 div ) at 8 by 10 cm centered area. |
| Trace Rotation Range | Adequate to align trace with center horizontal graticule line. |
| Standard Phosphor | P31. ${ }^{\text {a }}$ |
| Y-Axis Orthogonality | 0.1 division or less, over eight vertical divisions. No adjustment. |
| Nominal Accelerating Voltage | 16 kV . ${ }^{\text {a }}$ |

[^7]

Figure 1-1. Maximum input voltage vs frequency derating curve for the $\mathrm{CH} 1, \mathrm{CH} 2, \mathrm{CH} 3$, or CH 4 input connectors.

Table 1-2
Environmental Characteristics

| Characteristics | Description |
| :---: | :---: |
| STANDARD INSTRUMENT |  |
| Environmental Requirements | Instrument meets or exceeds the environmental requirements of MIL-T-28800D for Type III, Class 3, Style D equipment, as described below. ${ }^{\text {a }}$ |
| Temperature <br> Operating | $-10^{\circ} \mathrm{C}$ to $+55^{\circ} \mathrm{C}\left(+14^{\circ} \mathrm{F}\right.$ to $\left.+131^{\circ} \mathrm{F}\right)$. |
| Non-operating | $-51^{\circ} \mathrm{C} \text { to }+71^{\circ} \mathrm{C}\left(-60^{\circ} \mathrm{F} \text { to }+160^{\circ} \mathrm{F}\right)$ <br> Tested to MIL-T-28800D paragraphs 4.5.5.1.3 and 4.5.5.1.4, except in $4.5 \cdot 5.1 .3$, steps 4 and $5\left(-10^{\circ} \mathrm{C}\right.$ operating test) are performed ahead of step $2\left(-51^{\circ} \mathrm{C}\right.$ non-operating test). Equipment shall remain off upon return to room ambient during step 6. Excessive condensation shall be removed before operating during step 7 . |
| Altitude |  |
| Operating | To $4,572 \mathrm{~m}(15,000 \mathrm{ft})$. Maximum operating temperature decreases $1^{\circ} \mathrm{C}$ per 1000 ft above 5000 ft . |
| Non-Operating | To $15,240 \mathrm{~m}(50,000 \mathrm{ft})$. <br> Exceeds requirements of MIL-T-28800D, paragraph 4.5.5.2. |
| Humidity (Operating and Non-operating) | Five cycles (120 hours) referenced to MIL-T-28800D paragraph 4.5.5.1.2.2, for type III, class 3 instruments. <br> Non-operating and operating at $95 \%,-0 \%$ to $+2 \%$ relative humidity. Operating at $+30^{\circ} \mathrm{C}$ and $+55^{\circ} \mathrm{C}$ for all modes of operation. Non-operating at $+30^{\circ} \mathrm{C}$ to $+60^{\circ} \mathrm{C}$. |
| Radiated and conducted Emission required per VDE 0871 | Meets Category B. |
| Electrostatic Discharge | Withstands discharge of up to 20 kV . Test performed with probe containing a 500 pF capacitor with $1 \mathrm{~K} \Omega$ series resistance charged to the test voltage. <br> Conforms to Tektronix Standard 062-2862-00. |

[^8]Table 1-2 (cont)
Environmental Characteristics

| Characteristics | Description |
| :---: | :---: |
| EMI | Meets the following requirements in accordance with MIL-T-28800D and MLL-STD-461C. <br> CE 01:Part 2, narrow band requirements up to 15 kHz . <br> CE 03:Part 4. <br> CS 01:Part 2. <br> CS 02:Part 2. <br> CS 06:Part 5, limited to 300 V . <br> RE 01:Parts 5 and 6, except measured at 12 inches. <br> RE 02: Part 2, full limits to 1 GHz . <br> RS 02: Part 2, part 1. <br> RS 02: Part 2, part II, limited to 5 A at 60 Hz . <br> RS 03: Part 2, limited to 1 GHz . |
| Vibration (operating) | 15 minutes along each of 3 major axes at a total displacement of 0.25 inch p-p ( 4 g at 55 Hz ) with frequency varied from 10 Hz to 55 Hz in 1 -minute sweeps. Hold from 10 minutes at 55 Hz in each of the three major axes. All major resonances must be above 55 Hz . <br> Meets requirements of MIL-T-28800D, paragraph 4.5.5.3.1. |
| Bench Handling Test (cabinet on and cabinet off) | Each edge lifted four inches and allowed to free fall onto a solid wooden bench surface. <br> Meets requirements of MiL-T-28800D, paragraph 4.5.5.4.3. |
| Transportation Packaged Vibration Test | Meets the limits of the National Safe Transit Association test procedure $1 \mathrm{~A}-\mathrm{B}-1$; excursion of 1 inch p-p at $4.63 \mathrm{~Hz}(1.1 \mathrm{~g})$ for 30 minutes on the bottom and 30 minutes on the side (for a total of 60 minutes). |
| Package Drop Test | Meets the limits of the National Safe Transit Association test procedure 1A-b-2; 10 drops of 36 inches. |

Table 1-3
Mechanical Characteristics

| Characteristics | Description |
| :---: | :---: |
| STANDARD INSTRUMENT |  |
| Weight |  |
| With Front Cover, Accessories, and Accessories Pouch (without manual) | $8.9 \mathrm{~kg} \mathrm{(19.6} \mathrm{lb)}$. |
| With Power Cord | 7.9 kg (17.4 lb). |
| Shipping Weight (Domestic) | 11.7 kg ( 25.8 lb ). |
| Overall Dimensions Height | See Figure 1-2, Dimensional drawing. |
| With Feet and Accessories Pouch (empty) | Approx. 176.5 mm ( 6.95 in ) . |
| Without Accessories Pouch | 164 mm (6.46 in). |
| Width (with handle) | 362 mm (14.26 in). |
| Depth |  |
| With Front Cover on | 445.3 mm (17.53 in) |
| With Handle Extended | 521 mm (20.51 in). |
| Cooling | Forced air circulation; no air filter. |
| Finish | Tek Blue, pebble-grain finish painted on aluminum cabinet. |
| Construction | Aluminum alloy chassis. Plastic-laminate front panel. |

Table 1-3 (cont)
Mechanical Characteristics

| Characteristics | Description |
| :---: | :---: |
| RACKMOUNT INSTRUMENT |  |
| Weight <br> With Power Cord | $10.0 \mathrm{~kg}(22.0 \mathrm{lb})$. |
| Shipping Weight <br> Domestic, includes manual | 14.2 kg (31.3 lb). |
| Overall Dimensions Height Overall | See Figure 1-3, Dimensional drawing. $168 \mathrm{~mm}(6.6 \mathrm{in}) .$ |
| Center of mounting rail to bottom of cabinet | 89 mm (3.5 in). |
| From cabinet top or bottom to respective front-panel mounting holes | 38 mm (1.5 in). |
| Between front-panel mounting holes | 102 mm (4.0 in). |
| Width |  |
| Overall | 483 mm (19.0 in). |
| Between mounting hole centers | 464 mm (18.3 in). |
| Between outer edges of mounting rails | 427 mm (16.8 in). |
| Between handle centers | 450 mm (17.7 in). |
| Depth <br> Overall | 516 mm (20.31 in). |
| Front panel to rear of mounting rail (inside) | 465 mm (18.3 in). |
| Front panel to rear of mounting rail (outside) | 472 mm (18.6 in). |
| Handles | 44 mm (1.73 in) |

Table 1-3 (cont)
Mechanical Characteristics

| Characteristics | Description |
| :---: | :---: |
| Required Clearance dimensions |  |
| Height | $\geq 178 \mathrm{~mm}$ (7 in). |
| Width | $\geq 448 \mathrm{~mm}$ (17-5/8 in). |
| Depth | $\geq 508 \mathrm{~mm}$ (20 in). |
| Cooling | Forced air circulation; no air filter. |
| Finish | Tek Blue, pebble-grain finish painted on aluminum cabinet. |
| Construction | Aluminum alloy chassis, front-panel frame, and rear support. Plastic-laminate front panel. Glass-laminate circuit boards. |



Figure 1-2. Dimensional outline drawing, standard cabinet.

all dimensions rounded
TO NEAREST TENTH


REAR VIEW

Figure 1-3. Dimensional outline drawing, rackmount cabinet.

## PREPARATION FOR USE

## SAFETY

This section tells how to prepare for and to proceed with the initial start-up of the TEKTRONIX 22461 Y or 2246 Mod A Oscilloscope.

Refer to the Safety Summary at the front of this manual for power source, grounding, and other safety considerations pertaining to the use of the instrument. Before connecting the oscilloscope to a power source, read this section and the Safety Summary.

## LINE VOLTAGE AND POWER CORD

The 22461 Y and 2246 Mod A operate on line voltages from 90 to 250 V with line frequencies ranging from 48 to 440 Hz . No line voltage selecting is necessary. The detachable power cord may have to be changed to match the power source outlet (see Figure 2-1).

The detachable three-wire power cord has a threecontact plug for connection to both the power source and the protective ground. The power cord is secured to the rear panel by a securing clamp. The protective ground contact on the plug connects (through the power cord protective grounding conductor) to the accessible metal parts of the instrument. For electrical-shock protection, insert this plug into a power-source outlet that has a properly grounded protective-ground contact.

Instruments are shipped with the required power cord as ordered by the customer (see Figure 2-1). Contact your Tektronix representative or local Tektronix Field Office for additional power-cord information.

| Plug Configuration | Option | Power Cord/ Plug Type | Line Voltage | Reference Standards ${ }^{\text {b }}$ |
| :---: | :---: | :---: | :---: | :---: |
|  | U.S. Std. | $\begin{aligned} & \text { U.S. } \\ & 120 \mathrm{~V} \end{aligned}$ | 120 V | $\begin{gathered} \text { ANSI C73.11 } \\ \text { NEMA 5-15-P } \\ \text { IEC } 83 \\ \text { UL. } 198.6 \end{gathered}$ |
|  | A1 | $\begin{aligned} & \text { EURO } \\ & 220 \mathrm{~V} \end{aligned}$ | 220 V | $\begin{aligned} & \operatorname{CEE}(7), \\ & \\|_{1} \mid V \text { V } V \\ & \operatorname{IEC} 83 \\ & \operatorname{IEC} 127 \end{aligned}$ |
|  | A2 | $\begin{gathered} \mathrm{UK}^{\mathrm{a}} \\ 240 \mathrm{~V} \end{gathered}$ | 240 V | $\begin{aligned} & \text { BS } 1363 \\ & \text { IEC } 83 \\ & \text { EEC } 127 \end{aligned}$ |
|  | A3 | $\begin{aligned} & \text { Australian } \\ & 240 \mathrm{~V} \end{aligned}$ | 240 V | $\begin{aligned} & \text { AS C112 } \\ & \text { IEC } 127 \end{aligned}$ |
|  | A4 | North American 240 V | 240 V | $\begin{gathered} \text { ANSI C73.20 } \\ \text { NEMA } 6-15-\mathrm{P} \\ \text { IEC } 83 \\ \text { UL } 198.6 \end{gathered}$ |
|  | A5 | Switzerland 220 V | 220 V | $\begin{gathered} \text { SEV } \\ \text { IEC } 127 \end{gathered}$ |
| ${ }^{\text {a }}$ A 6A, type $C$ fuse is also installed inside the plug of the Option A2 power cord. <br> b Reference Standards Abbreviations: <br> ANSI-American National Standards institute <br> AS-Standards Association of Australia <br> BS—British Standards Institution <br> CEE-International Commission on Rules for the Approval of Electrical Equipment <br> IEC-International Electrotechnical Commission <br> NEMA-National Electrical Manufacturer's Association <br> SEV-Schweizervischer Elektrotechnischer Verein <br> UL-Underwriters Laboratories Inc. |  |  |  |  |

Figure 2-1. Optional power cords.

## LINE FUSE



The instrument may be damaged if operated with the wrong type and rating line fuse installed.

To verify the proper value of the power-input fuse for the 2246 1Y or 2246 Mod A, use the following procedure.

1. Disconnect the ac-power source from the instrument.
2. Press in the fuse-holder cap with a straightbladed screwdriver, then release it with a slight counterclockwise rotation.
3. Pull cap (containing fuse) out of fuse holder.
4. Check that the fuse is of the type and rating as specified on the rear panel.
5. If the installed fuse is not of the correct type and rating, replace it with a proper fuse and reinstall the fuse-holder cap.

## INSTRUMENT COOLING

You must provide adequate airflow into the instrument to prevent possible damage from overheated components. Before turning on the power, make sure that ventilation holes on the bottom and right side of the cabinet are not blocked. After turning the instrument on, check that air is being exhausted from the right side ventilation holes.

## START-UP

When the power is turned on, the instrument performs a self-diagnostic routine. If the instrument
fails to come on and operate normally, the Trigger MODE LEDs may be flashing to indicate the circuit location of a start-up error. Also, under certain conditions, the Tektronix Part Number of the device where the error exists may be displayed on the screen. Refer to Troubleshooting in the Maintenance section of this manual for an explanation of the start-up error codes.

When the instrument is turned on, a self-cal routine may run to set the voltage- and timingmeasurement constants. During normal operation, the power-on self cal happens only if the stored constants have been lost as the result of a dead memory back-up battery. The instrument may be used with no reduction in the measurement accuracy by running the SELF CAL MEASUREMENTS routine from the SERVICE MENU after the instrument has warmed up for at least 20 minutes.

To run the SELF CAL MEASUREMENTS routine:

Press the top and bottom menu-item select buttons. Select INTERNAL SETTINGS MENU, then SELF CAL MEASUREMENTS. Press RUN to start the routine, then QUIT to return to the normal oscilloscope mode.

## DETAILED OPERATING INFORMATION

For operating information for specific instrument functions, refer to the 2246 1Y and $2246 \operatorname{Mod} A$ Operators Manual.

# THEORY OF OPERATION 

## SECTION ORGANIZATION

This section contains general and detailed descriptions of the 22461 Y and 2246 Mod A Oscilloscope circuitry. The Block Diagram Description describes the general operation of the instrument functional circuits. Each major circuit is explained in detail in the Detailed Circuit Description. Schematic and block diagrams show the circuit components and interconnections between parts of the circuitry. The circuit descriptions are arranged in the same order as the schematic diagrams.

The detailed block diagrams and the schematic diagrams are in the Diagrams section at the rear of this manual. Smaller functional diagrams are in this section near the associated text. The schematic diagram associated with each circuit description is identified in the text. For best understanding of the circuit being described, refer to the applicable schematic and functional block diagrams.

## INTEGRATED CIRCUIT DESCRIPTIONS

## Digital Logic Conventions

Digital logic circuits perform many functions within the instrument. Functions and operation of the logic
circuits are represented by logic symbology and terminology. Most logic functions are described using the positive-logic convention. Positive logic is a system where the more positive of two levels is the TRUE (or 1) state; the more negative level is the FALSE (or 0) state. In this logic description, the TRUE state is high, and the FALSE state is low. Voltages of a high or low state vary among individual devices. For specific device characteristics of common parts, refer to the manufacturer's data book.

## Hybrids

The Channel 1 and Channel 2 attenuators and input buffers are hybrid devices combining thick-film and semiconductor technologies. These devices are made with interconnected circuitry on a single ceramic carrier and have improved performance characteristics over a more discrete type circuit.

## Linear Devices

The operation of individual linear integrated circuit devices is described in this section using waveforms or graphic techniques when needed to illustrate their circuit action.

## BLOCK DIAGRAM DESCRIPTION

## INTRODUCTION

The Block Diagram Description gives an overview of the schematic circuit functions (see Figures 9-4, $9-5 a$, and $9-5 b$ ). It is provided as an aid in understanding the overall operation of the 22461 Y and 2246 Mod A Oscilloscope circuitry before individual circuits are discussed in detail. The Simplified Block Diagram illustration shows the basic interconnections for signal flow and control signals. Schematic diagram numbers that are referred to in the text are shown by a diamond symbol in each block of the figure.

## VERTICAL INPUTS (Diagram 1)

The signals for viewing or for triggering are applied to the CH 1 through CH 4 vertical input BNC connectors via coaxial cables or probes. Channels 1 and 2 have a choice of AC or DC input coupling or GND. Channels 3 and 4 have DC input coupling only. Scaling of the Channel 1 and Channel 2 input signals has a range of 2 mV per division to 5 V per division without the use of external attenuators. Channels 3 and 4 are limited to two input attenuator choices: 0.1 V per div and 0.5 V per div.

Scaling of the Channel 1 and Channel 2 signals is done by a series of switchable attenuators that provide either no attenuation, X10 attenuation, or X100 attenuation of the input signal. A low-impedance attenuator following an input signal buffer produces X1, X2, and X5 attenuation steps. Additional control of input signal scaling is provided by the selectable gain Vertical Preamplifiers (shown in Diagram 2).

Channel 3 and Channel 4 input signals are buffered by high input impedance FET amplifiers; no input attenuation of the signal is provided. The gain choices for Channel 3 and Channel 4 are selected by the choice of Vertical Preamplifier gain setting only.

The Measurement Processor controls the operation of much of the switchable circuitry of the 2246 1Y and 2246 Mod $A$ via a common shift register data line (SR DATA). Data bits loaded into the attenuator control and gain shift register (designated SRO) set the magnetic relay switches for the input coupling and attenuator settings and select the gain settings of the Preamplifiers.

## VERTICAL PREAMPS AND OUTPUT AMPLIFIER (Diagram 2)

Each vertical channel has identical selectable-gain Preamplifiers. The calibrated gain for each is manually set during adjustment. Enabling of the Preamplifiers to display a channel input signal is controlled by the SLIC Display Logic (U600, Diagram 4). Preamplifier gain settings are controlled by the Measurement Processor via control bits loaded into the attenuator control and gain shift register (Diagram 1). Vertical channel trigger signal outputs are produced by each of the Preamplifiers for triggering the sweep from the applied signal.

The vertical outputs of each preamplifier are connected to a summing node at the input to the DelayLine Driver. There, the signal current (from the enabled Preamplifiers) and the no-signal standing currents (from the disabled Preamplifiers) are added with the current from the position signal switching circuit.

The signal current for the enabled channel (vertical channel signal plus its position offset) or the readout position current (enabled to the summing node during text and cursor displays) is applied to the Delay-Line Driver. There, it is buffered and
compensated to drive the vertical delay line. The delay line produces enough delay in the signal to permit the trigger circuitry to start the sweep before the vertical signal arrives at the crt deflection plates, and the rising edge of the triggering signal may be viewed.

From the output of the delay line, the signals are applied to the Vertical Output integrated circuit. The Vertical Output IC (U701) has provisions for vertical BEAM FIND, bandwidth limiting, vertical gain, and readout display jitter. External filter elements on the Vertical Output IC produce the bandwidth limiting when switched into the amplifier circuitry. The output signal from U701 is then applied to the Vertical Output Amplifier where it gets its final boost in power to drive the vertical crt deflection plates.

An auxiliary Vertical Comparator circuit (U702 and Q703) is shown in Diagram 2. Its purpose is to measure the gains and offsets during SELF CAL to determine the vertical calibration constants needed for the measurements and tracking cursor displays.

## A AND B TRIGGER SYSTEM (Diagram 3)

The A and B Trigger System provides the circuitry for trigger source, slope, coupling, and bandwidth selection; trigger level comparison; tv trigger detection; and dc measurements of the measurement source signal.

Trigger selection signals from the Display Logic IC (U600, Diagram 4) drive the switching circuitry internal to U421 and U431. The signals select the correct trigger source, slope, and coupling choice for the present front-panel control setting. For VERT MODE triggering with more than one vertical channel displayed, the trigger source selection changes as each channel is displayed. When the ADD Vertical Mode is selected, a special amplifier arrangement in U421 (for A) or U431 (for B) sums the CH 1 and CH 2 signals to provide an ADD trigger signal for display of the ADD waveform.

The Trigger CPLG (coupling) selections are AC, DC, HF REJ (high-frequency reject), LF REJ (lowfrequency reject), and NOISE REJ. Of these, all but NOISE REJ coupling are produced by selecting a filter path with the necessary bandwidth characteristics. NOISE REJ coupling is done in the Trigger Level Comparator circuit by decreasing the sensitivity of the comparator.

When the trigger signal level crosses the comparator threshold set by the Trigger LEVEL and SLOPE control settings, the comparator output changes states. That state change is applied to the Trigger Logic IC (U602, Diagram 4). The Trigger Logic circuitry then produces the gating that starts the A or B Sweep as appropriate.

Separate A and B Trigger bandwidth limit circuits before the Trigger Level Comparators allow the flexibility that is needed for using the B Trigger circuitry as the measurement signal channel. Even when the B Trigger signal itself is bandwidth limited, full bandwidth is used for making measurements. Signals are measured by using the B Trigger Level Comparator as a successive-approximation analog-to-digital converter to determine the peaks or dc level of the applied signal. When making a measurement, the B Trigger Level signal is driven in a binary search by the Measurement Processor (via the DAC system, Diagram 9) while the output of the B Trigger Level Comparator is monitored. When the smallest resolution output of the DAC system causes the comparator output to change states, the Measurement Processor stops the search and uses the DAC input value at that point as the measured value of the applied signal.

Video signal processing to obtain either Field or Line triggering is done in the TV Trigger Detector. Peak detectors determine the negative or positive peaks of the applied video signal. Those levels set the voltage at the reference input of the video signal comparator at a level that strips off all the video information (when the slope selection is correct for the polarity of the applied signal). The remaining composite sync signal is applied directly to the trigger system for Line triggering. Field triggering is obtained by filtering the composite sync to obtain only the vertical sync pulse.

The operating modes of the Trigger circuitry are controlled by the Measurement Processor. Auxiliary Data Shift Register U1103 (the last device in shift register 1) is serially loaded with control bits from the SR DATA line by the SR1 TTL clock. The state (high or low) of the control bits select the bandwidth setting of the A and B Triggers, TV LINE or TV FIELD triggering for the A Trigger system, and either the TV FIELD signal or the average DC voltage of the measurement channel for the B Trigger system. Additional control bits output from the Auxiliary Data Shift Register are the MAG signal (X10 Magnification on or off), $\overline{X-Y}$ signal ( $X-Y$ or $Y-T$ displays), and the $\overline{\text { VERT COMP ENABLEsignal (when vertical SELF CAL }}$ is done).

The average dc voltage of a signal being measured is found by filtering all the ac signal components
from the measurement channel signal. That dc level is then applied to the B Trigger Level Comparator where its value is determined by successive approximation as described earlier.

## DISPLAY AND TRIGGER LOGIC AND PROCESSOR INTERFACE (Diagram 4)

Control of the display states and the trigger system is done by two special devices. The Display Logic IC (U600, also know as SLIC or slow-logic IC) controls activities that enable the vertical channels for display and select the A and B Trigger System operating states. The Trigger Logic IC (U602, also known as FLIC or fast-logic IC) monitors the A and B Trigger signals, the A and B SWP END signals, the DLY END 0 and DLY END 1 signals, and controlling signals from the Display Logic IC. It outputs the $A$ and $B$ GATE and the Z-Axis signals that start the sweeps and unblank the crt at the appropriate times.

Setup data to the internal registers of the two logic devices is sent from the Measurement Processor over the MB DATA line. A register is enabled for loading by the address that is latched on the ADDRO-ADDR3 lines (from Diagram 8). Data bits are written to U600 with the SLIC WR strobe, and to U602 with the FLIC WR strobe. The contents of the internal registers of the Display Logic IC may also be read by the Measurement Processor using the SLIC RD strobe.

The Processor Interface portion of Diagram 4 handles the serial communications between the serial shift registers and the Measurement Processor. This circuitry is the Measurement Processor's means of controlling the circuit hardware setups in response to a front panel control setting. Data controlling the state of the serial data bit to be loaded into the shift registers is placed on the ADDRO-ADDR2 bus lines. That address is decoded to produce either a high or a low that is latched on the SR DATA signal line. The appropriate shift register clock is then generated to load the latched bit. Each bit is loaded in succession until all the control bits of a shift register are loaded.

The purpose of shift register (U502) is to permit the Measurement Processor to read back the outputs of the shift registers for diagnostic purposes and the output of the Vertical Comparator during vertical SELF CAL. The last bit from shift register 0 and shift register 1 ( $\overline{\text { RO FREEZE }}$ and $\overline{\text { BW LIMIT }}$ respectively) and the Vertical Comparator (VERT COMP) state are loaded in parallel and serially shifted out onto the MB RETURN line to be read by the Measurement Processor.

## A AND B SWEEPS AND DELAY COMPARATORS (Diagram 5)

The A and B Sweep circuitry sets the timing and produces the $A$ and $B$ ramp signals to drive the crt horizontal deflection plates. The Measurement Processor sets the hardware states using control bits loaded into shift register 1. One register (U302) holds the bits for selecting the A Sweep timing resistors and capacitors and one register (U303) holds the B Sweep control bits. The timing resistors are selected by multiplexers (U307 and U308 for A Sweep timing; U310 and U311 for B Sweep timing) that are switched by the states of the control bits; timing capacitors are selected directly by the control bits.

The starting level of the sweeps is held steady by a Baseline Stabilizing circuit, and the sweep ends are determined by two Sweep-End Comparators. A and B GATE signals from the Trigger Logic IC (U602, Diagram 4) control the start of the sweep ramps. A constant charging current to the timing capacitors produces a linear voltage rise across the capacitors. That voltage is buffered by the A and B Sweep Buffers for application to the Horizontal Output Amplifier (Diagram 6).

The SEC/DIV VAR control, when out of the calibrated detent position, changes the charging current delivered to the sweep timing capacitors proportional to its rotation. Decreasing the current lengthens the ramp to decrease the sweep speed.

Two comparator circuits are used to check the A Sweep ramp amplitude against the Reference Delay and Delta Delay voltages. Both Delay End Comparator outputs are applied to the Trigger Logic IC (U602, Diagram 4). The Trigger Logic IC monitors the delays to determine when the $B$ Sweep may either run (for RUNS AFTER B Trigger Mode) or accept a B Trigger (for any of the triggered B Sweep modes).

## HORIZONTAL OUTPUT AMPLIFIER (Diagram 6)

Deflection signals applied to the Horizontal Preamplifier (U802) are the A Sweep Ramp, the B Sweep Ramp, the horizontal readout, and the X-Axis input signal for $\mathrm{X}-\mathrm{Y}$ displays. Mode control signals HDO and HD1 (from Display Logic IC U600 to the Horizontal Preamplifier) select the horizontal display mode (A Sweep, B Sweep, Readout, or X-Y display). Other control signals to the Horizontal Preamplifier are the MAG signal (for X10 magnification of the sweep), the BEAM FIND signal
(decreases horizontal gain), and the horizontal position signal for positioning the display. The $X-Y$ signal controlling U301B reduces the range of the Horizontal POSITION signal delivered to the Horizontal Preamplifier when in the $X-Y$ display mode.

Five manual adjustments are associated with the Horizontal Preamplifier. They are the X10 and X1 gain, the Readout gain, the $X$-Axis signal gain, and Mag Registration. Mag Registration compensates for offset between X10 and X1 gains, but it is primarily used to center the readout displays horizontally.

The active single-ended deflection signal input to the Horizontal Preamplifier is amplified and converted to a differential output signal. That signal is further amplified and compensated by the Horizontal Output Amplifier to drive the horizontal deflection plates of the crt. The final output amplifier consists of four MOSFET transistors (Q801, Q802, Q805, and Q806). Two transistors are used for each deflection plate (left and right) to divide the power handling requirements.

## Z-AXIS, CRT, PROBE ADJUST, AND CONTROL MUX (Diagram 7)

This block of circuitry is divided into several different functions. The largest division is the Z-Axis and CRT circuitry. A INTEN, 8 INTEN, and RO INTEN input signals are applied to the Z-Axis circuit to set the associated display intensities. Enabling gates from the Display Controller (Diagram 4) select the appropriate Z-Axis input signal for application to the $Z$-Axis amplifier as the different display types are enabled. The amplified Z-Axis signals are then level shifted to the negative voltage of the crt cathode ( -2.7 kV ) in a dc restorer circuit. A similar dc restorer circuit provides auto focusing (at the fixed focus level set by the front panel FOCUS control) in response to the intensity level changes. The intensity and auto focus control voltages are applied to the crt where they modulate the electron beam flow that produces the display seen on the screen.

Multiplexer U506, under control of the Measurement Processor, scans the front panel intensity potentiometers and the probe code lines to check for a change. Signal selection for routing through the multiplexer is controlled by the three bits on the POT5-POT7 bus lines from the Pot Data Latch (Diagram 11). Output from the multiplexer is routed to the Front-Panel Multiplexer (U2309, Diagram 11) and multiplexed with other front-panel control levels. Outputs from U2309 are routed to the A-to-D Converter (U2306, Diagram 11) where a digital value representing their analog voltage level is determined. That value is checked against the previously obtained value for a selected potentiometer
or probe code to determine if a change has occurred and, if so, the amount and direction of the change. The Measurement Processor uses that information to generate new control voltages to the circuitry affected by the change.

The Probe Adjust circuit (U930 and associated circuitry) produces a square-wave signal which is output to the front panel PROBE ADJUST jack for compensating voltage probes and checking the vertical deflection system of the oscilloscope.

The Volts Cal circuit (U931 and an associated precision voltage divider) provides the accurate dc voltage levels used during vertical SELF CAL to check the gain and offset of the measurement channels.

The Scale Illumination circuit is made up of three incandescent graticule lamps and current-source transistors. The SCALE ILLUM potentiometer sets the bias level on the transistors' emitters.

## MEASUREMENT PROCESSOR (Diagram 8)

Many of the oscilloscope circuitry functions are directed by the Measurement Processor (U2501). The Measurement Processor, under firmware control, monitors the front-panel controls and sets up the circuitry under its control according to the settings made and the instructions contained in the System ROM.

The Measurement Processor communicates directly with the devices on its eight-bit data bus. The Measurement Processor selects the device to transfer data to or from by placing the address of the device on the Measurement Processor Address Bus. That address is decoded to produce a strobe that enables the bus device corresponding to the address. Writing to or reading from the enabled device is controlled by write or read ( $\overline{\mathrm{WR}}$ and $\overline{\mathrm{RD}}$ ) pulses from the Measurement Processor. Communication on the data bus is usually limited to high speed data transfer only (to and from the System RAM and from the System ROM) and not direct control of any circuit functions.

For controlling most of the circuit operating states, the Measurement Processor places serial bits on the bidirectional MB DATA line. Appropriate enabling strobes and clocks are generated either in its address decoding circuitry or by the Processor Interface circuitry (Diagram 4) to load the control data into 24 -bit or 32 -bit shift registers. The outputs of these registers control such things as attenuator settings, preamplifier gains, sweep timing, and trigger
operating modes; all circuit operating functions that either change with front panel settings only or at a slow rate.

Scanning of the front panel controls and lighting of the front-panel LEDs that back light the buttons is under control of the Measurement Processor. These events occur at long intervals compared to the operating speed of the Measurement Processor. The front-panel switch closures are read by the Measurement Processor over a serial communication line (SW BD DATA).

## READOUT SYSTEM (Diagram 9)

## Readout System

The Readout Processor (U2400) controls the display of text and cursor readouts as directed by the Measurement Processor. The ASCII code of each character (blanks included) in a full screen of readout (one field) is loaded into the appropriate memory location of the Character Code RAM (U2406) by the Measurement Processor. It is then up to the Readout Processor to control the display process.

When the Readout Processor addresses the Character Code RAM for display of the loaded characters, the address of a memory location dictates the place that the addressed character will appear on the face of the crt. The ASCII code found at the addressed location in the Character Code RAM then accesses the character to be displayed from the Character Dot Position ROM (U2408). The screen position of an individual dot within an addressed character is directed by the character data obtained from the Character Dot Position ROM.

The data bits specifying the character position on screen and the dot position within a given character are converted to analog vertical and horizontal position signals by the readout DACs (U2412 for vertical and U2413 for horizontal). For cursors and cursor related text, voltages representing the cursor positions are added in the output mixer circuitry (U2414, U2415, and U2416) to place the readout correctly on screen. Vertical position information needed for the measurement-tracking cursors and readouts is added in the Vertical Position Switching circuit (Diagram 2).

The dots are continually refreshed to maintain a flicker-free readout. When the readout data needs changing, the Measurement Processor halts the refreshing and loads the new screen of data into the Character Codes RAM.

## SWITCH BOARD AND INTERFACE (Diagram 10)

Most of the front panel switches that can be read by the Measurement Processor are "soft" switches; they are not connected directly into the circuit to be controlled. The front-panel control physical parameters of capacitance, leakage resistance, and inductance, therefore, cannot affect the operation of the controlled circuit. The wiper voltage of some of the potentiometers is digitized, and that digitized data is used by the Measurement Processor to set up the circuitry under its control as dictated by the control change.

The momentary push-button switches are rapidly scanned at short intervals by the Measurement Processor to check if one is being pressed. When a switch closure is detected, the Measurement Processor makes the necessary circuit or display changes as directed by its firmware instructions for that button and the existing operating states.

Functions are shown to be on by turning on the LED (light-emitting diode) that back lights the push button or panel label. The Measurement Processor controls the lighting via control registers (U2523 and U2524) that it reloads with control data to enable the correct LED with each button or mode change.

## ADC AND DAC SYSTEM (Diagram 11)

The ADC and DAC system is the Measurement Processor's control link to the analog circuitry.

When the Measurement Processor does a scan to determine the front panel control settings, the DAC system drives the input to the A-to-D converter comparator (U2306) in a binary search pattern to determine the voltage level applied to the other input of the comparator. The smallest incremental change in the DAC input data that produces a switch in the comparator's output identifies the digital value of the unknown voltage. The output of the comparator (AD COMP) is applied to the Data Buffer U2515 on Diagram 8.

## POWER SUPPLY (Diagram 12)

The low and high voltages required to power the 22461 Y and 2246 Mod A are produced by a highefficiency, switching power supply. Input ac voltage from 90 to 250 volts and from 48 to 445 Hz is converted to a dc voltage that powers a preregulator circuit. The preregulator supplies regulated power to an inverter switching circuit in the primary of the power transformer (T2204). The secondary voltages produced at the secondary windings of the transformer are rectified and filtered to provide the low voltage power requirements of the instrument.

High voltage to drive the crt is generated by a multiplier circuit (U2203) that provides the +14 kV postdeflection anode voltage and the -2.7 kV to the cathode. The 6.2 Vac heater voltage is supplied by a isolated secondary winding from the power transformer that is referenced to the -2.7 kV cathode voltage.

## DETAILED CIRCUIT DESCRIPTION

## VERTICAL INPUTS (Diagram 1)

Channel 1 and Channel 2 input circuits on this schematic diagram are arranged identically. Only Channel 1 circuit numbers are referred to in the discussion. CH 3 and CH 4 are also arranged identically to each other and described separately from CH 1 and CH 2.

## Attenuator and Vertical Mode Control Registers

The switching relays of Channel 1 are driven by transistor array U174. Drive to each of the transistors in
the array to switch the relay states is supplied by the Measurement Processor (U2501) via U171. That device is a portion of a shift register formed by U171, U172 (for channel 2 relays), and U173 (for Preamplifier gains). The devices are connected in series to form one long shift register (designated Shift Register 0). Serial data bits for the entire register string are loaded at pin 2 of U171 from the SR DATA line by the SRO CLOCK applied to pin 3 of all three devices. See Table 3-1 for data bit assignments. Tables 3-2, 3-3, 3-4, and 3-5 define the bit states for controlling the switching.

Table 3-1
Shift Register 0 Bit Assignment

| Pin | Signal | Controls |
| :---: | :---: | :---: |
| U171 |  |  |
| $\begin{array}{r} 4 \\ 5 \\ 6 \\ 7 \\ 14 \\ 13 \\ 12 \\ 11 \end{array}$ | $\overline{\mathrm{CH}} 1 \mathrm{GND}$ <br> CH 1 AC <br> CH $1 \times 101$ <br> $\mathrm{CH} 1 \times 102$ <br> $\mathrm{CH} 1 \times 1$ <br> NOT USED <br> $\mathrm{CH} 1 \times 5$ <br> CH 1 PREAMP 1 | K101-CH 1 GND Coupling (last bit) $\mathrm{K} 100-\mathrm{CH} 1 \mathrm{AC}$ Coupling <br> $\mathrm{K} 102-\mathrm{CH} 1 \times 10$ Attenuator 1 <br> $\mathrm{K} 103-\mathrm{CH} 1 \times 10$ Attenuator 2 <br> $\mathrm{K} 105-\mathrm{CH} 1 \mathrm{X} 1$ Buffer Attenuation <br> No connection <br> K104-CH 1 X5 Buffer Attenuation <br> U210-CH 1 Preamplifier Gain |
| U172 |  |  |
| $\begin{array}{r} 4 \\ 5 \\ 6 \\ 7 \\ 14 \\ 13 \\ 12 \\ 11 \end{array}$ | CH 1 PREAMP 0 <br> $\overline{\mathrm{CH}} 2$ GND <br> CH 2 AC <br> $\overline{\mathrm{CH} 2 \times 101}$ <br> $\mathrm{CH} 2 \times 102$ <br> $\mathrm{CH} 2 \times 1$ <br> NOT USED <br> $\mathrm{CH} 2 \times 5$ | U210-CH 1 Preamplifier Gain $\mathrm{K} 108-\mathrm{CH} 2$ GND Coupling K107-CH 2 AC Coupling $\mathrm{K} 109-\mathrm{CH} 2 \times 10$ Attenuator 1 <br> $\mathrm{K} 110-\mathrm{CH} 2 \times 10$ Attenuator 2 <br> $\mathrm{K} 112-\mathrm{CH} 2 \times 1$ Buffer Attenuation <br> No connection <br> K111-CH $2 \times 5$ Buffer Attenuation |
| U173 |  |  |
| $\begin{array}{r} 4 \\ 5 \\ 6 \\ 7 \\ 14 \\ 13 \\ 11 \end{array}$ | CH 2 PREAMP 1 <br> CH 2 PREAMP 0 <br> CH 2 INVERT <br> CH 3 PREAMP 1 <br> CH 4 PREAMP 1 <br> ZERO HYST <br> RO FREEZE | U220-CH 2 Preamplifier Gain <br> U220-CH 2 Preamplifier Gain <br> U220-CH 2 Preamplifier Invert <br> U230-CH 3 Preamplifier Gain <br> U240-CH 4 Preamplifier Gain <br> U431C-B Trigger Comparator Hysteresis U509C-Controls Readout for SELF CAL (first bit loaded) |

## Input Coupling

Signals applied to the CH 1 BNC connector are coupled to the CH 1 attenuator via the CH 1 Input Coupling circuit. Switching logic for those connections is shown in Table 3-2. Relay K100 switches between direct (DC) and capacitive coupling (AC) of the input signal; K101 switches between connecting the applied input signal and the VOLT CAL signal to the input of the attenuator. The VOLT CAL signal line
provides either the ground for GND Coupling in normal oscilloscope operation or a test voltage input for characterization during vertical SELF CAL.

With the input Coupling set to GND (both AC and DC off), the signal path is bypassed by C113. That capacitor filters any noise from the VOLT CAL signal line. There is no precharge of the input coupling capacitor (C112) when the coupling is in ground (GND). Resistor (R111), in series with the BNC input, is a damping resistor.

Table 3-2
Input Coupling Control Bit States

| Coupling | $\overline{\mathrm{CH} 1 \text { GND }}$ or | CH 1 AC or |
| :---: | :---: | :---: |
|  | $\overline{\mathrm{CH} 2 \text { GND }}$ | CH 2 AC |
| GND/CAL | 0 | 1 |
| AC | 1 | 1 |
| DC | 1 | 0 |

The probe coding signal (CH 1 PRB ) is applied to a multiplexer (U500, Diagram 7) where it is selected to be digitized in turn with the other probe-code signals and the front panel potentiometers. The Measurement Processor determines, from the digitized value of the voltage, the attenuation factor of any attached coded probe (Tektronix coded probes). The scale factor of the VOLTS/DIV readout is then switched to reflect the correct scaling of the displayed signal. Uncoded probes and coaxial cables are interpreted as having no attenuation for setting the readout scale factors.

## High-Impedance Attenuator

Switching relays K102 and K103 control the signal path through the high-impedance attenuator, AT117. Signal attenuation is done by two 10 X attenuator sections; for 100X attenuation, the two sections are cascaded (see Table 3-3 for switching logic). The $1 \mathrm{M} \Omega$ termination resistance at the output of the attenuator is divided into two parts: $750 \mathrm{k} \Omega$ and $250 \mathrm{k} \Omega$. An output taken across the total resistance is applied to the buffer amplifier fast-path input; another output taken across the $250 \mathrm{k} \Omega$ section is applied to the slow-path input. Lowfrequency compensation for the hybrid attenuators is adjusted by C10 and C11 (parts are part of the hybrid circuit on the ceramic carrier); input $C$ is adjusted using C114.

## Input Buffer Amplifier and 1X, 2X, 5X Attenuators

Input Buffer Amplifier U112 (for CH 1) is a hybrid device. The amplifier portion of the circuitry is a fast-path/slow-path buffer having unity voltage gain that presents a high-resistance, low-capacitance load to the signal from the high-impedance attenuator and a low output impedance to the
low-impedance attenuator at the output of the amplifier. The switchable low-impedance, voltage divider network of U112 provides 1, 2, and 5 times attenuation of the output signal for application to the Vertical Preamplifier.

The input signal is applied to pin 2 (fast-path input) and pin 4 (slow-path input) of $U 112$ from the $1 \mathrm{M} \Omega$ divider at the output of the high-impedance attenuator. Internal circuitry of U112 isolates the signal from loading of the low-impedance attenuator and provides the slow-path and fast-path signal amplification. The fast amplifier path quickly passes the fast leading and falling edges of an input signal with the slow path catching up to complete the signal transfer. The output of the buffer sees a $300 \Omega$ input impedance to the low-impedance attenuator, and the preamplifier sees a $75 \Omega$ output impedance at pin 8 of U112 for all VOLTS/DIV switch settings. At $2 \mathrm{mV} / \mathrm{div}$, additional capacitive loading is applied to the output of the input buffer amplifier by C185 and Q181. This increases the high-gain bandwidth of the amplifier.

## CH 3 and CH 4 Input Amplifiers

The CH 3 and CH 4 input buffer amplifiers are identical discrete FET amplifiers. Input coupling for these two vertical inputs is always DC; there is no coupling switch. The $1 \mathrm{M} \Omega$ input is formed by a series voltage divider that attenuates the input signal by five times for application to the gate of the input FETs. The VOLTS/DIV setting (either 0.1 V or 0.5 V ) is made in the Preamplifier stage of the channel. Operation of CH 3 is described; like components in CH 4 do the same job.

From the gate of Q131A, diode CR131 provides protection from negative overvoltages exceeding about -8 V . Input C is adjusted by C 134 for low-frequency compensation. High-frequency response is compensated by C138 across load resistor R137. Step balance is adjusted by R141 in the source lead of Q131B. The single-ended output of U131A is applied via R139 (a $75 \Omega$ resistor) to the CH 3 Preamplifier. The impedance seen by the other differential input of the Preamplifier (U230, pin 8, Diagram 2) is matched by the parallel combination of R158 and C159 in series with R160.

The probe-coding signal, CH 3 PRB , is read the same way as the CH 1 and CH 2 probe-coding signals. The VOLTS/DIV readout for Channel 3 is switched to correctly match the probe attenuation factor (when properly coded probes are used).

Table 3-3
CH 1 and CH 2 Attenuator and Gain Control Bit States

| VOLTS/DIV | $\begin{gathered} \text { CH } \begin{array}{c} \times 101 \\ \text { or } \end{array} \\ \overline{\mathrm{CH} 2 \times 101} \end{gathered}$ | $\begin{gathered} \mathrm{CH} 1 \times 102 \\ \text { or } \\ \mathrm{CH} 2 \times 102 \end{gathered}$ | $\begin{aligned} & \mathrm{CH} 1 \mathrm{X} 1 \\ & \text { or } \\ & \mathrm{CH} 2 \mathrm{X} \end{aligned}$ | NC | $\begin{aligned} & \mathrm{CH} 1 \mathrm{X} 5 \\ & \text { or } \\ & \mathrm{CH} 2 \times 5 \end{aligned}$ | CH 1 PREAMP 1 or CH 2 PREAMP 1 | CH 1 PREAMP 0 or CH 2 PREAMP 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 2 mV | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| 5 mV | 1 | 0 | 1 | 0 | 0 | 0 | 1 |
| 10 mV | 1 | 0 | 1 | 0 | 0 | 1 | 1 |
| 20 mV | 1 | 0 | 0 | 1 | 0 | 1 | 1 |
| 50 mV | 1 | 0 | 0 | 0 | 1 | 1 | 1 |
| 100 mV | 1 | 1 | 1 | 0 | 0 | 1 | 1 |
| 200 mV | 1 | 1 | 0 | 1 | 0 | 1 | 1 |
| 500 mV | 1 | 1 | 0 | 0 | 1 | 1 | 1 |
| 1 V | 0 | 1 | 1 | 0 | 0 | 1 | 1 |
| 2 V | 0 | 1 | 0 | 1 | 0 | 1 | 1 |
| 5 V | 0 | 1 | 0 | 0 | 1 | 1 | 1 |

## VERTICAL PREAMPS AND OUTPUT AMPLIFIER (Diagram 2)

## Vertical Preamplifiers

Each input channel has it own Vertical Preamplifier (CH 1-U210, CH $2-\mathrm{U} 220, \mathrm{CH} 3-\mathrm{U} 230, \mathrm{CH} 4-\mathrm{U} 240$ ). The gain setting of the Preamplifier is controlled by Measurement Processor U2501 via the assigned control bits from Shift Register 0 (see Table 3-3). Channel 1 and Channel 2 gains require two control bits (on pins 1 and 2 of the Preamplifiers) to set three different gains for $2 \mathrm{mV}, 5 \mathrm{mV}$, and 10 mV VOLTS/DIV scaling. From 10 mV per division and up, the gain of the CH 1 and CH 2 Vertical Preamplifiers is set to 10 mV per division. The 1, 2, 5 scaling sequence for the remaining VOLTS/DIV switch settings is obtained by switching the high- and low-impedance attenuators. Gain of the CH 3 and CH 4 preamplifiers is controlled by one bit each (on pin 2), since there are only two scaling settings ( 0.1 V and 0.5 V per division) to select (see Table 3-5 for the gain-control bit states).

The internal circuitry of each Vertical Preamplifier is matched for the $2 \mathrm{mV}, 5 \mathrm{mV}$, and 10 mV gain settings
and the dc offsets. The output gain of each Preamplifier is adjusted by varying the common-mode resistance across the output pins (pin 13 to pin 14) to produce calibrated gain for each of the vertical channels.

Each Vertical Preamplifier has a trigger pickoff (pins 17, 18, 19, and 20) for supplying the internal trigger signal to the A and B Trigger Source Selector Multiplexers. Capacitor coupling from pins 17 and 18 to pins 19 and 20 provides a fast-path signal into a duplicate, but level-shifted, slow-path signal line. The negative side of the differential trigger signal is terminated in a capacitor to ground (from pin 19) to provide a balance for the transmission line.

Table 3-4 CH 2 INVERT Control Bit

| Setting | CH 2 INV |
| :---: | :---: |
| Normal | 0 |
| INVERT | 1 |

Table 3-5
CH 3 and CH 4 Gain Control Bit

| VOLTS/DIV | PREAMP1 |
| :---: | :---: |
| 0.1 V | 0 |
| 0.5 V | 1 |

The VOLTS/DIV VAR controls for CH 1 and CH 2 (R2101 and R2103) directly vary the gain of the Vertical Preamplifiers between the calibrated VOLTS/DIV settings. The Measurement Processor detects whether the VAR control for a channel is in or out of its detent position; and, if out, a greater-than symbol ( $>$ ) is placed in front of the VOLTS/DIV readout to show that the channel is uncalibrated.

Each Preamplifier produces a standing current of about 11 mA into a common summing node. Output of the vertical signal from a Preamplifier is controlled by enabling signals (CH 1 EN through CH 4 EN ) from Display Logic IC U600 (shown on Diagram 4). The enabling signal that turns on a vertical channel signal also enables the position signal current for that channel through the Vertical Position Switching circuit (either U202 or U201) into the summing node.

## Delay Line Driver

The Delay Line Driver is a differential amplifier that provides the signal amplification needed to drive the delay line. The circuit is compensated to produce the needed circuit response at the output of the delay line. Both sides of the differential amplifier are identical and circuit operation of the positive side components is described.

Transistors Q250 and Q252 are arranged as a feedback amplifier. The parallel combination of R250F and R250G supplies the feedback from the emitter of Q252 back to the base of Q250. Diode CR260 provides a one-diode voltage drop in the feedback loop for proper biasing of the base-to-collector junction of the input transistor (Q250). Gain of the amplifier is set by the value of common-mode resistor R270 (there is a small dc voltage gain). If the Vertical Preamplifier and Vertical Position circuit output currents are exactly 11 mA (no signal and no offsets) the feedback current is zero. Some standing feedback current will be present if the sum of the input currents is not exactly 55 mA . A 1 mA current change of the input base current to Q250 produces a 41 mV change at the collector of Q252.

The no-signal dc output voltage from Q252 is +7.5 V , and the standing current is about 15 mA . The differential voltage between the positive and negative side of the delay line with no signal input is $0 \mathrm{~V} \pm 0.5 \mathrm{~V}$. The differential signal voltage input to the delay line is about 29 mV per graticule division of deflection.

Biasing of the input transistor bases is supplied by R262 and R264 (for Q250) and R263 and R265 (for Q251). Two resistors in series are used to provide the power handling needed (they are low-wattage precision resistors). The dc voltage at the bases of Q250 and Q251 is maintained at 7.5 V by a bias stabilization circuit. Operational amplifier U260 compares the common-mode voltage at the junction of R254 and R255 to the +7.5 V supply on its pin 3 input. If the base voltage is too low, U260 raises the common-mode emitter voltage (and thereby the base voltage) of the two input transistors.

Compensation components peak up the circuit response to counteract the roll off effects of the delay line. The three series-rc combinations (C272 and R272, C273 and R273, and C274 and R247) between the emitters of Q252 and Q253 compensate different frequency ranges to correctly shape the circuit response. The series-rc circuit between the collectors of Q252 and Q253 (C275 and R275) damps the gain at high frequencies to prevent oscillation. Voltage-variable capacitors CR262 and CR263 are used to compensate for increased rolloff at high operating temperature. As the temperature increases, thermistor RT201 reduces the bias across CR262 and CR263. This causes the capacitance of the series combination of CR262 and CR263 to increase, peaking the response further. Impedance matching and input termination of the $75 \Omega$ delay line is done by the parallel-series combination of R278, R279, R280, and R281.

## Vertical Position Switching

The Vertical Position Switching circuit consists of buffer amplifiers for the four vertical channel position signals (U203A, B, C, and D), two solid state switch arrays (U201 and U202), and a transistor paraphase amplifier circuit (U280, Q284, and Q285).

The vertical positioning voltages from the front panel POSITION controls are applied to the noninverting inputs of the four voltage-follower buffer amplifiers (U203A through U203D). The inputs and outputs of the amplifiers are capacitively bypassed to eliminate noise from the position signals. The buffered output signals are applied to switching arrays U201 and U202 for selection at the correct time for positioning the displayed trace and position-related readouts.

POSITION VOLTAGE SWITCHES. Selection of the channel or readout position signals to be supplied to the paraphase amplifier summing node is controlled by several sources. The vertical channel enable signals (CH 1 EN through CH 4 EN) from Display Controller U600 (Diagram 4) turn on the appropriate channel position signal for the enabled Vertical Preamplifier when displaying waveforms. The nominal position range of the vertical signal is $\pm 12$ divisions.

When position-dependent readout (labeled cursors that follow the vertical channel position controls) is displayed, the RO CH 1 POS EN through RO CH 4 POS EN signals from tri-state latch U2403 (shown on Diagram 9) enable the appropriate vertical position signal into the summing node at the input to the paraphase amplifier. The Readout Position Enable signal lines are tri-stated (open) during display of the channel signals so that the Vertical Channel Enable signals have control of the position enable lines. Also, the Readout Position signals cannot override the Vertical Channel Enable signal levels to turn on a Vertical Channel Preamplifier with the series resistors (R212, R222, R232, and R242) in the signal path. The vertical position of the enabled vertical channel is added to the position of the readout so that the cursors appear at the correct vertical position in the display.

When non-position tracking readout is displayed (i.e., menus and scale-factor readouts), the vertical screen position of the readout is conveyed by the RO VERT signal only. The RO VERT signal is enabled into the summing node input of the paraphase amplifier by the RO VERT EN signal for both readout types (po-sition-tracking or fixed). Extra noise bypassing provided by decoupling components R205, R207, and C268 on the RO VERT signal line reduces jitter of the readout display.

During vertical SELF CAL, the RO CH 1 POS EN through RO CH 4 POS EN signals enable the appropriate vertical position signal into the summing node at the input to the paraphase amplifier without turning on a channel Vertical Preamplifier. The gain and offset of the voltage followers and position switches may then be calculated independently from the vertical channel signal. The computed offsets are then used by the Measurement Processor to correctly place the posi-tion-tracking readouts (cursors) on the display relative to the vertical position of the waveform.

The TRACE SEP EN and RO TR SEP EN signals operate the same as described for the channel enable and readout position enable signals. A slight difference between the channel vertical position signals and the TRACE SEP signal is that TRACE SEP is attenuated
more. The higher value of R206 on pin 13 of U201 reduces the TRACE SEP range to $\pm 4$ divisions as compared to $\pm 12$ divisions for the vertical signals.

POSITION PARAPHASE AMPLIFIER. The Position Paraphase amplifier circuitry is formed by a transistor array (U280) driving two discrete transistors (Q284 and Q285). The circuit is configured as two negativefeedback amplifiers that produce a differential output current from the summed single-ended input current. Transistors U280B and U280E are constant-current sources for their associated amplifier pairs in the array. The feedback path for the U280A-U280F amplifier combination is via R286 from the emitter of Q284. The no-signal feedback current through R286 is 1 mA . Feedback for the U280C-U280D combination is via R289 from the emitter of Q285. Feedback current in R289 is $100 \mu \mathrm{~A}$. Both Q284 and Q285 are high beta transistors requiring little base-drive current. The overall vertical displacement response from the input (at the base of U280F) to the output is $200 \mu \mathrm{~A}$ per division of vertical screen displacement.

The signal applied to the base of U280C is the inverted position signal developed across R290 in the emitter of Q284. The signal is again inverted by U280C to drive the base of Q285 in the opposite direction from the signal at the base of Q284. The standing dc current (no signal input) output current into the delay line input summing node is 11 mA ; the same as the output of the vertical preamplifiers. Vertical centering of the menu and readout displays within the graticule area is done using VERTICAL READOUT CENTERING potentiometer R260.

## Vertical Output Amplifier

## WARNING

Vertical Output IC U701 runs hot and can burn you if touched. The metal tab on top of the device is NOT ground; it is the -5 V supply to the IC.

Vertical Output IC U701 buffers the signal output of the delay line and provides the circuitry for the BW LIMIT and BEAM FIND functions and for the vertical signal gain adjustment. The inputs to the Vertical Amplifier are terminated in $75 \Omega$ by external resistors R706 and R707. External filter components C707, L701, and L702 produce the bandwidth limiting of the vertical signal when internally switched into the output amplifier circuitry of U701.

Manual calibration of the vertical signal display to the crt graticule is done using VO Gain potentiometer R703. The components between pins 12 and 22 of U701 (Q704, R726, R727, and R728) provides gain correction for the small difference in gain between full bandwidth and bandwidth-limited operation of the Vertical Output IC. Correction for a thermal change between display of the signal and display of the readout is provided by the RO Jitter adjustment (R724).

WARNING<br>Vertical Output Amplifiers Q701 and Q702 run hot and can burn you if touched.

Vertical Output Amplifiers Q701 and Q702 provide the signal gain necessary to drive the vertical crt deflection plates. The deflection plates have a comparatively large capacitance, and to change the voltage as fast as necessary to deflect the crt beam, the Vertical Output Amplifiers have to handle large current demands. A reduction in circuit capacity is made by reducing the collector capacitance of the output transistors. The cases of Q701 and Q702 are NOT the collectors; they are connected to the transistors' base material; the case tabs mark the collector leads. In the collector circuits, T-coils L703 and L704 boost the vertical bandwidth of the output amplifiers; and R731 and R732 are damping resistors.

## Vertical Comparator

The Vertical Comparator circuit (U702, Q703, and associated components) allows the Measurement Processor to determine the gain and offset of the vertical system up to the input to the Vertical Preamplifier. The circuit is enabled only during the vertical self characterization routine. Known dc voltage levels are applied to the attenuator inputs, and U702 compares the voltage from the delay line to the HORIZ POS signal which is being driven in a binary search pattern. The output voltage is found by successively narrowing the search levels until the smallest change possible from the DAC system causes the Vertical Comparator output to change states. Using the measured value to compare against the known input voltage, the Measurement Processor determines a Vertical Calibration constant that must be applied to produce accurate voltage measurements.

## A AND B TRIGGER SYSTEM (Diagram 3)

Most of the trigger signal switching and trigger level comparator circuitry is contained on two integrated circuit devices (U421 and U431). Within the devices is the logic circuitry that drives the selectable variables of Trigger SOURCE, Trigger CPLG, and Trigger SLOPE for both the A and the B Triggers. Selection of the trigger variables is done by control bits generated by Display Sequencer U600 (Diagram 4). The remaining portions of the circuitry shown in Diagram 3 include the $A$ and $B$ Trigger bandwidth limiting circuitry, the TV Trigger Detector circuitry, the Auxiliary Control Register (part of Shift Register 1), and the DC Filter for the measurement system. The B Trigger circuitry does double duty in that measurements for the DC, +PEAK, -PEAK, and PEAK-to-PEAK values of a signal are done in the B Trigger channel. Consequently, voltage measurements cannot be done in ALT or B Horizontal Display Modes when the B Trigger circuitry is in use.

## A and B Trigger Source Selectors

Analog switching of the Trigger signal sources is done by the circuitry in U421A (for the A Trigger) and U431A (for the B Trigger). The possible Trigger SOURCE selections are the same for both the $A$ and the B Trigger system. They are CH1, CH 2, CH 3, CH 4, LINE, and VERT. In ALT Vertical MODE, when VERT is the selected source a trigger is obtained in succession for each displayed channel. A stably triggered display will be obtained for each channel signal without regard to frequency relationships between the applied signals. If ADD Vertical MODE is selected, a special adder circuit in U421A and U431A, adds the CH 1 and CH 2 signals to produce an ADD trigger signal composed of the two inputs. The LINE Trigger signal is a sample of the power-line input voltage. Multiplexer U1106A, in the input path for the LINE trigger signal, selects between the LINE signal (for oscilloscope operation) and the TB CAL signal (used for horizontal self characterization).

When a Voltage Measurement is being done, U431A in the B Trigger circuit acts as the measurement channel selector and selects either the CH 1 or the CH 2 input signal to be measured.

## $A$ and $B$ Trigger Coupling Selector

Coupling selections for DC, AC, HF REJ, and LF REJ are done by U421B for the A Trigger and U431B for the B Trigger. The trigger signal path is through a filter circuit having the proper bandpass characteristics for the selected trigger coupling. NOISE REJ coupling is done differently. The two Trigger LEVEL Comparators (U421C and U431C) have selectable hysteresis. For NOISE REJ Trigger CPLG, the
hysteresis is increased so that a larger signal change is required to produce a state change at the output of the comparators. Trigger Coupling control logic is shown in Table 3-6.
Another signal source selectable in the Trigger Coupling Selectors is the output of the TV Trigger Detector (TV LINE or TV FIELD). An applied composite video signal is separated so that the horizontal line or vertical field sync pulse can be used to
trigger the oscilloscope for Television signal display (see TV Trigger Detector description). Selection between LINE or FIELD for the A Trigger source is done by multiplexer U1104A with its output being applied to pin 18 of U421B. Pin 18 of U431B in the B Trigger system has an input of either the TV Line sync signal, for TV triggering of the B Sweep, or the output of the Measurement Signal Low-Pass Filter, when the DC measurement mode is active.

Table 3-6
Trigger Selection Logic

| Front Panel Coupling Selection | Latched Bit Values |  |  |  | Description |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | SLOPE | TS2 | TS1 | TSO |  |
| A Sweep Mode (U421) AUTO LEVEL, AUTO, NORM, or SGL SEQ |  |  |  |  |  |
| DC | 0 | 0 | 1 | 0 | DC Coupled |
| NOISE REJ | 1 | 0 | 1 | 0 | DC Coupled, Noise Reject |
| HF REJ | 0 | 1 | 0 | 1 | HF Reject |
| LF REJ | 0 | 0 | 1 | 1 | LF Reject |
| $A C$ | 0 | 1 | 0 | 0 | AC Coupled |
| A Sweep Mode (U421) TV LINE or TV FIELD |  |  |  |  |  |
| DC | 1 | 0 | 0 | 0 | TV Input, Noise Reject |
| NOISE REJ | 1 | 0 | 0 | 0 | TV Input, Noise Reject |
| HF REJ | 1 | 0 | 0 | 0 | TV Input, Noise Reject |
| LF REJ | 1 | 0 | 0 | 0 | TV Input, Noise Reject |
| AC | 1 | 0 | 0 | 0 | TV Input, Noise Reject |
| B Sweep Mode (U431) AUTO LEVEL, RUNS AFTER or NORM |  |  |  |  |  |
| DC | 0 | 0 | 1 | 0 | DC Coupled |
| NOISE REJ | 1 | 0 | 1 | 0 | DC Coupled, Noise Reject |
| HF REJ | 0 | 1 | 0 | 1 | HF Reject |
| LF REJ | 0 | 0 | 1 | 1 | LF Reject |
| AC | 0 | 1 | 0 | 0 | AC Coupled |
| B Sweep Mode (U432) TV LINE |  |  |  |  |  |
| DC | 1 | 0 | 0 | 0 | TV Input, Noise Reject |
| NOISE REJ | 1 | 0 | 0 | 0 | TV Input, Noise Reject |
| HF REJ | 1 | 0 | 0 | 0 | TV Input, Noise Reject |
| LF REJ | 1 | 0 | 0 | 0 | TV Input, Noise Reject |
| AC | 1 | 0 | 0 | 0 | TV Input, Noise Reject |

MEASUREMENT SIGNAL LOW-PASS FILTER. The average dc level of a signal is obtained for measurement by filtering the measurement channel signal to remove all but the dc component of the signal. A active RC filter circuit formed by U1101B, R1154, R1155, C1154, and C1155 does the filtering with U1101B buffering the filtered output voltage to isolate it from loading. The dc level is applied back to the Trigger Coupling switch (U431B, pin 18) for input to the B Trigger Comparator (U431C) where the actual measurement is done (see the B Trigger Comparator description).

## A and B Trigger Bandwidth Limit Circuits

The A Trigger Bandwidth Limit circuit components (Q440, U441F, CR432, C432, L432, R432, Q444, and U441E) act to roll off the trigger circuit bandwidth when $\overline{\text { BW LIMIT }}$ is active (low). The B Trigger Bandwidth Limit circuit components do the same job (with some additional compensation components), but can be selected independently of the SCOPE BW front panel setting (by the Measurement Processor using the BW FULL B signal). That is because the B Trigger Channel is used for the Measurement system, and the circuit bandwidth must be full for making measurements. The actual circuit operation for both is the same, and only the A Trigger Bandwidth limiting action is described.

For full trigger bandwidth, the BW LIMIT signal from Auxiliary Register U 1103 is written high by the Measurement Processor. That high is inverted to a low by U441E and U441F and applied to the bases of Q440 and Q444. The low output turns off Q444 and disconnects C444 from ground. The purpose of C444 is to act as part of an LC filter that rolls off the signal. The low applied to the base of Q440 turns that transistor on pulling the anode of CR432 up and forward biasing it. The trigger signal ac path then bypasses L432 and R432 through CR432 and C432. The dc component of the trigger signal is still via L432 and R432.

When the bandwidth is limited, the BW LIMIT signal is low. That is inverted to a high that turns on Q444 (connecting C444 to ground) and turns off Q440 (reverse biasing CR432). The trigger signal path is now through L432 and R432 with C444 connected to ground to roll off the circuit bandwidth.

## A Trigger Comparator

The Trigger signal is compared with the A Trigger LEVEL setting by U421C to determine the signal level and slope of the trigger signal that produces a sweep trigger. The comparator slope is set internally by the switching logic; the Trigger comparison level
is set using the front panel Trigger LEVEL control. A fixed amount of hysteresis in the A Trigger Level Comparator prevents double triggering on signals accompanied by normal noise. NOISE REJ coupling increases the hysteresis by a factor of four to reduce the Comparator's sensitivity to noise if triggering on very noisy signals is required. Once a level state change occurs, a larger change in the opposite direction is required (because of the circuit hysteresis) to reverse the state change. The differential output of U421C is applied to the Trigger Logic IC (U602, Diagram 4) where the gating signals to start the display sweep are generated.

## B Trigger Comparator and Measurement A-to-D Converter

For B Trigger signal comparison, the B Trigger Level Comparator (U431C) works the same as the A Trigger Level Comparator. Its differences lay in its use as the Measurement Channel A-to-D Converter for making signal voltage measurements. When a measurement is being done, the ZERO HYST control bit from Shift Register 0 (U173, Diagram 1) is set high. This high turns off Q480 and disconnects U431C pin 28 from ground. The biasing combination of R476 and R486 between the -5 V supply and ground reduces the hysteresis of the B Trigger Comparator to zero. A small incremental change in signal level to the comparator will then cause it to change output states. The B REF TRIG LVL signal on pin 24 of U431C is driven in a binary search pattern by the Measurement Processor (via the DAC System) while monitoring for state changes at the output. The smallest incremental input change of the B REF TRIG LVL that produces an output change then defines the voltage point being measured (+PEAK, -PEAK, or DC). When peak-to-peak voltage measurement is done, the Measurement Processor merely measures one peak voltage of the signal, then the other.

The output of the B Trigger Level Comparator is applied to the B Trigger input of U602 (Diagram 4) via delay line DL22. The 18 ns delay produced permits the leading edge of the B trigger signal to be viewed when displaying the B Sweep.

## Auxiliary Shift Register

Auxiliary Shift Register U1103 is the last register in Shift Register 1. Control bits loaded into the register from the AUX DATA signal line (from U303 pin 9, Diagram 5) are serially shifted through Sweep Shift Register U302 and U303 (Diagram 5). Circuit functions controlled by the bits in U1103 are the following:

B TV TRIG EN: Switches between the B TV Trigger signal and the DC measurement signal voltage (U1106C).

TV FIELD SEL: Switches the A Trigger between TV FIELD and TV LINE (U1104A).

MAG: Controls the $\times 10$ Magnification function of the Horizontal Output Preamplifier (U802, Diagram 6).

VERT COMP EN : Turns on the Vertical Comparator (U702, Diagram 2) during voltage self characterization.

TB CAL: Switches the time-base calibration signal into the $B$ trigger system during horizontal self characterization (U1106A).

BW FULL B: Switches between full and limited B Trigger bandwidth.

BW LIMIT : Switches between full and limited A Trigger bandwidth. The BW LIMIT signal has a second use. As the last bit in Shift Register 1, it is fed back to the Measurement Processor during diagnostic checks done on the Shift Registers.
$\overline{\mathrm{XY}}$ : Switches the range of the horizontal position signal (HORIZ POS) between that needed for $Y-T$ display and that needed for $X-Y$ display (U301B, Diagram 6).

Multiplexer (U1106A) normally provides the Line Trigger signal picked off from the Power Supply input. For self characterization (SELF CAL) of the Time Base, the switch outputs the TB CAL signal obtained from the Measurement Processor (U2501, Diagram 8).

## TV Trigger Detector

INPUT AMPLIFIER. The signal at pin 19 of U421A is applied to pin 3 of U1101A via a low-pass filter formed by R426, L426, and C426. The filter limits the bandwidth of the $X$-AXIS signal to about 5 MHz for application to the Horizontal Preamplifier (U802, Diagram 6) and to the TV Trigger Detector circuitry. Operational amplifier U1101A provides low-pass gain of the applied composite video signal that further attenuates the video portion of the signal relative to the sync pulses. The output signal from U1101A is applied to the Peak Detectors and the Sync Comparator.

PEAK DETECTORS. The peak detectors determine the positive and negative peaks of the applied composite video signal. Those peaks voltages are applied across a voltage divider circuit used to set the comparison level (slice level) to one input of a comparator. That level is such that, when the user
selects the correct sync polarity for the applied signal, the middle of the sync tips is at the threshold level of the comparator. The output of the comparator then switches only on the sync tips of the applied signal. The peak detectors are complementary in that the positive-peak detector transistors (Q1101, Q1102, and Q1103) and the negative-peak detector transistors (Q1104, Q1105, and Q1106) are complementary types (PNP-NPN). Both detectors are driven from the same input signal; the positive peaks of the video signal forward bias Q1101, and the negative peaks forward bias Q1104. The operation of the positive peak detector is described.

The composite video signal is applied to the emitter of Q1101. A positive-going signal increases the current through Q1101, causing the collector voltage to rise. The rising collector voltage biases on Q1102 harder, and C1114 charges up rapidly following the positive-going signal up to its positive peak. When the input signal starts negative, Q1101 is turned off immediately by the charge held on C1114. That leaves C1114 holding the positive peak voltage of the input signal. Emitter-follower Q1103 applies that peak voltage level to U1104B pin 3 via R1117. R1136 to the -7.5 V from pin 3 provides a fixed offset to the signal level. The negative-peak detector does the same type of operation on the signal to apply the negative peak voltage to pin 5 of U1104B.

When the sync polarity is selected to match the sync of the applied video signal (by the user with the A SLOPE switch), the voltage level at the selected input of U 1104 B is at the middle of the sync-tip voltage. If the wrong polarity is selected, triggering will take place on the video signal. For signal generator signals, the effect may not be noticeable, except for a shift of the trigger point; but if composite video signals are being viewed, the display will be unstable when the wrong polarity is selected.

SYNC COMPARATOR. The incoming composite video signal is applied to the plus input (pin 3) of the Video Sync Comparator (U1102A). The Video Sync Comparator looks at the signal level on pin 2 and compares it with the incoming video signal level. When the incoming level crosses the comparison threshold, the output of U1102A switches state. That state change occurs at the mid level of the sync pulses. The output signal of U1102A (TV LINE) is applied directly to U1104A pin 2 and U1106C pin 1 to be available for selection for the A and the B Trigger systems for TV LINE triggering.

FIELD SYNC FILTER. The filter circuit composed of R1132, R1133, C1106, C1107, and U1102B processes the output of U1102A further to determine when the vertical field sync signal is present. The time constant of the filter elements is such that the line sync pulses between vertical fields
cannot move the voltage on U1102B pin 5 across the comparison threshold (ground on pin 6).

During the vertical field sync pulse, the frequency of the serration pulses (line and equalizing) doubles. The filter capacitors will then be discharged enough to go below ground and switch the output state of U1102B. That signal is applied to U1104A pin 1 to be available as the TV FIELD Sync trigger signal for the A Trigger system.

SYNC SWITCHING. Solid-state switches U1104A and U1106C provide switching between the TV FIELD and the TV LINE signal for the A Trigger and between TV LINE from A SOURCE and the average DC level of the measurement channel for the B Trigger. The switching states are controled by the Measurement Processor via the TV FIELD SEL and the B TV TRIG EN control signals from the Auxiliary Control Register (U1103).

## DISPLAY AND TRIGGER LOGIC AND PROCESSOR INTERFACE (Diagram 4)

The Display Sequencer or SLIC (slow-logic integrated circuit, U600) performs most of the slow logic functions required to run the display functions. This integrated circuit contains a microprocessor interface, the display sequencer logic circuitry, the trigger holdoff timer, the chop clock, and an interface to the on-screen readout control logic.

The microprocessor interface of $U 600$ provides the capability to serially load the internal control register, write the internal read/write memory, do some limited real-time control over a few sequencer functions, and monitor status information.

The Display Sequencer contains a read/write memory for storing the display states to be sequenced through and logic for sequencing the $A$ and B Sweep displays and trigger sources. The sequencer also provides control signals that are needed to do waveform measurements.

An internal trigger holdoff timer provides a pulse with programmable width that is triggered on at the end of A Sweep (or at the end of B Sweep). The pulse width may be set from $1 \mu \mathrm{~s}$ to greater than 0.5 s , depending on the internal counter divide ratio, and the holdoff oscillator frequency at pin 15.

The chop clock circuit generates a phase-dithered chop clock and blanking signal, derived from an external frequency source. With 10 MHz applied, the chop rate can be 1.25 MHz or 625 kHz , with a blanking time of about 200 ns ( 625 kHz is used in the 22461 Y and 2246 Mod A).

The readout interface circuit responds to the readout request and readout blanking inputs, and generates a blanking signal (BLANK, pin 18) to control the Z-Axis Amplifier enabling signals from U602. The chop blanking signal also gets routed through this circuit.

## Pin Description

The following is a description of Display Sequencer U600 pin functions (see Figure 3-1 for pin numbers).


Figure 3-1. Display Sequencer IC (SLIC, U600) pin-out diagram.

DIO: Data 10 pin. This pin is tied to the Measurement Processor MB DATA line. Data to be clocked into the control register is presented here, and status data can be read out on this pin when the $\overline{\mathrm{RD}}$ input is low (tri-state output). See Table 3-7.

TDI: Trigger data input pin. When $\mathrm{A} 3=\mathrm{A} 2=1$, data on this pin is sent to the DIO pin (when $\overline{R D}$ is low).
$\overline{\mathrm{RD}}$ : Read enable input (active low). Bringing this pin low causes internal status data (selected with A3-A0) to be presented on the DIO pin for transfer to the Measurement Processor.

WR: Write enable input (active low). A negative-going pulse on this pin performs actions described in the Table 3-7.

SOUT: Strobe output pin (active low). When $A 3, A 2, A 1$, and $A 0=1111$, $\overline{\text { SOUT }}$ goes low when the $\overline{W R}$ pin is pulled low. Otherwise, SOUT is always high.

A3, A2, A1, A0: Address inputs. The ADDRO-ADDR3 selection bits are latched from the Measurement Processor address bus by U2512, Diagram 8.

A GATE: A Sweep Gate input (active low).

B GATE: B Sweep Gate input (active low).

TC: Timing clock input.

LFC: Low-frequency clock input. A signal derived from the calibrator circuit is used for skewing the chop-clock phase.
$\overline{\mathrm{ROR}}$ : Readout request input (active low). A low causes the CH $1 \mathrm{EN}, \mathrm{CH} 2 \mathrm{EN}, \mathrm{CH} 3 \mathrm{EN}$, CH 4 EN, HD1, HDO, and TS outputs to all go low, and allows the $\overline{\mathrm{ROB}}$ input to have complete control of the BLANK output. If $\overline{\mathrm{ROB}}$ is low when $\overline{R O R}$ goes low, then the internal timing will be such that the BLANK output will go high quickly enough to blank the display before switching transients can be shown on screen (see the detailed description of the readout interface).
$\overline{\mathrm{ROB}}$ : Readout blank input (active low). During readout active time ( $\overline{\mathrm{ROR}}=\mathrm{low}$ ), the $\overline{\mathrm{ROB}}$ input is inverted and sent to the BLANK output.

OSC OUT: The external holdoff oscillator output drives this pin. A falling edge causes the internal holdoff counter to increment.

OSC RST: Oscillator reset output. Internal logic causes this output to go high to discharge the external holdoff oscillator timing capacitor at the end of holdoff (see detailed description of the holdoff timer operation).

CH 1 EN: Channel 1 enable output (active high).

CH 2 EN: Channel 2 enable output (active high).

CH 3 EN: Channel 3 enable output (active high).

CH 4 EN: Channel 4 enable output (active high).

Table 3-7
Display Sequencer (U600) Control Bit Assignments

| A3 | A2 | A1 | AO | DIO when $\overline{\text { RD LO }}$ | Action when WR Strobed |
| :---: | :---: | :---: | :---: | :--- | :--- |
| 0 | 0 | 0 | 0 | Control Reg. msb | DIO clocked into Control Reg. (a) |
| 0 | 0 | 0 | 1 | RAM comparator | RAM written from Control Reg. |
| 0 | 0 | 1 | 0 | EOSS flag | RAM address incremented (b) |
| 0 | 0 | 1 | 1 | EOS signal (c) | RESET is strobed (d) |
| 0 | 1 | 0 | 0 | A Gate Detect flag | MRESET is strobed (e) |
| 0 | 1 | 0 | 1 | B Gate Detect flag | RAM load mode enabled (f) |
| 0 | 1 | 1 | 0 | A Gate Detect flag | A/B GATE-detected flags reset |
| 0 | 1 | 1 | 1 | B Gate Detect flag | Set A slope output (g) |
| 1 | 0 | 0 | 0 | (h) | Forces B1/B2 Source/Slope/Delay (i) |
| 1 | 0 | 0 | 1 | (h) | Forces B Slope output (j) |
| 1 | 0 | 1 | 0 | (h) | Sets BLANK output HI (k) |
| 1 | 0 | 1 | 1 | (h) | Sets THO output HI (k) |
| 1 | 1 | 0 | 0 | TDI data | (see description of TEST input) |
| 1 | 1 | 0 | 1 | TDI data | (see description of TEST input) |
| 1 | 1 | 1 | 0 | TDI data | Sets norm B Source/Slope/Delay (l) |
| 1 | 1 | 1 | 1 | TDI data | SOUT pin gets strobed |

## Notes:

(a) Data is clocked into the control register on the rising edge of $\overline{W R}$.
(b) RAM load mode must be enabled; the address increments on the rising edge of $\overline{W R}$.
(c) EOS (end of sequence) goes high for the last state of any display sequence. EOS is read out for test purposes.
(d) The THO output should be set high when RESET is strobed for proper initialization. This does the following:
a. It initializes the display sequencer back to the first display state (RAM address 000 ). In ALT VERT Mode, all vertical enable, horizontal enable, and trig source outputs are initialized. In CHOP VERT Mode, the horizontal enable and trig source outputs are initialized, but the vertical enable outputs continue to cycle at the chop clock rate.
b. It resets the EOSS (end of single sequence) flag.
c. It resets the trigger holdoff timer.
(e) Used for initialization, during testing of the device.
(f) A rising edge on $\overline{W R}$ with $D I O=1$ enables the RAM load mode; a rising edge on $\overline{W R}$ with DIO $=0$ disables the RAM load mode.
(g) A rising edge on $\overline{W R}$ with $D I O=1$ sets the $A$ Slope output high; a rising edge on $\overline{W R}$ with DIO $=0$ sets the A Slope output low.
(h) Used for device testing only.
(I) A rising edge on $\overline{W R}$ with DIO $=1$ forces the B1 Trigger Source, the B1 Slope, and sets the DS output high; a rising edge on $\overline{W R}$ with DIO $=0$ forces the B2 Trigger Source, the B2 Slope, and sets the DS output low.
(j) A rising edge on $\overline{W R}$ with DIO $=1$ forces the B SLOPE output high; a rising edge on $\overline{W R}$ with DIO $=0$ forces the $B$ SLOPE output low. This forcing function takes precedence over the force B1/B2 Source/Slope/Delay feature described in note (i) above. This forcing function is canceled by applying a negative strobe to the $\overline{W R}$ input with the address $=1110$.
(k) A rising edge on $\overline{W R}$ with $D I O=1$ sets the output high; a rising edge on $\overline{W R}$ with DIO $=0$ allows the output to behave normally.
(I) A negative pulse on $\overline{W R}$ with address $=1110$ will cancel the effects of (i) above and allow the B Source, B Slope, and DS outputs to behave normally.

ATS 2, ATS 1, ATS 0: A Trigger Source Select outputs. These bits either correspond to three bits of the control register, or they track with the vertical channel enable outputs (in ALT Vertical Mode with VERT MODE trigger selected). These outputs change state on the rising edge of the THO output, or when RESET is strobed while THO is high. The encoding scheme is shown in Table 3-8.

Table 3-8
A Trigger Source Select Bits

| ATS 2 | ATS 1 | ATS 0 | SOURCE |
| :---: | :---: | :---: | :--- |
| 0 | 0 | 0 | CH 1 |
| 0 | 0 | 1 | CH 2 |
| 0 | 1 | 0 | $\mathrm{CH} \mathrm{1}+\mathrm{CH} \mathrm{2}$ |
| 0 | 1 | 1 | CH 3 |
| 1 | 0 | 0 | CH 4 |
| 1 | 0 | 1 | Line |

A SLOPE: A Trigger slope output.

BTS 2, BTS 1, BTS 0: B Trigger Source Select outputs. These bits correspond to either one of two sets of three bits in the control register, or they can track with the vertical channel enable outputs (in ALT Vertical MODE). These outputs normally change state on the rising edge of the THO output, or when RESET is strobed while THO is high. If B1 or B2 Source/Slope/Delay is being forced, the outputs will correspond directly with one of the two three-bit sets in the control register. The encoding scheme matches that used for the A trigger source select bits shown in Table 3-8.

B SLOPE: B Trigger Slope output. This output is set to either one of two bits in the control register. This output normally changes state on the rising edge of the THO output, or when RESET is strobed while THO is high. It may also be forced high or low by the Measurement Processor via the processor interface.

HD1, HDO: Horizontal display enable outputs. These outputs normally change state on the rising edge of the THO output, or when RESET is strobed while THO is high. The encoding scheme is shown in Table 3-9.

Table 3-9
Horizontal Display Mode Select Bits

| HD1 | HD0 | SOURCE |
| :---: | :---: | :---: |
| 0 | 0 | Readout displayed |
| 0 | 1 | A Sweep displayed |
| 1 | 0 | B Sweep displayed |
| 1 | 1 | $X-Y$ mode |

DS: Delay select output. This output normally changes state on the rising edge of the THO output, or when RESET is strobed while THO is high. It may also be forced by the Measurement Processor via the processor interface. DS high selects the first delay (B1), and DS low selects the second delay (B2).

TS: Trace separation output. This output changes state on the rising edge of the THO output, or when RESET is strobed while THO is high. TS goes high to enable trace separation; TS goes low during a readout request cycle.
$\overline{Z E N}: Z-A x i s$ enable output (active low). This output goes low when the ZAP control bit is set high, or when the selected $B$ trigger source channel (as presented on the BTS 2, BTS 1, and BTS 0 output pins) is the same as the channel being enabled for display.

MGE: Measurement gate enable output (active low). This output behaves the same way as ZEN, except in chop vertical mode, in which MGE stays in a low state. Also, the ZAP control bit has no effect on MGE.

THO: Trigger holdoff output (active high). Outputs the variable holdoff pulse. In single sequence mode, this output will go high after the last A Sweep of the sequence and stay high until RESET is strobed. This output may also be forced high via the Measurement Processor interface.

BLANK: This output is controlled from three sources. At the end of a readout request cycle (when $\overline{R O R}$ goes high), the BLANK output will be asserted for four to six timing clock periods (to hide vertical source switching transients). Chop blanking pulses can be routed to this output (however, when $\overline{R O R}$ is low, chop blanking is automatically
inhibited). Lastly, this output may be forced high via the Measurement Processor interface.

TEST: Test mode enable input (active low). TEST is held high and not used in normal operation. This pin is pulled high to force normal operation, but may be pulled low to enable the test mode. Enabling test mode does the following:

1. Disables single sequence and $B$ Ends $A$ modes, no matter what code is in the control register.
2. Reconfigures the trigger holdoff timer to make it more easily testable (see control register description for control bits H4-HO).
3. $\mathrm{A} 3, \mathrm{~A} 2, \mathrm{~A} 1, \mathrm{~A} 0=1100$ allows a negativegoing pulse on $\overline{W R}$ to reset only the control register.
4. $\mathrm{A} 3, \mathrm{~A} 2, \mathrm{~A} 1, \mathrm{AO}=1101$ allows a negativegoing pulse on $\overline{W R}$ to preset control register bits B1-B6.

## Control Register Description

The Display Sequencer internal control register is a 26-bit, serial-shift register that receives control-bit data from the Measurement Processor. Table 3-10 lists the control signal name(s) associated with each register bit. Bit number 1 receives the data from the DIO pin (via the Processor Interface) after one low-to-high transition on the $\overline{W R}$ input pin ( $A 3=A 2=A 1$ $=A 0=0$ ). Bit number 26 receives this data after 25 more low-to-high transitions on the $\overline{W R}$ input. Bit number 26 is the most-significant bit position of the internal shift register.

RD5-RD0: Data inputs to the internal RAM. The RAM address comes from a three-bit, binary up-counter. To write data into the RAM, the first six bits are loaded into the control register with the RAM data word. With $A 3, A 2, A 1, A 0=0001$, a negativegoing pulse on the $\overline{W R}$ input will write the data into RAM. To set the RAM address, the RAM load mode must be enabled. In RAM load mode, a low-to-high transition on the $\overline{W R}$ input (with $A 3, A 2, A 1, A 0=0010$ ) will increment the RAM address by one. There are eight consecutive RAM locations (addresses 000 to 111); the address counter will increment to 111, then wrap around to 000. Strobing RESET resets the counter to 000. See the Display Sequencer detailed description to find out what the RAM outputs do.

Table 3-10
Shift Register 1 Control Bit Data

| Bit Nr | Control Signal Name(s) |  |  |
| :---: | :---: | :---: | :---: |
| 1 | AS2 | RD5 | AC3 |
| 2 | AS1 | RD4 | AC2 |
| 3 | ASO | RD3 | AC1 |
| 4 | ZAP | RD2 |  |
| 5 | B1S2 | RD1 | BC3 |
| 6 | B1S1 | RDO | BC2 |
| 7 | B1S0 |  | BC1 |
| 8 | B1SLOPE |  | BC0 |
| 9 | B2S2 |  |  |
| 10 | B2S1 |  |  |
| 11 | B2S0 |  |  |
| 12 | B2SLOPE |  |  |
| 13 | VM1 |  |  |
| 14 | VMO |  |  |
| 15 | HM1 |  |  |
| 16 | HMO |  |  |
| 17 | DD |  |  |
| 18 | SSE |  |  |
| 19 | B ENDS A |  |  |
| 20 | H4 |  |  |
| 21 | H3 |  |  |
| 22 | H2 |  |  |
| 23 | H1 |  |  |
| 24 | H0 |  |  |
| 25 | FSEL |  |  |
| 26 | CBEN |  |  |

The RD5-RD0 bits also go to the inputs of an internal RAM comparator. The RAM outputs are sensed by the other comparator input. If the two inputs match, the comparator output will be high. The RAM comparator output can be read by the Measurement Processor through the processor interface.

AC3-AC1: The A Trigger CPLG select bits. $\mathrm{BC} 3-\mathrm{BCO}$ are the B Trigger CPLG and SLOPE select bits. To write these bits into the trigger coupling circuits, the Measurement Processor loads the control register as follows: Bits 1, 2, and 3 are set to $A C 3, A C 2$, and $A C 1$ respectively, and the $A$ SLOPE output is set to AC0. Bits $5,6,7$, and 8 are set to $\mathrm{BC} 3, \mathrm{BC} 2, \mathrm{BC} 1$, and BC 0 respectively. The RAM load mode is enabled, the force
mode is enabled, the force B1/B2 feature is disabled, and THO is strobed once (or RESET is strobed once while THO is high). At this point, output pins ATS2, ATS1, ATSO, and A SLOPE are set to AC3, AC2, AC1, and AC0 respectively; and output pins BTS2, BTS1, BTSO, and B SLOPE are set to BC3, BC2, $\mathrm{BC1}$, and BCO respectively. The Measurement Processor then strobes the latches in the Trigger Coupling Select Logic circuits to make the trigger coupling selections. The RAM load mode is then disabled to resume normal Display Sequencer operation.

AS2, AS1, AS0: A Trigger SOURCE select bits. See Table 3-11 for the bit encoding of the control signals when not loading the RAM or coupling circuits.

Table 3-11
Trigger Source Select

| AS2 | AS1 | AS0 | SOURCE |
| :---: | :---: | :---: | :--- |
| 0 | 0 | 0 | CH 1 |
| 0 | 0 | 1 | CH 2 |
| 0 | 1 | 0 | $\mathrm{CH} 1+\mathrm{CH} \mathrm{2}$ |
| 0 | 1 | 1 | CH 3 |
| 1 | 0 | 0 | CH 4 |
| 1 | 0 | 1 | Line |
| 1 | 1 | 0 | -- |
| 1 | 1 | 1 | VERT MODE |

For any binary code except 111; AS2, AS1, and ASO are presented on output pins ATS2, ATS1, and ATSO respectively after a THO rising edge. For binary code 111, the data on the three output pins will correspond to the channel being enabled for display; it alternates as the channel displays alternate and change state on the rising edges of THO. The RAM load mode is disabled to get the A Trigger SOURCE to alternate.

ZAP: Setting this bit high forces the $\overline{\text { ZEN }}$ output low. This bit is low to allow normal operation of the ZEN output.

B1S2, B1S1, B1S0: B1 Trigger SOURCE select bits. Bit encoding is the same as the encoding for the A Trigger SOURCE select bits.

B2S2, B2S1, B2S0: B2 Trigger SOURCE select bits. Encoded the same as A Trigger SOURCE select bits, except that code 111 does not select VERT Mode trigger. Selection between B1 SOURCE and B2 SOURCE is normally made with the DS (delay select) output signal. $D S=1$ selects $B 1$, and $D S=0$ selects B2. If the B1 select bits are 111 and the B1 SOURCE is selected (not forced), then the data on output pins BTS2, BTS1, and BTS0 will track with the selected vertical channel (similar to the A Trigger SOURCE select outputs).

B1 SLOPE, B2 SLOPE: B Trigger SLOPE bits. One of these two bits is presented on the B SLOPE output pin (if B SLOPE isn't being forced), in the same way that the B1 and B2 sources are selected. When B1 SOURCE is selected, then B1 SLOPE is also selected, and B2 SLOPE gets selected when B2 SOURCE is selected.

VM1, VM0: Vertical MODE control bits. See Table 3-12 for encoding.

Table 3-12
Vertical MODE Select

| VM1 | VM0 | MODE |
| :---: | :---: | :--- |
| 0 | 0 | Not used |
| 0 | 1 | Chop Mode |
| 1 | 0 | Alt Mode (with no measurement) |
| 1 | 1 | Alt Mode (with measurement) |

HM1, HM0: Horizontal MODE control bits. See Table 3-13 for encoding.

Table 3-13
Horizontal MODE Select

| HM1 | HM0 | MODE |
| :---: | :---: | :--- |
| 0 | 0 | A only |
| 0 | 1 | ALT |
| 1 | 0 | B only |
| 1 | 1 | X-Y |

Table 3-14
Holdoff Counter Encoding

| H4 | H3 | H2 | H1 | H0 | Count Length | H4 | H3 | H2 | H1 | H0 | Count Length |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 10000 |
| 0 | 0 | 0 | 0 | 1 | 2 | 1 | 0 | 0 | 0 | 1 | 20000 |
| 0 | 0 | 0 | 1 | 0 | 5 | 1 | 0 | 0 | 1 | 0 | 50000 |
| 0 | 0 | 0 | 1 | 1 | 5 | 1 | 0 | 0 | 1 | 1 | 50000 |
| 0 | 0 | 1 | 0 | 0 | 10 | 1 | 0 | 1 | 0 | 0 | 100000 |
| 0 | 0 | 1 | 0 | 1 | 20 | 1 | 0 | 1 | 0 | 1 | 200000 |
| 0 | 0 | 1 | 1 | 0 | 50 | 1 | 0 | 1 | 1 | 0 | 500000 |
| 0 | 0 | 1 | 1 | 1 | 50 | 1 | 0 | 1 | 1 | 1 | 500000 |
| 0 | 1 | 0 | 0 | 0 | 100 | 1 | 1 | 0 | 0 | 0 | 100000 |
| 0 | 1 | 0 | 0 | 1 | 200 | 1 | 1 | 0 | 0 | 1 | 200000 |
| 0 | 1 | 0 | 1 | 0 | 500 | 1 | 1 | 0 | 1 | 0 | 500000 |
| 0 | 1 | 0 | 1 | 1 | 500 | 1 | 1 | 0 | 1 | 1 | 500000 |
| 0 | 1 | 1 | 0 | 0 | 1000 | 1 | 1 | 1 | 0 | 0 | 100000 |
| 0 | 1 | 1 | 0 | 1 | 2000 | 1 | 1 | 1 | 0 | 1 | 200000 |
| 0 | 1 | 1 | 1 | 0 | 5000 | 1 | 1 | 1 | 1 | 0 | 500000 |
| 0 | 1 | 1 | 1 | 1 | 5000 | 1 | 1 | 1 | 1 | 1 | 1 |

a Strobing RESET presets the holdoff counter to 499999 to simplify testing.

DD: Dual-delay control bit. $D D=1$ for dual delay (delta time), and $D D=0$ for single delay.

SSE: SGL SEQ enable. SSE = 1 for single sequence mode or 0 for repetitive mode.

B ENDS A: $B$ ends $A$ enable (active high).

H4, H3, H2, H1, HO: Holdoff time. Encoded as in Table 3-14. With the TEST pin held high for normal operation.)

FSEL: Chop frequency select bit. With 10 MHz on the TC input pin, FSEL $=1$ provides a chop frequency of 625 kHz ; FSEL $=0$ produces 1.25 MHz ( 625 kHz is used).

CBEN: Chop blank enable bit. $C B E N=1$ allows the chop blanking signal to be passed out the BLANK output pin (when ROR is high); CBEN $=0$ inhibits chop blanking.

## Display Sequencer Operation

The internal RAM is programmed for the desired vertical channel display sequence, for both CHOP and ALT Vertical Modes. In ALT mode, the RAM also controls the horizontal display control outputs. In CHOP mode, the RAM still controls the vertical channel displays, but different logic controls the horizontal display selection.

RAM data bits RD5, RD4, and RD3 are programmed for a particular channel display (see Table 3-15).

Table 3-15
Display Sequencer Channel Select Logic Bits

| RD5 | RD4 | RD3 | Channel |
| :---: | :---: | :---: | :--- |
| 0 | 0 | 0 | CH 1 |
| 0 | 0 | 1 | CH 2 |
| 0 | 1 | 0 | $\mathrm{CH} \mathrm{1}+\mathrm{CH} 2$ |
| 0 | 1 | 1 | CH 3 |
| 1 | 0 | 0 | CH 4 |

Bit RD2 selects between the A Sweep display and the B Sweep display (only used in ALT Vertical Mode (with measurement). The A Sweep is displayed if this bit is set high (outputs HD1, HDO = 01), otherwise the B Sweep is displayed (outputs HD1, HDO = 10). Bit RD1 controls the DS (delay select) output pin in ALT Vertical Mode (with or without measurement). Finally, bit RDO marks the last state in a display sequence. When the RDO bit goes high, the sequencer finishes its current state and jumps back to the initial state (RAM address 000 is the initial state). In ALT Vertical Mode, the sequencer will advance to the next state either on each rising edge of the trigger holdoff pulse (ALT Vertical Mode with measurement), or on every other rising edge of the trigger holdoff pulse (ALT Vertical Mode with no measurement).

The first type of ALT Mode is used when there is an intensified zone (with or without an accompanying B Sweep) for only one or two of the displayed channel(s); every display state can be completely specified by programming the RAM properly (no more than eight display states are ever needed for any measurement display sequence; hence, the RAM is limited to eight addresses). The second type of ALT Mode is used when there are intensified zones and B Sweeps for all channels displayed. In this mode, HD1 and HDO automatically alternate between the A sweep and the B Sweep on each rising edge of the trigger holdoff pulse. Whenever HD1 and HD0 switch from the B Sweep back to the A Sweep, the vertical sequencer advances to its next state. This second type of ALT Vertical Mode is used only when more than eight RAM locations are needed to define a long display sequence in ALT Horizontal Mode.

In ALT Vertical Mode, the vertical and horizontal display enable outputs are initialized as follows: the trigger holdoff output is forced high (via the processor interface), RESET is strobed, then trigger holdoff is unforced to allow sweeps to occur. This procedure ensures that the display enable and trigger source outputs are initialized to the first state of the programmed display sequence.

In CHOP Vertical Mode, the leading edge of the chop blanking pulses advance the vertical display enable outputs. RAM bits RD5, RD4, and RD3 still determine the vertical channel displayed, and RAM bit RDO marks the last display state in the sequence. RAM bits RD2, and RD1 are not used in CHOP Mode. Other circuitry, clocked by the trigger holdoff pulse, drives the horizontal display control outputs. The same initialization procedure as described above for ALT Vertical Mode is used. However, only the trigger source and horizontal display enable outputs are initialized. The vertical-display-enable outputs cycle at the CHOP rate. Table 3-16 specifies the behavior of the horizontal- display-enable outputs for all horizontal and vertical modes.

## Trigger Holdoff Timer

When the B ENDS A control bit is low, the holdoff timer is triggered by the rising edge of $\overline{A G A T E}$. When the B ENDS A control bit is high, the holdoff timer is triggered by either the rising edge of $\bar{B}$ GATE, or the rising edge of $\bar{A}$ GATE, whichever occurs first. The THO output pin will go high immediately, and go low after the programmed number of holdoff oscillator cycles. In SGL SEQ Mode (again, with the TEST input pin high), the EOSS (end of single sequence) flag will go high and the THO output will stay high after the last A Sweep of the programmed sequence. Strobing RESET will reset the EOSS flag, and set the THO output back low again, if THO hasn't been forced high via the Measurement Processor interface.

HOLDOFF OSCILLATOR. A relaxation oscillator circuit formed by U601, Q600, Q601 and associated components is connected between the OSC OUT and OSC RST pins to provide the input count pulses to the holdoff timer. The HOLDOFF voltage applied to the base of Q600 sets up a charging current into timing capacitor C600. When the holdoff timer is inactive, the OSC RST output pin is high, and C600 is held discharged. With the capacitor discharged, the output of the oscillator is held high. When a rising edge of $\bar{A}$ GATE (or $\bar{B}$ GATE in $B$ ends $A$ mode) occurs, the OSC RST output will go low and allow the voltage across C600 to ramp up. When this voltage crosses an upper threshold, the output of U601 at pin 7 goes low. This negative transition increments the internal hoidoff counter, and causes the OSC RST output to go high, again discharging C600. When the voltage drops below a lower threshold, the oscillator output again goes high to repeat the oscillation cycle. After the last negative transition on the OSC OUT pin for a particular count length, the OSC RST output will go high and stay there until the next time the THO timer is triggered.

Table 3-16
Horizontal and Vertical Display Response

| Delay and Vertical Modes |  |  | HORIZONTAL CONTROL SIGNAL OUTPUTS |  |  |  |  | Readout Active$(\overline{R O R}=0)$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Signal <br> Names | Readout Inactive ( $\overline{\mathrm{ROR}}=1$ ) |  |  |  |  |
|  |  |  | Horizontal Modes (HM1 HMO) |  |
| DD | VM1 | VMO |  | A Only (0 0) | A Alt B (01) | B Only $\left(\begin{array}{ll} 1 & 0 \end{array}\right)$ | $\begin{aligned} & X / Y \\ & \left(\begin{array}{ll} 1 & 1 \end{array}\right) \end{aligned}$ |  |
| 0 0 | $\begin{aligned} & 0 \\ & \text { or } \\ & 0 \end{aligned}$ | 0 1 |  | $\begin{array}{cl} \text { NSSS } & \text { (a) } \\ \text { DS } & \\ \text { HDO } & \\ \text { HD1 } & \\ \text { TS } & \end{array}$ | $\begin{gathered} 1 \\ \mathrm{HI} \\ \mathrm{HI} \\ \mathrm{LO} \\ \mathrm{LO} \end{gathered}$ | $\begin{gathered} 2 \\ \mathrm{HI} \\ \text { (d) } \\ \hline \mathrm{HDO} \\ \mathrm{HD} 1 \end{gathered}$ | $\begin{gathered} 1 \\ \mathrm{HI} \\ \mathrm{HI} \\ \mathrm{HI} \\ \mathrm{LO} \end{gathered}$ | (b) <br> HI <br> LO <br> HI <br> LO | (c) <br> (c) <br> LO <br> LO <br> LO |
| 1 1 | $\begin{aligned} & 0 \\ & \text { or } \\ & 0 \end{aligned}$ | 0 1 | $\begin{gathered} \text { NSSS (a) } \\ \text { DS } \\ \text { HDO } \\ \text { HD1 } \\ \text { TS } \end{gathered}$ | $\begin{gathered} 2 \\ \text { (d) } \\ \mathrm{HI} \\ \mathrm{LO} \\ \mathrm{LO} \end{gathered}$ | $\begin{gathered} 4 \\ \text { (e) } \\ \text { (d) } \\ \text { HDO } \\ \text { HD1 } \end{gathered}$ | $\begin{gathered} 2 \\ \text { (d) } \\ \mathrm{LO} \\ \mathrm{HI} \\ \mathrm{LO} \end{gathered}$ | (b) <br> LO <br> HI <br> HI <br> LO | (c) <br> (c) <br> LO <br> LO <br> LO |
| 0 | 1 | 0 | $\begin{gathered} \text { NSSS (a) } \\ \text { DS } \\ \text { HD0 } \\ \text { HD1 } \\ \text { TS } \end{gathered}$ | $\begin{aligned} & \text { (f) } \\ & \mathrm{HI} \\ & \mathrm{HI} \\ & \mathrm{LO} \\ & \mathrm{LO} \\ & \hline \end{aligned}$ | $\begin{gathered} \text { (f) } \\ \mathrm{HI} \\ \text { (d) } \\ \hline \mathrm{HDO} \\ \text { HD1 } \end{gathered}$ | (f) <br> HI <br> LO <br> H <br> LO | (b) <br> HI <br> H <br> H <br> LO | (c) <br> (c) <br> LO <br> LO <br> LO |
| 0 | 1 | 1 | $\begin{gathered} \text { NSSS (a) } \\ \text { DS } \\ \text { HDO } \\ \text { HD1 } \\ \text { TS } \end{gathered}$ | $\begin{aligned} & \mathrm{g}) \\ & \mathrm{HI} \\ & \mathrm{HI} \\ & \mathrm{LO} \\ & \mathrm{LO} \end{aligned}$ | $\begin{gathered} \text { (g) } \\ \text { H } \\ \text { (h) } \end{gathered}$ | (g) <br> HI <br> LO <br> HI <br> LO | (b) <br> HI <br> H <br> HI LO | (c) <br> (c) <br> LO <br> LO <br> LO |
| 1 | 1 | 0 | $\begin{gathered} \text { NSSS (a) } \\ \text { DS } \\ \text { HD0 } \\ \text { HD1 } \\ \text { TS } \\ \hline \end{gathered}$ | (f) <br> (i) <br> HI <br> LO <br> LO | $\begin{gathered} \text { (f) } \\ \text { (i) } \\ \text { (h) } \\ \hline \text { HDO } \\ \text { HD1 } \end{gathered}$ | (f) <br> (I) <br> LO <br> HI <br> LO | (b) <br> (i) <br> HI <br> HI <br> LO | (c) <br> (c) <br> LO <br> LO <br> LO |
| 1 | 1 | 1 | $\begin{gathered} \text { NSSS (a) } \\ \text { DS } \\ \text { HD0 } \\ \text { HD1 } \\ \text { TS } \end{gathered}$ | (g) <br> (h) <br> HI <br> LO <br> LO | (g) <br> (h) <br> (h) <br> HDO <br> HD1 | (g) <br> (h) <br> LO <br> HI <br> LO | (b) <br> (h) <br> HI <br> HI <br> LO | (c) <br> (c) <br> LO <br> LO <br> LO |

NOTES:
(a) NSSS = Complete A Sweep cycles in a single sequence.
(b) Not applicable in single sequence mode.
(c) Signal state not affected by readout.
(d) Signal changes state after each rising edge of THO; initialized to a high state in single sequence mode.
(e) Signal changes state after every other rising edge of THO; it is initialized to a high state in SGL SEQ mode.
(f) NSSS = Two times the number of states programmed into the vertical sequencer. In ALT Vertical Mode with no measurement, the vertical sequencer advances to its next state at the end of every other A GATE.

## NOTES (cont):

(g) NSSS = The number of states programmed into the vertical sequencer.
(h) Programmable with the vertical sequencer.
(i) Programmable with the vertical sequencer. There are two A Sweeps per vertical display state.

## Sweep Gate Detection

Display Logic IC U600 also contains sweep gate detect latches that can be read out and reset via the Measurement Processor interface. The A GATE detect latch output will go high on the rising edge of $\bar{A}$ GATE after a falling edge of $\bar{A}$ GATE, if the $\overline{M G E}$ signal is low (i.e., the latch is armed by MGE). The B GATE detect latch output goes high when B GATE goes low (level sensitive). The A GATE latch is reset on the leading edge of the A/B RESET signal, so that the latch will not miss an A GATE occurring before the end of the latch reset interval. The B GATE latch resets when the A/B RESET signal is low.

## Chop Clock

The clock frequency applied to the TC input pin is either divided by 8 (FSEL $=0$ ), or divided by 16 (FSEL $=1$ ), producing a positive-going pulse at the BLANK output pin (when enabled) with a width equal to about two times the period of the clock signal on the TC input. To produce phase skewing, the chop frequency divider circuit is forced to skip ahead by four TC clock periods on a rising edge of $\overline{A G A T E}$. This skipping is gated on and off by applying a low-frequency clock signal (about 1 kHz from the Calibrator circuit) to the LFC (low-frequency clock)
input pin. Internally, the LFC signal is divided by two, and when the resulting square wave is high, count skip-ahead is enabled.

## Readout Interface

The Readout Interface accepts inputs from the $\overline{\text { ROR }}$ and $\overline{R O B}$ pins, and drives the BLANK output pin. When $\overline{R O R}$ is high, the BLANK output is controlled by the chop blank signal (when enabled by the CBEN control bit).

When the $\overline{R O R}$ input is low, chop blanking is disabled and the $\overline{R O B}$ input is inverted and allowed to control the BLANK output. When the $\overline{R O R}$ input goes from low to high, the BLANK output remains connected to the readout blank signal for an additional four to six TC clock periods. Normally, the $\overline{\mathrm{ROB}}$ input will be low during this time so that the BLANK output will be high to mask vertical source-switching transients. The HD1, HD0, and TS outputs are disabled two to four TC periods after $\widehat{R O R}$ goes low, and are again enabled two TC periods before the BLANK output is disconnected from the readout blank signal ( $(\overline{\mathrm{ROB}})$. For any readout request cycle, the $\overline{R O R}$ input remains low for greater than six TC clock periods. Relative timing of ROR, BLANK, HDO and HD1 (HDx), TS, and vertical channel enables ( $C H \times E N$ ) is shown in Figure 3-2.


Figure 3-2. Readout interface relative signal timing.

## Trigger Logic IC (FLIC)

The Trigger Logic IC or FLIC (fast-logic integrated circuit, U602 Diagram 4) does most of the fast logic functions required to run the oscilloscope. The functions are: A Sweep control, B Sweep control and measurement gate generation, Z-Axis control, and trigger status detection.

The A Sweep logic generates the A Sweep gate signal (A GATE), and provides trigger status information about the state of the A Trigger. The B Sweep logic interfaces to the Delay Time Comparators (Diagram 3) and generates the B Sweep gate ( $B$ GATE) and measurement gate signals. There is also some logic that monitors the B Trigger signal status when making voltage measurements with the B Trigger circuit. The Z-Axis control logic provides outputs for controlling the crt beam intensity.

The Trigger Logic circuit is done in an ECL (emittercoupled logic) gate array, and all inputs and outputs are compatible with standard ECL components.

## Pin Description

The following is a description of the fast logic pin outs (see Figure 3-3).

BLANK: Blanking input, from the Display Logic IC (U600).

HD1, HDO: Horizontal display select inputs, from the Display Logic IC.
$\overline{\text { ZEN }}:$ Z-Axis enable input, from the Display Logic IC. Active low.

BUSY: Counter busy signal. Not used in the 2246 1Y and $2246 \operatorname{Mod} A$.

ATRIG: A Sweep trigger input.

EOAS: End of A Sweep. This signal goes high when the A Sweep ramp crosses its end-of-sweep threshold.

THO: Trigger holdoff input from the Display Logic IC.


Figure 3-3. Trigger Logic IC (FLIC, U602) pin-out diagram.

SIN: Strobe input. Latches data into the internal register. Active low.

A1, A0: Address inputs. See Table 3-17 for addressing codes.

EOBS: End of B Sweep. This signal goes high when the B Sweep ramp crosses its end-of-sweep threshold.

DLY12: Input from first delay comparator. The comparator for the delay input switches from low to high after the end of either the first or the second sweep delay.

DLY2: Input from second delay comparator. This comparator normally switches from low to high after the end of the second sweep delay (in dual-delay mode).

B TRIG: B Sweep trigger input.
$\overline{\text { MGE : Measurement gate enable input from }}$ the Display Logic IC. Active low.

MSEL: Measurement select input. MSEL=1 causes the DLY12 signal rising edges to sample the B TRIG input in strobed volts measurements. MSEL=0 selects the DLY2 signal rising edges.

DS: Delay select signal from the Display Logic IC. DS=1 selects first delay.

DATA: Data input to the internal, control shift register.

S1: Crt beam-intensity control output. Turns on the beam current for the A Sweep displays. Active Iow.

S2: Crt beam-intensity control output. Turns on the beam current for the B Sweep displays. Active low.

S3: Crt beam-intensity control output. Turns on the beam current for the A Sweep intensified zone displays. Active low.

S4: Crt beam-intensity control output. Turns on the beam current for the Readout displays. Active low.

A GATE: A Sweep gate output. Starts the A Sweep ramp. Active high.

TDO: Trigger data output. Data to be read is selected via the A1 and A0 inputs (see Table 3-17).

B GATE: B Sweep gate output. Starts the B Sweep ramp. Active high.
$\overline{B U B}: B$ Sweep unblanking output. Active low.

C GATE : Measurement gate output. Not used externally in the 22461 Y and 2246 Mod A.

Table 3-17
Trigger Logic IC Addressing Logic

| A1 | A0 | Output of TDO pin | Action when <br> SIN Strobed |
| :--- | :--- | :--- | :--- |
| 0 | 0 | Strobed Volts Latch | DATA clocked <br> into Control Reg |
| 0 | 1 | Auto baseline Latch | Resets Auto base- <br> line Latch |
| 1 | 0 | A Trigger Latch | Resets A Trigger <br> Latch |
| 1 | 1 | Peak Volts Latch | Resets Peak Volts <br> Latch |

## Trigger Logic IC Control Register Description

The control register of U602 is an 8-bit shift register that receives input from the DATA pin. Bit 1 receives the data on a low-to-high transition on the $\overline{S I N}$ pin ( $\mathrm{A} 1=\mathrm{AO}=0$ ). Bit 8 receives this data after seven more low-to-high transitions on the $\overline{S I N}$ pin. Bit 8 is the most significant bit of the control register. Table 3-18 lists the control signal name associated with each control register bit.

DM1, DMO: These bits select the delay mode (see Table 3-19).

BRUN: This bit determines whether the $B$ Sweep is in RUNS AFTER delay mode or Triggered After delay mode. BRUN $=1$ selects RUNS AFTER Mode.

PM1, PM0: These bits select the peak volts detection mode as shown in Table 3-20.

Table 3-18
Control Register Signal-bit Names

| Bit | Name |
| :---: | :---: |
| 1 | DM0 |
| 2 | DM1 |
| 3 | BRUN |
| 4 | PM0 |
| 5 | PM1 |
| 6 | ZM0 |
| 7 | ZM1 |
| 8 | ARUN |

Table 3-19
Delay Mode Selection Control Bits

| DM1 | DM0 | Delay Mode |
| :---: | :---: | :--- |
| 0 | 0 | First delay set to zero |
| 0 | 1 | First and second delays set to zero |
| 1 | 0 | Normal delay mode |
| 1 | 1 | B Sweep disabled |

Table 3-20
Peak Volts Detection Mode Logic

| PM1 | PM0 | Peak Detection Mode |
| :---: | :---: | :--- |
| 0 | 0 | Nongated |
| 0 | 1 | Gated from end of delay to <br> end of A Sweep |
| 1 | 0 | Gated with C GATE |
| 1 | 1 | Gated with A GATE |

$\overline{\mathrm{a}} \overline{\mathrm{C} \text { GATE }}$ not used externally in 2246A.

ZM1, ZM0: These bits determine the intensified zone mode. See the $Z$-Axis logic discussion.

ARUN: This bit determines whether the A Sweep is in the free-run mode or in the triggered mode. ARUN=1 selects the free-run mode.

## A Sweep Logic

When ARUN is high, the A Sweep logic works as follows. A high on the THO input causes the A GATE output to go low. As soon as THO goes low, the A GATE output will go high and the A Sweep runs. At the end of the A Sweep there is a low-to-high transition on the EOAS input. That sets the the internal end-of-A-sweep latch causing the A GATE output to go low, and the A Sweep shuts off. This state exists during sweep retrace and the baseline stabilization period until the end of holdoff when the THO input once again goes high. That resets the end-of-A-sweep latch and starts another A Sweep cycle. Normally, the falling edge of A GATE will cause an externally generated pulse to be presented on the THO input, thus completing the loop and allowing the A Sweep to free-run (auto-level and auto triggered mode when the sweep is not triggered).

When ARUN is low, the operation is similar except that after a pulse on the THO input, A GATE won't go high until a low-to-high transition is presented on the A TRIG input (triggered sweep mode).

For either free-run or triggered modes, THO going high will cause the A GATE output to immediately go low, if the end-of-A-Sweep latch is set or not. Once the end-of-A-Sweep latch has been set, no more A Sweeps can happen until the THO input is pulsed (at the end of the holdoff). The end-of-A-Sweep latch can only be set with the EOAS input when A GATE is high.

The A Sweep logic of U602 also monitors the A TRIG input to latch certain A Trigger events. One latch (the auto-baseline latch) will set on any low-to-high transition on the A TRIG input. Another latch (the A Trigger latch) is level sensitive and will set when the A TRIG input is high. Both latches may be read out through the TDO (trigger-data out) pin, selected by the A1 and AO address input pins. That data is applied to the TDI (trigger data in) pin of U600 and placed in the Display Logic IC's internal register to be read by the Measurement Processor. Both latches may also be reset via the SIN pin (see description of $\mathrm{A} 1, \mathrm{AO}$, and $\overline{\mathrm{SIN}}$ input pins).

## B Sweep Logic

The B Sweep logic functions about the same as the A Sweep logic, except that more signals must be monitored to determine when the B Sweep can run. When DM1 and DMO $=11$, the B Sweep can't run at
all. When DM1 and DM0 $=10$, the B Sweep won't be allowed to run or trigger until the DLY12 input goes high while the A GATE signal is also high (the normal delayed sweep mode). When DM1 and DMO $=01$, the B Sweep will be allowed to run or trigger immediately after the A GATE signal goes high (no B Sweep delay). When DM1 and DMO $=00$, then the B Sweep will be allowed to run or trigger immediately after the A GATE signal goes high, if the DS (delay select) input is high. If DS is low, the B Sweep is allowed to run or trigger as soon as the DLY12 input goes high while the A GATE signal is also high.

The B Sweep logic behaves as follows. The B GATE signal goes high and $\overline{B U B}$ ( $B$ Sweep unblanking) goes low together when the appropriate conditions (described in the preceding paragraph) are met. A low-to-high transition on the EOBS input will then set the end-of-B-sweep latch, causing $\overline{B U B}$ to go high. B GATE doesn't go low until the A GATE signal goes low. This is used internally to generate the $\overline{\mathrm{S} 2}$ and S3 outputs in some modes, and is used externally to carry out the $B$ ends $A$ mode.

The DLY12 input goes to a level-sensitive latch; if $A$ GATE is high and DLY12 momentarily goes high, the latch will be set, so that the DLY12 input does not need to be held high throughout the sweep cycle. A high level on the THO input will cause the A GATE signal to go low. That resets this latch and causes the reset of the rest of the sweep logic, forcing $B$ GATE low and $\overline{B U B}$ high.

The DLY2 input also goes to a level sensitive latch. This second latch also gets reset when A GATE goes low. Together with the DLY1 latch output, A GATE, and the $\overline{M G E}$ input, the C GATE output signal gets generated (not used externally in the 2246 1Y and 2246 Mod A). C GATE goes low if A GATE is high, the DLY1 latch has been set, the DLY2 latch is still reset, and the $\overline{\mathrm{MGE}}$ input is low.

## Peak Volts Logic

The peak volts logic detects the positive and negative peaks of the B TRIG signal. It consists of a levelsensitive latch that can be gated by the C GATE signal, the A GATE signal, the DLY12 latch output, or continuously. The latch may be reset by strobing the $\overline{S I N}$ input with $A 1$ and $A 0$ set to 11 . The latch output
can be read at the TDO pin with A1 and A0 set this way. The Measurement Processor reads the state of the peak volts latch to determine when it has found the correct digital value of the signal peak being measured by the B Trigger Level Comparator.

The peak-detect latch output will go high when the $B$ TRIG input goes high (if the gating condition selected by PM1 and PM0 is satisfied). The latch output goes low when reset.

## Strobed Volts Logic

This logic samples the state of the B TRIG signal with the delay comparator outputs when making gated voltage measurements. The strobed volts latch consists of an edge-triggered flip-flop with a multiplexer driving the clock input, and the B TRIG signal driving the D input. When MSEL=1, the DLY12 latch output clocks the flip-flop. When MSEL=0, the DLY2 latch output clocks the flip-flop. The state of the flip-flop is read out at the TDO pin by the Measurement Processor when A1, A0 $=00$. The flip-flop is reset by strobing the $\overline{S I N}$ input with $A 1, A 0=11$.

## Z-Axis Logic

This logic drives the $Z$-Axis control outputs $(\overline{S 1}-\overline{S 4})$. These outputs have the following control action:
$\overline{\text { S1 }}$ Turns on the $A$ intensity current switch (active low).
$\overline{\mathrm{S} 2}$ Turns on the B intensity current switch (active low).
$\overline{\text { S3 }}$ Turns on the A intensified current switch (active low).
$\overline{\mathrm{S} 4}$ Turns on the Readout intensity current switch (active low).

Table 3-21 describes what the $\overline{\mathrm{S} 1}-\overline{\mathrm{S4}}$ outputs do as a function of ZM1, ZMO, HD1, HD0, $\bar{A}$ GATE, $\overline{B U B}, \overline{\mathrm{C}}$ GATE $, \overline{\mathrm{BUSY}}, \mathrm{BLANK}$, and $\overline{Z E N}$.

Table 3-21
Z-Axis Switching Logic

| ZM1 | ZMO | HD1 | HDO | $\overline{\mathbf{S 1}}$ | $\overline{\mathbf{S 2}}$ | $\overline{\mathbf{S 3}}$ | $\overline{\mathbf{S 4}}$ | Display Mode |
| :--- | :--- | :--- | :--- | :---: | :---: | :---: | :---: | :--- |
| 0 | 0 | 0 | 0 | 1 | 1 | 1 | (c) | Readout |
| 0 | 0 | 0 | 1 | (a) | 1 | (e) | 1 | A Sweep intensified by BUSY |
| 0 | 0 | 1 | 0 | 1 | (b) | 1 | 1 | B Sweep |
| 0 | 0 | 1 | 1 | (c) | 1 | 1 | 1 | X/Y |
| 0 | 1 | 0 | 0 | 1 | 1 | 1 | (c) | Readout |
| 0 | 1 | 0 | 1 | (a) | 1 | (d) | 1 | A Sweep intensified by C GATE |
| 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | Blank |
| 0 | 1 | 1 | 1 | (c) | 1 | 1 | 1 | X/Y |
| 1 | 0 | 0 | 0 | 1 | 1 | 1 | (b) | Readout |
| 1 | 0 | 0 | 1 | (a) | 1 | (b) | 1 | A Sweep intensified by BUB |
| 1 | 0 | 1 | 0 | 1 | (b) | 1 | 1 | B Sweep |
| 1 | 0 | 1 | 1 | (c) | 1 | 1 | 1 | X/Y |
| 1 | 1 | 0 | 0 | 1 | 1 | 1 | (c) | Readout |
| 1 | 1 | 0 | 1 | (a) | 1 | 1 | 1 | A Sweep no intensified zone |
| 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | Blank |
| 1 | 1 | 1 | 1 | (c) | 1 | 1 | 1 | X/Y |

Notes:
(a) = BLANK or $\overline{\mathrm{A} \text { GATE }}$
(b) = BLANK or $\overline{\mathrm{A} \text { GATE }}$ or $\overline{\mathrm{BUB}}$ or $\overline{\mathrm{ZEN}}$
(c) = BLANK
(d) = BLANK or $\overline{\text { A GATE }}$ or C GATE or $\overline{\text { ZEN }}$
$(e)=$ BLANK or A GATE or BUSY or $\overline{Z E N}$ or is a logical-or function.

## ECL-to-CMOS Level Shifters

The Trigger Logic IC, U602, is an ECL device. Its output signal swing is the standard ECL range of about 0.6 V . All the ECL logic devices in the 22461 Y and 2246 Mod $A$ are powered from the +5 V supply rather than a -5 V supply. The resulting output voltage swing is from about 4.5 V to about 3.9 V between the high and low ECL logic levels. As U602 must pass signals to the Display Sequencer IC (U600) at CMOS highs and lows (about 3.9 V and 0 $\checkmark$ respectively in this application), logic level translators are required. That job is done by an identical translator circuit for each of the three signals that must be sent. The circuit action of U603C, Q604, and Q605 (the $\overline{\mathrm{A} \text { GATE }}$ translator) is described.
The single-ended A GATE output signal of U602 at pin 14 is applied to pin 4 of U603B. With its other input pin left open, U603B is used as a line driver
only to produce a differential output signal. That differential signal is applied to the bases of a differential amplifier pair of pnp transistors (Q604 and Q605). The output signal is taken across R612 in the emitter of Q604. The emitter of Q605 is connected directly to ground. When the A GATE output of U602 is high (at 4.5 V ), the voltage applied to the base of Q604 is 4.5 V , and the voltage on the base of Q605 is 3.9 V . These voltage levels bias Q605 on and Q604 off, with a resulting output level across emitter resistor R612 of 0 V to the $\overline{\mathrm{A} G A T E}$ (active low) input of U600. When the A GATE output of U602 goes low at the end of the sweep, the bias voltage levels on Q604 and Q605 reverse, and Q604 is biased on (and Q605 off). Signal current through emitter resistor R612 develops a voltage of about 3.9 V (the unasserted level) to the A GATE input of U600.

## Display Logic Clock

The Display Logic clock signal at 10 MHz is generated by a transistor oscillator circuit composed of Q608, Y600, and associated components. The frequency of oscillation is controlled by a ceramic resonator, Y600, in the feedback path from the collector to the base of Q608.

## A AND B SWEEPS AND DELAY COMPARATORS (Diagram 5)

## Sweep Control Shift Registers

Two serial shift registers provide the control interface between the Measurement Processor and the $A$ and $B$ Sweep circuitry. Control bits loaded into registers U302 for A Sweep and U303 for B Sweep are serially clocked from the SR DATA line by the SR1 CLK pulse. The states of the loaded bits select the A and B Sweep timing by choosing the correct charging current and timing capacitor to provide the full range of sweep speeds. Other control bits loaded into the two registers select the delay voltage applied to the Delay Comparators and the output voltage from the VOLTS CAL circuit (used for measurement SELF CAL). Extra bits are shifted through the two shift registers into the Auxiliary Data Register (U1103, Diagram 3) via the AUX DATA signal line to control the trigger bandwidth, the TV Sync Detector switching, and the functions of 10X MAG, $\mathrm{X}-\mathrm{Y}$ display, and Vertical Comparator enabling.

## A and B Sweep Timing

Refer to Figure 3-4 for a simplified schematic of the A Sweep circuitry.

TIMING RESISTORS. The Sweep Timing resistors in resistor pack R313 are shared between the A Sweep and the $B$ Sweep circuitry; those in resistor pack R321 are divided between the two sweep circuits. Timing Resistor selection is done by multiplexers U308 and U307 for the A Sweep and by U310 and U311 for the B Sweep. The multiplexers are driven by the Measurement Processor via control bits loaded into Shift Register 1 (U302 and U303). (See Table $3-22$ for the control bit coding.)

SEC/DIV VAR CIRCUIT. Variable sweep speed is controlled by the TIME VAR voltage applied to operational amplifier U309B. The amplifier controls the current passing through Darlington transistor Q301 to the
voltage divider formed by resistor pack R313. The voltages at the taps of the voltage divider set the forward bias on the charging-current pass transistor, Q307, via operational amplifier U304. When the SEC/DIV VAR control is in its detent (calibrated) position, diode CR301 is reverse biased, and the divider formed by R311 and R314 between the +2.5 V reference and ground precisely sets the input voltage to the noninverting input of U309B. With a fixed voltage output from U309B, the current through Q301 and R313 is also a fixed value. When the SEC/DIV VAR control is rotated out of its detent position, the voltage at the junction of R309 and R310 decreases to forward bias CR301. The input voltage to U309B and, therefore, the current to R313 decreases in proportion to the amount of rotation of the SEC/DIV VAR control. A decreasing voltage at the output taps of R313 decreases the charging current through Q307 to increase the sweep ramp time.

A AND B SWEEP TIMING CAPACITORS. The timing capacitor selection circuitry is similar for the $A$ and the B Sweep, but the B Sweep has fewer range steps and doesn't require two selectable capacitors. Only the A Sweep timing capacitor selection is described; like components in the B Sweep circuit do the same job.

Timing capacitance for the A Sweep is made up of a combination of fixed, variable, stray, and selectable components. Sweep timing for the fastest A Sweep speeds is done with a combination of the fixed, variable, and stray capacitance and the selectable charging current supplied through R321, U308, Q307 and Q330. When the slower sweep speeds are selected, additional capacitors must be switched into the circuit to produce a longer charging time. The capacitors that are always in the A Sweep charging path are C315 (a fixed capacitor), C314 (a variable capacitor used to adjust the A Sweep timing at the fastest sweep speeds), and the stray circuit capacitance.

The base-to-collector junction capacitance Q330 changes as the voltage between the base and collector of Q330 increases during ramp up. At the fastest A Sweep speeds, that change would affect the timing at the start of the charging ramp. To compensate for the junction-capacity effect of Q330, transistor Q328 (connected as a diode) is added between the charging current path and the A Sweep Buffer output. The capacitive current through the reverse-biased junction of Q328 adds current to the output to make up for the current required to charge the base-to-collector capacity of Q330 in the input of the Sweep Buffer.


Figure 3-4. Simplified Sweep Circuit.

The selectable sweep timing capacitors come in a matched set of three capacitors, two for the $A$ Sweep timing (C307A and C307B) and one for the B Sweep timing (C307C). When added capacitance is needed for a sweep speed setting, the Measurement Processor loads selection control bits into Shift Register 1 (U302 for the A Sweep) that turn on either Q305 or Q306 or both. Assume that Q305 is biased on by a high control bit from pin 5 of U302. Capacitor C307B is then added in parallel to the capacitors in the charging path, and a longer ramp time is needed to reach the end-of-sweep voltage level. Control bits selecting the charging current are also loaded at the same time. See Table 3-22 for the A Sweep timing and control bit selections (as0-as5) and Table 3-23 for the B Sweep bit selections (bs0-bs4).

## Baseline Stabilizer

The job of the Baseline Stabilizer circuit (Q302, Q303, and Q304 for the A Sweep and Q315, Q316, and Q317 for the B Sweep) is to tie the start of the sweep ramps to the same fixed level for each sweep. Operation of the A Sweep stabilizer is described.

A differential circuit formed by Q302 and Q303 compares the A Sweep feedback signal on the base of Q303 against the reference voltage on the base of Q302, to control the base bias current to Q304 and thereby the sweep baseline level. Operational amplifier U309A generates the fixed reference that the baseline voltage level is compared against. The reference voltage amplifier has a gain of -0.8 (less than one and inverted); and, with +2.5 V applied to the inverting input and the noninverting input grounded, the output level is -2 V . Capacitor C305 filters the output to eliminate noise that could cause sweep start jitter. The filtered voltage is applied to the junction of R317, R354, and C305 and references both Baseline Stabilizer circuits.

## A and B Sweep Start

The A and B Sweep Start circuits operate the same way with like components in each doing the same job; only the A Sweep Start circuit is described. Sweep time may be divided into three periods:
baseline, run-up, and retrace (see Figure 3-5). Sweep start and length of sweep run-up is controlled by the A GATE and A GATE signals from the Trigger Logic IC (U602, Diagram 4).

A GATE SIGNALS. The A GATE and $\overline{\text { A GATE }}$ signals are applied via 8.2 V zener diodes (VR301 and VR302) to the bases of Q308 and Q309 in a differential amplifier configuration. The input circuit to the differential pair level shifts the ECL signals ( 4.3 V to 3.4 V ) to the proper biasing levels $(-3.9 \mathrm{~V}$ to -4.8 V ) for the bases of the differential amplifier transistors. Transistor Q326 in the emitter circuit of Q308 and Q309 is the current source for the differential pair. Transistor Q311 is part of the bias circuit for Q326 and provides feedback to the base of Q326 that controls the current provided to Q308 while the sweep is being held at the baseline level.

BASELINE STATE. In the baseline state (during sweep holdoff), Q308 and Q304 are on and Q309 is off, and the level at the collector of Q308 is held at -2.8 V . That voltage is buffered by the A Sweep Buffer (with about a 0.7 V rise across the base-toemitter junction of Q312) and fed back to the base of Q303 where it is compared with the -2 V reference produced by operational amplifier U309A. If the baseline voltage is too low compared to the output of U309A, Q303 (the retrace current regulator) is biased on a little harder. Additional base current is available to Q304, and it conducts harder to raise the output baseline voltage to the reference voltage level. The opposite action occurs if the baseline voltage is too high.

A smaller feedback loop formed by Q311 and R305 controls the gain of Q326 so that the standing current available (about 3 mA ) is just enough to keep Q304 biased on during the baseline state. When the states of the gate signals reverse, Q309 is turned on and Q308 turns off. The standing current then conducts through Q309 to rapidly pull the base of Q304 down to shut it off. When the base voltage reaches about $-2.7 \mathrm{~V}, \mathrm{Q} 333$ conducts. That action clamps the base voltage of Q304 (and the collector voltage of Q309) at that level and prevents Q309 from saturating so that it will have a short turn-off time when the sweep ends.

Table 3-22
A Sweep Timing Selections

| SPEED | $1{ }_{\text {timing }}$ | $\mathrm{C}_{\text {timing }}$ | as0 | as1 | as2 | as3 | as 4 | as5 | Min H.O. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 20 ns | 2 mA | C314/C315 | 0 | 0 | 1 | 1 | 1 | 0 | $2.0 \mu \mathrm{~s}$ |
| 50 ns | $800 \mu \mathrm{~A}$ | " | 0 | 0 | 0 | 0 | 1 | 0 | $2.0 \mu \mathrm{~s}$ |
| 100 ns | $400 \mu \mathrm{~A}$ | " | 0 | 0 | 1 | 0 | 0 | 1 | $2.0 \mu \mathrm{~s}$ |
| 200 ns | $200 \mu \mathrm{~A}$ | " | 0 | 0 | 0 | 1 | 0 | 1 | $2.0 \mu \mathrm{~s}$ |
| 500 ns | $80 \mu \mathrm{~A}$ | " | 0 | 0 | 0 | 0 | 0 | 1 | 2.0 ¢ |
| $1 \mu \mathrm{~s}^{\text {a }}$ | $40 \mu \mathrm{~A}$ | " | 0 | 0 | 1 | 0 | 0 | 0 | $2.0 \mu \mathrm{~s}$ |
| $2 \mu \mathrm{~s}^{\text {a }}$ | $20 \mu \mathrm{~A}$ | " | 0 | 0 | 0 | 1 | 0 | 0 | $4.0 \mu \mathrm{~s}$ |
| $5 \mu \mathrm{~s}^{\text {a }}$ | $8 \mu \mathrm{~A}$ | " | 0 | 0 | 0 | 0 | 0 | 0 | $10 \mu \mathrm{~s}$ |
| $1 \mu \mathrm{~s}$ | 4 mA | C307B | 0 | 1 | 1 | 0 | 1 | 0 | $2.0 \mu \mathrm{~s}$ |
| $2 \mu \mathrm{~s}$ | 2 mA | " | 0 | 1 | 0 | 1 | 1 | 0 | $4.0 \mu \mathrm{~s}$ |
| $5 \mu \mathrm{~s}$ | $800 \mu \mathrm{~A}$ | " | 0 | 1 | 0 | 0 | 1 | 0 | $10 \mu \mathrm{~s}$ |
| $10 \mu \mathrm{~s}$ | $400 \mu \mathrm{~A}$ | " | 0 | 1 | 1 | 0 | 0 | 1 | $20 \mu \mathrm{~s}$ |
| $20 \mu \mathrm{~s}$ | $200 \mu \mathrm{~A}$ | " | 0 | 1 | 0 | 1 | 0 | 1 | $40 \mu \mathrm{~s}$ |
| $50 \mu \mathrm{~s}$ | $80 \mu \mathrm{~A}$ | " | 0 | 1 | 0 | 0 | 0 | 1 | $100 \mu \mathrm{~s}$ |
| $100 \mu \mathrm{~s}$ | $40 \mu \mathrm{~A}$ | " | 0 | 1 | 1 | 0 | 0 | 0 | $200 \mu \mathrm{~s}$ |
| $200 \mu \mathrm{~s}$ | $20 \mu \mathrm{~A}$ | " | 0 | 1 | 0 | 1 | 0 | 0 | $400 \mu \mathrm{~s}$ |
| $500 \mu \mathrm{~s}$ | $8 \mu \mathrm{~A}$ | " | 0 | 1 | 0 | 0 | 0 | 0 | 1.0 ms |
| 1 ms | 4 mA | C307A | 1 | 0 | 1 | 0 | 1 | 0 | 2.0 ms |
| 2 ms | 2 mA | " | 1 | 0 | 0 | 1 | 1 | 0 | 4.0 ms |
| 5 ms | $800 \mu \mathrm{~A}$ | " | 1 | 0 | 0 | 0 | 1 | 0 | 10 ms |
| 10 ms | $400 \mu \mathrm{~A}$ | " | 1 | 0 | 1 | 0 | 0 | 1 | 20 ms |
| 20 ms | $200 \mu \mathrm{~A}$ | " | 1 | 0 | 0 | 1 | 0 | 1 | 40 ms |
| 50 ms | $80 \mu \mathrm{~A}$ | " | 1 | 0 | 0 | 0 | 0 | 1 | 100 ms |
| 100 ms | $40 \mu \mathrm{~A}$ | " | 1 | 0 | 1 | 0 | 0 | 0 | 200 ms |
| 200 ms | $20 \mu \mathrm{~A}$ | " | 1 | 0 | 0 | 1 | 0 | 0 | 400 ms |
| 500 ms | $8 \mu \mathrm{~A}$ | " | 1 | 0 | 0 | 0 | 0 | 0 | 1 s |

${ }^{\text {a }}$ used only during horizontal characterization.

Table 3-23
B Sweep Timing Selections

| SPEED | 1 timing | $\mathrm{C}_{\text {timing }}$ | bs0 | bs 1 | bs2 | bs3 | bs 4 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 20 ns | 2 mA | C329/C330 | 0 | 1 | 1 | 1 | 0 |
| 50 ns | $800 \mu \mathrm{~A}$ | " | 0 | 0 | 0 | 1 | 0 |
| 100 ns | $400 \mu \mathrm{~A}$ | " | 0 | 1 | 0 | 0 | 1 |
| 100 ms | $200 \mu \mathrm{~A}$ | " | 0 | 0 | 1 | 0 | 1 |
| 100 ms | $80 \mu \mathrm{~A}$ | 11 | 0 | 0 | 0 | 0 | 1 |
| $1 \mu \mathrm{~s}$ | $40 \mu \mathrm{~A}$ | " | 0 | 1 | 0 | 0 | 0 |
| $2 \mu \mathrm{~s}$ | $20 \mu \mathrm{~A}$ | 11 | 0 | 0 | 1 | 0 | 0 |
| $5 \mu \mathrm{~s}$ | $8 \mu \mathrm{~A}$ | 11 | 0 | 0 | 0 | 0 | 0 |
| $10 \mu \mathrm{~s}$ | 4 mA | C307C | 1 | 1 | 0 | 1 | 0 |
| $20 \mu \mathrm{~s}$ | 2 mA | " | 1 | 0 | 1 | 1 | 0 |
| $50 \mu \mathrm{~s}$ | $800 \mu \mathrm{~A}$ | " | 1 | 0 | 0 | 1 | 0 |
| $100 \mu s$ | $400 \mu \mathrm{~A}$ | " | 1 | 1 | 0 | 0 | 1 |
| $100 \mu \mathrm{~s}$ | $200 \mu \mathrm{~A}$ | " | 1 | 0 | 1 | 0 | 1 |
| $100 \mu \mathrm{~s}$ | $80 \mu \mathrm{~A}$ | 11 | 1 | 0 | 0 | 0 | 1 |
| 1 ms | $40 \mu \mathrm{~A}$ | 11 | 1 | 1 | 0 | 0 | 0 |
| 2 ms | $20 \mu \mathrm{~A}$ | 1 | 1 | 0 | 1 | 0 | 0 |
| 5 ms | $8 \mu \mathrm{~A}$ | 11 | 1 | 0 | 0 | 0 | 0 |

RUNUP STATE. With Q304 and Q308 off, the charging current from the timing circuit can begin charging the timing capacitors, and the voltage at the emitter of Q304 ramps up linearly. That ramp is buffered by the A Sweep Buffer (U310A and B and Q312) to drive the Horizontal Output Amplifier. As the ramp is running up, it is being compared with a fixed reference level by the Sweep End Comparators. When the ramp level reaches the comparison level, the A SWP END signal goes high. That signals the Trigger Logic IC, U602, to end the A GATE signal, and the sweep is switched to the retrace state. The sweep ramp is also being fed back to the base of Q303. At the point in the ramp that the base voltage of Q303 exceeds that on the base of Q302 (the -2 V baseline reference), Q303 is biased off and Q302 conducts. This biasing conditions disables the feedback loop that stabilizes the baseline voltage level, and it remains off until the
feedback voltage during the retrace period falls back to near the -2 V baseline reference voltage on the base of Q302.

RETRACE STATE. At the end of the sweep, the gate signals reverse state. Transistor Q309 is biased off, and Q308 is biased on. Retrace current supplied by Q308 quickly returns the voltage across the timing capacitor to a little below the baseline voltage level. That retrace current is regulated by Q311 and Q326 to produce a rapid, yet rate-controlled retrace. At the point of the fall in feedback voltage where Q303 starts to turn on, base current becomes available to Q304 to turn it on, and the feedback loop that stabilizes the voltage at the baseline level again becomes active.


Figure 3-5. A Sweep Start circuit waveforms.

## A and B Sweep Buffers

The A Sweep Buffer (Q310A and B, and Q312) and B Sweep Buffer (Q323A and B, and Q325) buffer the voltage ramp as the timing capacitors charge. In the A Sweep circuit, Q310A and Q310B are highimpedance FET amplifiers driving emitter follower Q312. The output signal from the emitter of Q312 is applied to the Delay Time Comparators, the End-ofSweep Comparators, fed back to the Baseline Stabilizer circuit, and sent to the Horizontal Output Amplifier (Diagram 6) as the A RAMP horizontal deflection signal.

## Sweep End Comparators

The sweep ramp signals must horizontally deflect the electron beam across the entire face of the crt. Comparators U316A, B, C, and D determine when the $A$ and $B$ Sweeps have reached the required amplitude. These comparators check the sweep voltage against the reference level that defines the end of the sweep and generate the A SWP END and B SWP END signals when that level is reached. The sweep-end signals are applied to the Trigger Logic IC (U602) so that device knows when the sweeps
are done. The Trigger Logic IC then switches the states of the A GATE or the B GATE signal (as appropriate) to reset the sweep circuitry to its baseline level.

## Delay Time Comparators

When the A Sweep ramp runs, its amplitude is compared against two delay levels by the comparators of U313. The differential outputs of the REF delay comparator change states when the A Sweep crosses the first delay level. The differential output signal from the delay comparator is applied to ECL line receiver U315C. That device has a high gain and produces a fast-rise signal at an ECL level. When the DLY END 0 (reference delay completed) is received by the Trigger Logic IC (U602, Diagram 4), a B GATE is produced to start the B Sweep in RUNS AFTER B Trigger mode. That B Sweep displays the applied waveform at the first (reference) delay setting. At the end of the delay in RUNS AFTER mode, the Trigger Logic IC begins watching for a $B$ Trigger signal that must occur before a B GATE is produced.

The differential output of the second delay comparator in U313 changes states when the A Sweep
ramp at pin 9 crosses the second (delta) delay level applied to pin 6. At that point, the DLY END 1 signal is produced at the output of U315A (pin 2) and applied to U602. A second B GATE signal is then produced to start another B Sweep ramp to display the signal at the delta delay setting.

## Delay Time Switching

The DELTA DELAY and REF DELAY voltage level are applied to multiplexer U301 from the DAC circuit. The Measurement Processor established those voltages based on the settings of the CURSOR/TIME POSITION controls made by the user from the front panel. Switch section U301A is held permanently switched to direct the DELTA DELAY signal to its output pin.

## HORIZONTAL OUTPUT AMPLIFIER (Diagram 6)

## Horizontal Preamplifier

Horizontal Preamplifier IC U802 converts singleended horizontal signals (A sweep, B sweep, horizontal readout, and X-Axis) into differential outputs to drive the crt horizontal deflection plates. The horizontal preamplifier signals are selected by the HDO and HD1 logic signals from Display Sequencer U600 on Diagram 4. Magnified sweep, beam find, horizontal positioning, and horizontal gain adjustments ( X 1 and X 10 ) are provided in U 802 and associated components.

The function of each pin of 4802 is as follows:

RO (Pin 1): RO HORIZ. Input for horizontal component of the readout display.

GA1 (Pin 2): Adjustment of R825 sets the horizontal X1 gain.

A RAMP (Pin 3): Input for the A Sweep signal.

GND (Pin 4): Ground connection for U802.
B RAMP (Pin 5): Input for the B Sweep signal.

MAG (Pin 6): Selects $\times 10$ magnified sweep when high or normal sweep when low. Magnified mode is selected when in $X-Y$ horizontal mode.
$X$ (Pin 7): $X-A X I S$. This is the $X$-Axis signal input when in $X-Y$ horizontal mode. The
signal source is the CH 1 trigger signal from U421A (Diagram 3). Adjustment of R827 sets the gain of the $X$-Axis signal.

HDO (Pin 8): Pin 8 (HDO) and pin 11 (HD1) are logic lines that select the horizontal input signal to output differentially at pins 18 and 19. Table 3-24 gives the selection logic.

Table 3-24
HDO and HD1 Logic

| HD1 | HD0 | Horiz Signal Selected |
| :---: | :---: | :---: |
| 0 | 0 | RO HORIZ |
| 0 | 1 | A SWEEP |
| 1 | 0 | B SWEEP |
| 1 | 1 | X-AXIS |

$V_{E E}$ (Pin 9): -5 V supply to U 802.
GA10 (Pin 10): Adjustment of R826 sets the horizontal $\times 10$ gain.

HD1 (Pin 11): See the description for HDO above.

ROUT (Pin 12): Horizontal Preamplifier differential output signal for the right deflection plate.

LOUT (Pin 13): Horizontal Preamplifier differential output signal for the left deflection plate.

BF (Pin 14): The BEAM FIND signal from U503 (Diagram 4) switch the Beam Find feature on or off. BEAM FIND on reduces the horizontal deflection to within the graticule area. Vertical deflection is also reduced and the intensity is set to a fixed viewing level to aid in locating off-screen, over-deflected, or under-intensified displays.

POSITION (Pin 15): Input for the horizontal position control signal. Multiplexer section U301B switches to reduce the range of the Horizontal POSITION control to match that of the Vertical POSITION controls when in $X-Y$ horizontal mode. When $X-Y$ display mode is selected, a low $\overline{X Y}$ signal on Pin 9 of U301B connects the pin 5 input to the horizontal position input of U802. The signal at pin 5 is a reduced horizontal positioning signal produced by the R353/R358 voltage divider.
$V_{\mathrm{CC}}($ Pin 16) $:+7 \mathrm{~V}$ supply to U 802 .

## Driver Amplifiers

The differential output current signal from U802 passes through common-base current amplifiers Q809 and Q810. These transistors drive current-tovoltage converters Q803-Q804 and Q807-Q808. Emitter followers Q804 and Q803 convert the current signal to a voltage signal to drive the complementary-FET output amplifiers, Q801 and Q802, to produce the negative-going deflection voltage. Emitter followers Q808 and Q807 convert the other side of the differential current to drive Q805 and Q806 to produce the positive-going horizontal deflection voltage.

The circuit of Q804 and Q803 is configured to respond rapidly to a negative-going feedback signal; the circuit of Q807 and Q808 is configured to respond quickly to the positive-going feedback signal. Zener diode VR802 and associated resistors R843 and R844 maintain the collector bias of Q803 and Q808 at 24 V .

Magnifier registration and horizontal readout centering is set by MAG REG potentiometer R809. Adjustment of R809 is done to balance the currents into the emitters of Q809 and Q810 to obtain the correct horizontal position of the readout within the graticule display area.

## Output Amplifier

The differential circuitry of both sides of the Horizontal Output Amplifier is similar; operation of only one side of the amplifier is described. Complementary-FET amplifiers Q801 and Q802 produce the negative-going horizontal signal to drive the left deflection plate. Two transistors are used to provide adequate power handling. Since the two gates are at different bias levels, signal voltage is applied to the gate of transistor Q801 via C803. Resistor R828, connected between the source and drain of Q801, is a parallel current path around Q801 that balances the power handling requirements of the two FETs. The amplifier FETs are high gain devices, and the overall gain must be reduced to maintain circuit stability at the faster sweep rates. To provide the high-frequency gain reduction, resistor R850 is in series with C802, from the source of Q802 to the drain of Q801, to damp the driving-energy to Q801 supplied by C802 during the sweep retrace transitions. Feedback resistor R806 provides positive feedback and sets the overall gain of the output amplifier stage. A parallel trimmer capacitor across the feedback resistor, C807, adjusts the 2 ns sweep timing for its best linearity. Impedance matching to the deflection plate and additional signal damping is provided by R802.

As the gate voltage of Q802 increases to follow the input ramp signal, the drain voltage goes negative from about 87 V toward the 15 V source voltage. At the same time, the signal on the gate of complementary-FET Q801 is reducing the current through Q801, thereby allowing its source voltage to fall. At the end of the ramp signal, the input voltage falls, and through the positive feedback, Q802 is rapidly biased off. That also biases Q801 on, and the energy stored in C802 quickly returns the deflection plate voltage back to its starting point.

## Common-Mode Stabilizer

Operation amplifier U801A compares the node voltage at the junction of R820, R821, and R822 to ground. Its output drives the amplifier input common-mode point (at the junction of R811 and R812). The purpose of this dc feedback circuit is to keep the average voltage level on the right and left horizontal deflection plates set to the center of the amplifier's dynamic operating range (about 70 V ).

## Z-AXIS, CRT, PROBE ADJUST, AND CONTROL MUX (Diagram 7)

## Z-Axis and Auto Focus Amplifiers

The $Z$-Axis and Auto Focus Amplifiers circuit operate on the same principle and both get their drive signal from the $Z$-Axis Focus Driver. However, the differences are enough that both circuits are described.

Z-AXIS AMPLIFIER. Intensity control signal current from the Z-Axis/Focus driver is applied to the $Z$-Axis amplifier via Q2707. That transistor acts as a current buffer amplifier. The input signal line is clamped at 5.4 V by Q2715 to prevent an overdrive of the Z Axis circuit. The Z-Axis Amplifier output transistors consist of Q2701 and Q2702 on one side of the complementary-symmetry totem-pole output amplifier and Q2703 and Q2704 on the other side. Two transistors are used on each side to divide the power handling requirements needed to drive the crt control grid. The crt grid capacity is large, and requires a relatively large amount of power to change the intensity level quickly.

In the base circuit of Q2704, CR2705 prevents the base-to-emitter voltage from exceeding 0.6 V . Zener diode VR2701 dc level-shifts the signal voltage level at the emitter of Q2705 for proper biasing of Q2704. The ac signal components are bypassed around VR2701 by C2703. Base biasing for Q2702 and Q2703 is taken from a seriesresistance divider formed by R2711, R2712, R2713, and R2714 between ground and the +130 V supply.

Base biasing for Q2701 is provided by R2715 and R2716 in series between ground and the +130 V supply.

A negative-going input signal to the base of Q2705 causes that transistor to decrease conduction, and the voltage at the top of C2705 goes negative following the input signal. Transistor Q2701 is biased on harder by the negative transition, and Q2704 decreases in conduction. At the $Z$-Axis output signal line (collector of Q2702), the increasing conduction causes the voltage to rise towards the +130 V supply level. A positive-going input signal has the reverse effect on the output signal. The full output-voltage swing of about 60 V is produced by a 3 mA current change of the $Z$-Axis Focus/Driver signal current.

Gain of the Z-Axis Amplifier stage is set by the feedback through R2708 and R2709 from the collector of Q2702 to the base of Q2705. The amplifier is compensated by the variable capacitor (C2704, Z-Axis Response) in parallel with the feedback resistors.

BEAM FIND. The Z-Axis portion of the BEAM FIND circuit consists of R2705 and Q2706. When BEAM FIND is active, Q2706 is biased on. This clamps the Z-Axis signal line via R2706 and raises the voltage at the base of Q2705 to a level that produces a bright trace.

## Auto Focus Amplifier

The Auto Focus Amplifier (Q2708, Q2709, Q2711, Q2712, and Q2713) uses a sample of the Z-Axis/Focus Driver signal current from W2701 to drive the auto-focus circuit. The input signal is inverted by Q2708 to drive Q2711 in a complementary fashion to Q2705 in the Z-Axis Amplifier circuit (as the opposite circuit action must happen to produce the correct auto-focus response). The auto-focus output amplifier is similar to the Z-Axis amplifier, but it uses only one complementary transistor on each side (not as much power is needed to drive the focus grid as needed to drive the intensity grid).

## Dc Restorers

The Z-Axis and the Auto Focus DC Restorers are similar in operation. Both circuits are described, but only the added portions of Auto Focus circuitry are included in the discussion of the Auto Focus circuit.

The Dc Restorers set the crt control-grid and focusgrid biases and couple the ac and dc components of
the $Z$-Axis and the Auto Focus Amplifier outputs to the crt grids. Direct coupling of the $\mathrm{Z}-\mathrm{Axis}$ and Auto Focus signals to the crt control grid is not employed because of the high potential differences involved. Refer to Figure 3-6 during the following discussion.

Z-AXIS DC RESTORER. Ac drive to the Z-Axis Dc Restorer circuit is obtained from pin 12 of T2204. The drive voltage has a peak amplitude of about $\pm 130 \mathrm{~V}$ at a frequency of about 18 kHz and is coupled into the Z-Axis Dc Restorer circuit through R2722 and C2713. The cathode of diode CR2704 is biased by Grid Bias potentiometer R2719 and referenced to ground via R2720. The ac-drive voltage is clamped to the voltage set by the Grid Bias potentiometer wiper whenever the positive peaks forward bias diode CR2704. Capacitor C2710 prevents significant loading of the potentiometer wiper voltage when CR2704 conducts.

The Z-Axis Amplifier output voltage, which varies between +16 V and +66 V , is applied to the Dc Restorer at the anode of CR2703. The ac-drive voltage holds CR2703 reverse biased until the voltage falls below the Z-Axis Amplifier output voltage level. At that point, CR2703 becomes forward biased and clamps the junction of CR2703, CR2704, and C2713 to the Z-Axis output level. Thus, the 18 kHz ac-drive voltage is clamped at two levels to produce a roughly square-wave 18 kHz signal with a positive dc-offset level.

The Dc Restorer is referenced to the -2750 V crt cathode voltage through CR2702 and R2723. Initially, both C2712 and C2711 charge up to a level determined by the difference between the $Z$-Axis output voltage and the crt cathode voltage. Capacitor C2712 charges from the Z-Axis output through R2721, R2723, CR2702, and CR2703, to the crt cathode. Capacitor C2711 charges through R2723 (a series damping resistor), CR2702, and CR2701 to the crt cathode.

During the positive transitions of the ac drive (from the lower clamped level toward the higher clamped level) the charge on C2712 increases due to the rising voltage. The voltage increase across C2712 is equal to the amplitude of the positive transition. The negative transition is coupled through C2712 to reverse bias CR2702 and forward bias CR2701. The increased charge of C2712 is then transferred to C2711 as C2712 discharges toward the Z-Axis output level. Successive cycles of the ac input to the Dc Restorer charge C2711 to a voltage equal to the initial level plus the amplitude of the clamped square-wave input.


Figure 3-6. Simplified diagram of the DC Restorer circuitry.

The charge held by C2711 sets the control-grid bias voltage. If more charge is added to that already present on C2711, the control grid becomes more negative (display dimmer). Conversely, if less charge is added, the control-grid voltage level becomes closer to the cathode-voltage level, and the display becomes brighter. During periods that C 2712 is charging, the crt control-grid voltage is
held constant by the long time-constant discharge path of C2711 through R2724.

Fast-rise and fast-fall transitions of the Z-Axis output signal are coupled to the crt control grid through C2711 to start the crt writing-beam current toward the new intensity level. The Dc Restorer output level
then follows the Z-Axis output-voltage level to set the new bias voltage for the crt control grid.

Neon lamps DS2702 and DS2701 protect the crt from excessive grid-to-cathode voltage if the potential on either the control grid or the cathode is lost for any reason.

AUTO FOCUS DC RESTORER. The action of the Auto Focus circuit has to be in reverse of the action of the Z-Axis circuit. The differential transistor pair of Q2708 and Q2709 provides drive to the Auto Focus Amplifier that is inverted in polarity to the Z-Axis signal. As the intensity increases (more beam current), the focus grid bias must become more positive to maintain the focus of the beam. Also, since the focus grid operates at a less negative level than the control grid, the Auto Focus DC Restorer is referenced to the -2750 V supply via a voltage divider chain.

The FOCUS potentiometer (R2758) voltage is taken across the middle resistor of the divider string to provide an adjustable focus voltage that sets the nominal focus level. Capacitor C2758 filters the reference supply voltage for the focus circuit.

## Volts Cal Signal Source

This circuit provides the precision voltages required for setting the voltage measurement constants during the SELF CAL routine. Ground is connected to the vertical input when GND Input Coupling is selected.

Five voltages are selected from a precision voltage divider, R921, and multiplexed through U931 to the vertical inputs at the appropriate time during the SELF CAL routine. Selection is controlled by three binary coded lines (VOLT CAL 0, 1, 2) from U303. Those control bits and the selected output voltage may be checked one at a time by running the VOLT REF exerciser from the Service Menu.

## Control Multiplexer

Multiplexer U506, controlled by Data Latch U2313 on Diagram 11, selects the A INTEN, B INTEN, and READOUT control levels and probe code voltages to be sent on the PROBE MUX signal line to multiplexer U2309 on Diagram 11. The bit coding is shown in Table 3-25. The selected output from U2309 is applied to the A-to-D Converter (U2306, Diagram
11) where it is digitized and sent to the Measurement Processor.

Table 3-25
Front-Panel Multiplexer Channel Select Bits

| CONTROL LINE |  | Analog <br> Signal <br> Selected |  |
| :---: | :---: | :---: | :---: |
| POT7 | POT6 |  |  |
| 0 | 0 | 0 | A INTEN |
| 0 | 0 | 1 | RO INTEN |
| 0 | 1 | 0 | CH 1 PROBE |
| 0 | 1 | 1 | CH 2 PROBE |
| 1 | 0 | 0 | CH 3 PROBE |
| 1 | 0 | 1 | CH 4 PROBE |
| 1 | 1 | 0 | B INTEN |
| 1 | 1 | 1 | ANALOG GND |

## Scale Illumination

Front-panel SCALE ILLUM control R905 varies the base current of Q905, Q907, and Q908 to set the intensity levels of the scale illumination bulbs (DS901, DS902, DS903).

## NOTE

Bulb life is extended by keeping SCALE ILLUM control set low or off except when full intensity is required.

## Probe Adjust Circuit

The Probe Adjust circuit generates a 0.5 V square wave signal at about 1 kHz . Operational amplifier U930A has a gain of about 4 . The +2.5 V reference on its noninverting input produces a little over 10 V at the output pin. That voltage is divided by the voltage divider formed by R936, CR936, and R937 for a peak amplitude of the signal of 0.5 V during the time CR936 is forward biased. When CR936 is reverse biased by the output of U930B, the Probe Adjust output voltage is pulled down to 0 V through R937 to ground.

Operational amplifier U930B is a free-running oscillator circuit with a period of about 1 ms . The oscillator frequency is determined mainly by the charging time constant of C935 and R935. The voltage divider formed by R938, R934, and R939 divides the +15 V supply to provide a positive voltage on pin 5 of the oscillator to get the circuit into oscillation. (When the circuit is oscillating, the feedback signal switches the pin 5 voltage between about +8 V to 0 V .) The gain of the amplifier is high enough to drive pin 7 to the positive supply voltage level at about 14 V , and the signal voltage level on pin 5 rises to a little over 8 V from the feedback current supplied by R933. The CLK 1 K signal taken from the junction of R934 and R939 is supplied to U600 and is used to skew the chop-clock frequency. The skew prevents the oscilloscope from triggering on the chop frequency when displaying multiple traces in CHOP Mode.

At that level CR935 is reverse biased, and CR936 is forward biased (by the output of U930A) to pass the Probe Adjust high level output signal current. Charging current through feedback resistor R934 charges C935 up from 0 V toward the output voltage level. As soon as the charge on C935 (and the voltage on pin 6 of U930B) reaches the voltage level on pin 5, the output level at pin 7 drops to about -5 V , and C935 must then begin discharging to the new voltage level. At that point CR935 is forward biased and that reverse biases CR936 so that the Probe Adjust output voltage drops to 0 V. Resistor R940, in series with CR935, limits current flow to protect U930 and CR935 in the event of a static discharge to the PROBE ADJUST output connector.

## Crt

The Trace Rotation adjustment, R911, varies the current through the Trace Rotation coil. The Trace Rotation coil is located between the crt face and the vertical and horizontal deflection plates, and it affects both the vertical and horizontal alignment of the trace.

The Geometry adjustment, R2784, varies the voltage level on the horizontal deflection-plate shields to control the overall geometry of the display (minimizes bowing of the display).

The Astigmatism adjustment, R2788, varies the voltage level on the astigmatism grid to obtain the best-focused display over the whole face of the crt.

## MEASUREMENT PROCESSOR (Diagram 8)

The Measurement Processor circuitry includes the Processor (U2501), the System RAM (U2521), the System ROM (U2519), communication bus latches
and transceivers, the Address Decoding circuitry, and the Power-On Reset IC (U2502).

## Power-On Reset

The +5 V supply is monitored by U 2502 to generate the reset signals throughout the instrument. These reset signals initialize the states of the logic devices and ensure that memory writes to any of the RAM spaces cannot occur until the +5 V supply is up to its correct operating level. The RESET signal output at pin 6 is initially high during power up (as soon as the voltage has reached the operating level of the RESET IC, U2502). That high signal is inverted by U2506C to produce the SYS RESET signal. The SYS RESET signal holds Processor U2501 in its reset state.

The SYS RESET signal also resets and initializes the Readout Processor (U2400, Diagram 9). At pin 5 of U2502, a RESET signal is generated. That signal biases Q2507 off to prevent System RAM U2521 from being selected by any random states that might occur on the address lines during reset as the voltage is rising.

About 5 ms after the +5 V supply reaches the operating level required for the Processor, the RESET condition is removed, allowing the Processor to operate. At power off (and for a momentary drop in the +5 V supply), when the +5 V supply falls below the safe operating level of the logic devices, the RESET condition occurs to prevent random operation.

## Measurement Processor

FUNCTION. Measurement Processor U2501 is a multitasking device. Its major functions are the following:

1. Continually refreshes the front panel indicator LEDs. One column of the six-column LED matrix is refreshed every 2.048 ms .
2. Continually scans the front panel switch settings, sensing rotation of rotary switches and closures or openings of momentary-contact switches. One column of the six-column switch matrix (the same column number of LEDs being refreshed) is read every 2.048 ms .
3. Communicates with the Readout Processor and Readout RAM to set attributes for each readout field, put text into each field, and turn the readout fields on or off.
4. Scans the front panel pots and sets control voltage levels. The Measurement Processor
selects a pot to be scanned by connecting it to Comparator U2306 in the d-to-a circuitry. The Measurement Processor does a successive-approximation a-to-d conversion on each pot, using the DAC (U2302) to output the search values to the Comparator. Pot values are scanned, processed, and converted to analog control values by the DAC. The analog levels from the DAC are output to the controlled devices via sample-and-hold circuits (U2304, U2305 on Diagram 11).
5. Sets up the hardware state of the instrument, including shift registers 0 and 1, BEAM FIND, and the operating states of U600 (SLIC) and $U 602$ (FLIC). This setup takes place as needed for every change of a front panel momentary-contact or rotary-contact switch.
6. Keeps track of trigger status and controls the trigger levels when in AUTO LEVEL mode. It uses FLIC (U602) to find the A Trigger status (writing to FLIC to reset the A Trigger latch, and reading from it to get the status). It uses SLIC (U600) to find the B Trigger status (writing to SLIC to reset the B Gate latch, and reading from it to get the status). To reacquire the trigger level (positive and negative peaks of the trigger source waveform) it uses the Trigger ICs (U421 and U431), and the Trigger Comparators in FLIC (it writes to FLIC to reset the Trigger comparator latches, and reads from FLIC to get the status of the latches). To switch between free-running and triggered mode in AUTO LEVEL and AUTO trigger modes, it writes to the control register in FLIC; it switches to triggered mode when trigger frequency is sufficiently high and to free-run mode when too low.
7. Tracks the trigger level and ground with cursors. The cursors are displayed by directing the Readout system to display cursor characters, and using the DAC system to set the REF CURSOR and DELTA CURSOR level to match the trigger or ground point on the waveform.
8. Does automated measurements. Some measurements are knob-driven. They are:
$\leqslant$ SEC $\rightarrow$
$k 1 /$ SEC $\rightarrow 1$
$\leqslant$ PHASE $\rightarrow$
$\stackrel{1}{ }$ VOLTS $\rightarrow$

+ VOLTS $\rightarrow$

When these measurements are running, a new digital value is displayed, and the cursor or delay-zone position is changed only when the user changes the setting of one of the continuousrotation CURSOR/TIME POSITION controls. Other measurements are waveform-driven. They are:

```
DC
+PEAK
-PEAK
PK-PK
GATED +PEAK
GATED -PEAK
GATED PK-PK
```

When these measurements are running, a new digital value is displayed and the cursor position is changed each time a measurement cycle occurs. These measurements use the B trigger system; and, for DC, the low-pass circuit formed by U1101B and associated filter components (Diagram 3).
9. Calibrates the measurement system. The vertical and horizontal gains of the instrument are set by manual potentiometer adjustments; therefore, the Processor does not control the match between the waveform display and the graticule. However, it does adjust the measurement results to compensate for any error in the vertical or horizontal gain. (An example of this is that there could be more than $0.5 \%$ error in matching a time base signal to the graticule, but less than $0.5 \%$ error in a time measurement done on that signal).

In the Time Base calibration routine, the Measurement Processor uses the TB Cal signal, the Trigger circuitry, the A Sweep system, and U602 (FLIC) to find the match between the delay levels (REF DELAY and DELTA DELAY) and edges of the calibration signal. In the Vertical System calibration, the Processor uses the Voltage Reference Generator (U931, Diagram 7), the Readout System, the Vertical Preamplifiers, the Delay Line Driver, and the Vertical Comparator (U702) to find the match between Readout REF CURSOR and DELTA CURSOR levels and vertical outputs generated by the preamplifiers. It uses the Voltage Reference Generator, the Vertical Preamplifiers, and the Trigger circuitry to find the match between trigger levels and trigger signals picked off from the Vertical Preamplifiers.

MEASUREMENT PROCESSOR SIGNALS. Table $3-26$ is a listing of signal name and function of the Measurement Processor signals.

## Data Buffers

BUS 0 BIDIRECTIONAL BUFFER. Buffer U2515 communicates the serial bit data to and from the Measurement Processor. Seven data lines of the eight available are used in this application. The remaining one is connected to the +5 V supply to prevent random states and noise from affecting the other data lines in the device. The buffer is enabled via U2503B when both pins 38 (MCSO) and 39 ( $\overline{\mathrm{DEN}}$ ) of the Processor are low. The direction of transfer is controlled by the DT/ $\bar{R}$ output of the processor.

BUS 1 BIDIRECTIONAL DATA BUFFER. Data communication to and from the Measurement Processor is via Buffer U2514. Direction of the data transfer is controlled by the DT//̄R (Data Transmit/Received) output from the Measurement Processor. Data
enabling occurs when pin 39 (DEN) goes low while pin 38 (MCSO) is high.

BUS ARBITRATION GATES. The Bus Arbitration logic (U2503A and B, and U2506D) controls which Bus Buffer is enabled for communication with the Measurement Processor. This control logic is necessary since both buffers cannot be active at the same time. Bus 1 (U2514) is the eight-bit data communication bus, and Bus 0 (U2515) uses seven bits to communicate single-bit data to the Measurement Processor. On the Bus 0 ADO signal line, the Measurement Processor sends the serial MB DATA to each of the operating mode Shift Registers and to SLIC (U600) and FLIC (U602). Additional arbitration is provided by U2503C to produce a SLIC RD strobe when the Measurement Processor wants to read the status of the Display Controller.

Table 3-26
Measurement Processor Signals

| Signal Name | Signal Function |
| :--- | :--- |
| SYS RESET | Master reset for the Processor board. |
| CLK 8M | 8 MHz clock for the Readout Processor. |
| AD0-AD7 | Multiplexed address/data lines for the Measurement Processor. |
| A8-A15 | Address lines for the Measurement Processor. |
| A16-A17 | Multiplexed address/status lines. |
| DO-D7 | Data lines for Bus 1 (to memory and readout). |
| ADDR3-ADDRO | Latched addresses to Main board. |
| $\overline{R O}$ INTR | Indicates the Readout System is busy when asserted. |
| MB RETURN | Return data from the Main board Shift Register 2. |
| SW BD DATA | Data from the switch board. |
| AD COMP | Output of the A-to-D Converter Comparator, U2306. |
| MB DATA | Bidirectional data line to/from the Main board. |
| TB CAL | Time-base calibration signal to trigger circuit. |

## Address Latches

MULTIPLEXED AD BUS ADDRESS LATCH. Since the ADO through AD7 bits are multiplexed between address and data information, the addressing information needs to be latched to hold it for stable addressing (demultiplexed). The ALE (Address Latch Enable) signal from the Measurement Processor (pin 61) goes high when the address bits are stable, and the bits are latched into U2513. The device is permanently enabled by the grounded enable pin.

NONMULTIPLEXED ADDRESS BUS ADDRESS LATCH (U2512). Some of the nonmultiplexed address bits are also latched to maintain them between ALE strobes. The latching also prevents address line problems on the Main board from locking up the Measurement Processor. From U2512, latched addresses ADDRO-ADDR3 (A12-A15) are routed to the Display Controller (U600) for addressing the internal registers in that device. Those address lines are also applied to U501 (Diagram 4) for additional decoding to load the Analog Control Shift Registers with the serial data supplied from the MB DATA signal line. Two address lines (A16-A17) are latched in U2512 for use by the System ROM U2519.

## Measurement Processor ROM

The operating code for the Measurement Processor is stored in the System ROM (U2519). Immediately after the Power On Reset ends, the Measurement Processor fetches the first command from the reset vector and begins running the program.

## Measurement Processor RAM

The Measurement Processor RAM (U2521) provides storage space for intermediate-step calculation results, the front-panel settings, and the system calibration constants. The Processor RAM is battery backed up so that data stored during operation remains intact during periods of power off. When the instrument is turned on again, the stored front panel settings return the oscilloscope to the same operating state that was present at power off. The stored calibration constants preserve the accuracy of the measurement system (assuming the instrument is warmed up and was warmed up when the SELF CAL routine was last done). If the backup battery is dead, or if the stored calibration constants are lost for some other reason, the instrument will do a SELF CAL at power on. This restores accuracy to the instrument (unless the problem is a RAM fault, in which case the instrument cannot SELF CAL), but
the battery circuitry should be checked and the battery replaced if necessary. Also, the SELF CAL routine should be run again after the instrument is warmed up to generate accurate calibration constants at the operating temperature. If the power-off front panel settings are lost for any reason, the power-on conditions that are set up are only restored in valid states (but not any predefined setup).

## Address Decoder

The Address Decoders (U2517 and U2518) allow the Measurement Processor to enable any device on the busses for communication. Enabling signals $\overline{B U S O ~ S E L ~ a n d ~ D A C ~ S E L ~ f r o m ~ t h e ~ p r o c e s s o r ~ s e l e c t ~}$ the Address Decoder (either U2517 or U2518) that is actively decoding when the $\overline{W R}$ signal is low.

## Backup Battery

To keep the data stored in the Measurement Processor RAM (U2521) during power off, a back-up battery system, BT2501 (and BT2502 in 2246 Opt. 1Y), CR2502, and R2506, is used. The battery supplies the energy to maintain the memory states of the static RAM. The battery is not rechargeable and has an operating life of one or five years, depending on battery type (see "FRONT PANEL SETUP MEMORY" in Table 1-1 for more information). When the instrument is on, CR2502 becomes reverse biased to prevent any reverse current; when off, CR2501 is reverse biased to isolate the back-up battery from the +5 V supply. If the battery requires replacement, observe the proper safety precautions in the handling and disposition of the replaced battery (see the WARNING under "Battery" in the Specification).

## READOUT SYSTEM (Diagram 9)

## Readout Processor

The Readout Processor (U2400) is an eight-bit microcomputer, containing its own internal ROM and RAM. The Readout Processor controls the display of text and cursors on the crt. It refreshes each character in the display every 16 ms . When the refresh rate becomes too high, refresh stops until the rate is low enough again. When the refresh rate becomes too low, refresh is done by taking control of the crt beam for a character at a time (Fast mode), until the refresh catches up. When the refresh rate is just right, refresh is done a dot at a time (Slow mode).

Each refreshed dot or character is refreshed with the appropriate display position attributes. The attributes define the characters or dots as:

Stationary text that stays put at a fixed point on screen (examples are scale factor and menu displays);

Cursor-level offset text whose position is determined by the REF CURSOR or DELTA CURSOR control levels only (examples are the time-measurement cursors) ; or

Cursor-level and position-level offset text whose display position is determined by both the cursor levels and the vertical position controls (an example is the TRACK TRIG LEVEL cursor).

The Readout Processor also communicates with the Measurement Processor system to obtain its RAM programming (for determining the display types) and report its status.

## Measurement/Readout Processor Communication Protocol

A data byte is transmitted between the Measurement Processor and Readout Processor as follows:

1. The Measurement Processor waits until RO INTR is unasserted (the Readout Processor is ready to receive).
2. The Measurement Processor writes a byte to tri-state Write Latch U2401 by strobing $\overline{R O} B \cup F$ WR ; this asserts $\overline{R O \text { INTR }}$ (from Interrupt Latch U2417C and D) and causes an interrupt to the Readout Processor.
3. The Readout Processor, when interrupted, reads the Write Latch (U2401); it then unasserts RO INTR by clocking the Interrupt Latch to reset it. (This is the same clock used when the Readout Processor writes to tristate Read Latch U2402.)

Communication from the Readout Processor to the Measurement Processor is done for diagnostics only and can be started only by the Measurement Processor. The Measurement Processor may check the communication link by comparing bytes sent to bytes received, query the Character Code RAM contents, and check the Character ROM identification header. The replies are all sent between the Readout Processor and Measurement Processor a byte at a time as follows.

1. The Readout Processor waits until $\overline{R O \text { INTR }}$ is asserted (the Measurement Processor is ready to receive).
2. The Readout Processor writes a byte to tristate latch U2402; the clock that does the write also unasserts $\overline{\mathrm{RO} \text { INTR }}$.
3. The Measurement Processor waits until $\overline{\mathrm{RO} \text { INTR }}$ is unasserted, then reads tri-state latch U2402. It then strobes $\overline{\mathrm{RO} \text { BUF WR }}$ to assert $\overline{\text { RO INTR (if another byte is coming }}$ from the Measurement Processor).

## Display Refreshing

READOUT FIELD. A Readout field is refreshed in this way:

1. The display field is selected by latching the top address bits for the field into U2411 (FLD2-FLDO).
2. The mixing attributes for the field are latched into U2411 (MIX3-MIX0).
3. The position-tracking attributes for the field are latched into U2403 (CH 4 POS EN through CH 1 POS EN and RO TRACE SEP EN).
4. The starting address for the field (set by communication with the Measurement Processor) is latched into counters U2404 and U2405 ( $\mathrm{CH} 7-\mathrm{CHO}$ ).
5. One character at a time, all the characters in the field are refreshed until the top address for the field (set by communication with the Measurement Processor) has been refreshed.

READOUT CHARACTER. A Readout character is refreshed in this way:

1. $\overline{\mathrm{RO} R U N}$ is asserted. This tells the Dot Refresher PAL (U2410) to begin the character refresh and releases the reset on the Dot Counter (U2407) and the Dot Refresher divider (U2409B).
2. For each dot in the character, the next dot is refreshed.
3. When the final dot is refreshed, $\overline{\mathrm{EOCH}}$ (end-of-character at U2408 pin 17) becomes asserted, and $\overline{\mathrm{Q} \mathrm{EOCH}}$ (the latched version) becomes asserted. The Readout Processor unasserts $\overline{\mathrm{RO} \text { RUN, and increments the char- }}$ acter address counter lines $\mathrm{CH} 7-\mathrm{CHO}$.

READOUT DOT. A Readout dot is refreshed in this way:

1. $\overline{\mathrm{RO} \text { REQ }}$ is unasserted (this causes RO HORIZ and RO VERT to control the crt horizontal and vertical) briefly to show the dot.
2. $\overline{\mathrm{RO}} \mathrm{BLANK}$ is unasserted then asserted (this unblanks then blanks the crt beam).
3. $\overline{\text { DOT CLK }}$ is asserted and unasserted (this increments the dot counter lines DOT4-DOTO).

FAST REFRESH. Fast refresh occurs when the Processor asserts FAST (whenever the refresh rate is too low) or when $\overline{\mathrm{A} G A T E}$ is unasserted (the sweep is in holdoff). In this mode, $\overline{R O R E Q}$ is asserted at the start of a character, and unasserted at the end. Whenever $\overline{\mathrm{RO} R E Q}$ is asserted, the Readout system controls the crt beam intensity and the vertical and horizontal position of the beam. Dots are refreshed every $1.6 \mu$ s during fast refresh.

SLOW REFRESH. Slow refresh occurs when the Processor unasserts FAST (when the refresh rate is not falling behind in refreshing the readout) and $\overline{A G A T E}$ is asserted. In this mode, $\overline{R O R E Q}$ is asserted before each dot in a character, and unasserted after each dot.

Data flow for the dots in a character is roughly this:

1. FLD2-FLD0 give the current field being refreshed.
2. $\mathrm{CH} 7-\mathrm{CHO}$ give the position of the character within that field. $\mathrm{CH} 7-\mathrm{CH} 5$ gives the row within the Readout (row 0 at the bottom, and 7 at the top), and $\mathrm{CH} 4-\mathrm{CHO}$ gives the column (column 0 at the left, column $1 f$ hex at the right).
3. Given the field and character position, the RAM (U2406) outputs the character code (the code for the character that is to be displayed at that position) on R7-RO.
4. DOT4-DOTO gives the dot that is being refreshed within the character.
5. Given the character code and dot number, ROM U2408 outputs the position of the dot within the character. There are up to 31 dots in a character, in an array of 128 possible dot positions (16 vertical by 8 horizontal). DD6-DD3 gives the vertical position of the dot, and DD2-DDO gives the horizontal position.
6. Given the row and column containing the character, and the vertical and horizontal position of the dot, U2412 generates the vertical analog current for the dot, and U2413 the horizontal analog current.
7. U2414 sets up the mixing for the vertical output signal (see Readout Position Mixer).
8. U2415 sets up the mixing for the horizontal output signal.

## Interrupt Request Latch

When the Measurement Processor wants to write new display data to the Readout Processor or Character Codes RAM (U2406), it latches the new data into the Readout Write Latch (U2401) from the DO-D7 bus lines by issuing the RO BUF WR (readout buffer write) strobe to the Interrupt Request Latch (U2417). The output of U2417D (pin 11) is latched low and the Readout Processor is interrupted from its display processes ( $\overline{\mathrm{RO} \text { INTR }}$ goes low). The Readout Processor enables the Readout Write Latch and reads in the new data. When the character is received, the Readout Processor transfers the byte to the Character Code RAM and resets the Interrupt Request Latch (U2417D) to let $\overline{\mathrm{RO} \text { INTR }}$ go high again.

## Communication Latches

Communication from the Measurement Processor and the Readout Processor is done via the Readout Write Latch (U2401). The Readout Read Latch (U2402) is used only for diagnostics communication.

## Character Position Address Counter

The starting address of a readout field to be displayed is loaded into the Character Position Address Counter (U2404 and U2405). The counter then sequences through the addresses of the characters loaded in Character Code RAM U2406. The vertical and horizontal position of the character being displayed is also defined by the output of the counter and is supplied to the Vertical and Horizontal DACS on the $\mathrm{CHO}-\mathrm{CH} 7$ bus lines.

## Character Codes RAM

The ASCll codes needed to display a field of readout are loaded into the Character Codes RAM (U2406) from the Measurement Processor via the Readout Writer Buffer (U2401) on the R0-R7 bus lines. When the field is displayed, the RAM is addressed in sequence by the Character Position Address Counter to output those codes for a display refresh. The field of codes accessed by the FLD0-FLD2
address lines defines either text (menus, measurement readouts, and error messages), vertical cursors, or horizontal cursors. Each field has space for up to 255 characters, and each field is superimposed over the others on the crt. The difference between vertical and horizontal orientation of the cursors types is done by rotating the character field 90 degrees. Hexadecimal addresses for a field are shown in Figure 3-7.


Figure 3-7. Display addresses.

## Character Dot Counter

The Character Dot Counter (U2407A and B) is reset before the start of each character display. When $\overline{R O}$ RUN goes low (the start of a refresh cycle), the reset is released and the clock signal from the Dot Refresher (U2410) clocks the output of the counter through the number of counts needed to address all the dots in a character stored in the Character Dot Position ROM (U2408).

## Character Dot Position ROM

The dot sequence and dot position to display each character is stored in the Character Dot Position ROM (U2408). Character addressing for the display is provided by the Character Codes RAM (U2406) on the RO-R6 bus lines. Addressing of the individual dots within a character is provided from the Character Dot Counter (U2407A and U2407B) on the DOTO-DOT4 signal lines. The pixel information output by the Character Dot Position ROM defines the vertical and horizontal position of the dot to be displayed. At the end of a character display, the $\overline{\mathrm{EOCH}}$ signal is generated from U2408 pin 17 to the Dot Refresher (U2410) to let that device know that the
character is finished and the next character can be started.

## Dot Refresher

Dot Refresher U2410 is a programmable-AND, fixed-OR logic (PAL) device. It monitors $\overline{R O R U N}$ for its low states to determine when a refresh cycle starts. It then assert $\overline{\mathrm{RO} R E Q}$ to take control of the display for refreshing the displayed character dots. $\overline{R O}$ BLANK goes high then low again for each displayed dot. The clock signal then goes low and high again to clock the Character Dot Counter (U2407A and U2407B) to the address of the next dot in the character being refreshed. In Fast mode (when there is low demand for display time or the refresh rate is getting too slow), each character is completely refreshed. In Slow mode, the dots are refreshed at the rate of only one dot per each readout request.

When all the dots in a character have been refreshed, the $\overline{E O C H}$ (end-of-character) signal from Character Dot Position ROM U2408 tells U2410 that there are no more pixels to be refreshed in that character. $\overline{\mathrm{RO}} \mathrm{REQ}$ is then unasserted to release control of the display system and $\bar{Q} E O C H$ (U2410, pin 18) is sent to the Readout Processor to tell it that the Dot Refresher is finished with the character.

The Dot Refresher also asserts the $\overline{\text { POS EN }}$ signal low (pin 19) when readout associated with any of the traces is being displayed. That signal enables the Readout Position Enable Latch (U2403).

## Divider/Counter

The 8 MHz System Clock is divided down to 4 MHz by Divider/Counter U2409A for clocking the Readout Processor and to 2 MHz to clock the Dot Refresher (after inversion by U 2417 B ). The 2 MHz signal also clocks U2409B, a second divider that produces the signals that cycle the Dot Refresher through its internal states.

## Readout Position Enable Latch

When the readouts must follow the Channel Vertical POSITION controls or the TRACE SEP control, the vertical position information must be added to the readout position. This job is done in the Vertical Position Switching circuitry (Diagram 2). The time of enabling and the readout position that is enabled is determined by the Readout Processor. The correct enabling data for the next field of characters to be displayed is latched into U2403 from the RO-R7 (bits $0-4$ only) bus by the $\overline{\text { POS STB }}$ signal (U2403, pin
11). See Table 3-27. When a field is being refreshed, the outputs of U2403 are enabled by the $\overline{\text { POS EN }}$ signal from the Dot Refresher, U2410 pin 19.

Table 3-27
Position Enable Bit Assignment

| b4 | b3 | b2 | b1 | b0 | Value |
| :---: | :---: | :---: | :---: | :---: | :---: |
| x | x | x | x | 0 | Disable CH 1 position current |
| x | x | x | x | 1 | Enable CH 1 position current |
| X | x | x | 0 | x | Disable CH 2 position current |
| X | $x$ | x | 1 | $\times$ | Enable CH 2 position current |
| x | X | 0 | x | $\times$ | Disable CH 3 position current |
| x | x | 1 | x | x | Enable CH 3 position current |
| x | 0 | $\times$ | x | $x$ | Disable CH 4 position current |
| X | 1 | x | x | x | Enable CH 4 position current |
| 0 | $x$ | $x$ | x | x | Disable Trace Sep current |
| 1 | x | X | X | x | Enable Trace Sep current |

## Readout DACs

Vertical Character and Dot position data bytes are converted to analog current for eventual application to the Vertical Delay Line by Vertical Readout DAC U2412. The vertical signal current is applied to both signal mixer multiplexers (U2414 and U2415). When fixed position text is displayed, the output mixer selects a fixed position value to mix with the horizontal output signal to define the readout position on the display. When positionable text is displayed (such as time cursors), the cursor position signal is mixed with the horizontal output signal. That summed signal then defines (vertically) where a character (dot) is displayed on the crt. Vertical Readouts that follow the Channel Vertical POSITION controls (tracking cursors and associated text) have their position information summed with the Vertical Position Switching circuitry (Diagram 2) just before the Delay Line Driver.

Horizontal Character and Dot position data bytes are converted to analog current for application to the Horizontal Preamplifier (U802, Diagram 6) by Horizontal Readout DAC U2413. The horizontal signal current is applied to both signal mixer multiplexers (U2414 and U2415). When fixed position text is displayed, the output mixer selects a fixed position value to mix with the horizontal output signal to define the readout position on the display. When positionable text is displayed (such as time cursors), the cursor position signal is mixed with the horizontal character position signal. That summed signal then defines (horizontally) where a character dot is displayed on the crt. None of the readout (text or cursors) is positionable using the Horizontal POSITION control.

## Field and Mixer Control Latch

Selection signals for switching the Readout Position Mixer multiplexers (U2414 and U2415) are latched into Field and Mixer Control Latch U2411 by the MIX STB output from the Readout Processor (U2400 pin 25). Three field selection bits used in addressing the Character Code RAM are also loaded from the data byte output from U2400 on the R0-R7 data bus. The MIX3-MIX0 bits select the combination of fixed, positionable, and character (dots) signals that are mixed to produce the required readout positions on the crt.

The Field signals (FLDO, FLD1, and FLD2) access the type of characters that are displayed (menus and readout labels, vertical cursors, or horizontal cursors). Each of the three fields contains space for 255 characters. Characters from each field are superimposable over the other field's characters in the display. The attributes implicitly affect the field specified by b0, b1, and b2 (b2 is always handled as if zero, even if not communicated as zero).

## Readout Position Mixers

The Readout Position Mixer (U2414, U2415) selects either fixed or cursor-position voltages to mix with the character signals to position them in the display. Selection is done with the MIX0-MIX3 signal levels set by the Measurement Processor for the particular field of characters being displayed (see Table 3-28).

The 2246 1Y and 2246 Mod A Readout Output Mixer allows three modes of display to present the text and vertical or horizontal cursors.

TEXT OUTPUT MODE. The vertical output displays vertical text information, locked to crt vertical screen position. The horizontal output displays horizontal text information, locked to crt horizontal screen position.

Table 3-28
Field and Mixer Attribute Bit Assignment

| MIX3 | MIX2 | MIX1 | MIX0 | NC | FLD2 | FLD1 | FLD0 | Value |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :--- |
| x | x | x | x | x | b 2 | b 1 | b0 | Field number $(0,1$, or 2) |
| x | x | 0 | 0 | x | x | x | x | Route Horiz DAC to Horiz Ampl |
| x | x | 0 | 1 | x | x | x | x | Route Cursor0 to Horiz Amplifier |
| x | x | 1 | 0 | x | x | x | x | Route Cursor1 to Horiz Amplifier |
| x | x | 1 | 1 | x | x | x | x | Unassigned |
| 0 | 0 | x | x | x | x | x | x | Route Vert DAC to Vert Ampl |
| 0 | 1 | x | x | x | x | x | x | Route Vert DAC + Cursor0 to Vert Amplifier |
| 1 | 0 | x | x | x | x | x | x | Route Vert DAC + Cursor1 to Vert Amplifier |
| 1 | 0 | x | x | x | x | x | x | Route Horiz DAC to Vert Ampl |

HORIZONTAL CURSOR MODE. The vertical output displays vertical text information, whose position is controlled by an analog cursor level control. The horizontal output displays horizontal text information, locked to crt horizontal screen position.

VERTICAL CURSOR MODE. The vertical output provides a ramp signal, locked to crt vertical screen position. The horizontal output matches the voltage of an analog cursor level control.

MIXER OPERATION. The readout system displays text in a pixel-type representation. For example, an underlined letter " $A$ "' may be represented as in Figure 3-8. Blackened spaces in the illustration denote a displayed pixel.

For each character, one pixel at a time is displayed by driving the vertical and horizontal outputs to values representing the vertical and horizontal position of a pixel within the character and then unblanking the $Z$-Axis.

Multiplexers U2414 and U2415 are ganged electronic switches that mix current and voltage settings. Vertical Readout DAC U2412 (vertical text generator) provides an output current from pin 2 that is proportionate to the vertical position of the pixel being displayed; the minimum output is 0 mA . Horizontal Readout DAC U2413 (horizontal text generator) provides an output current that is proportionate to the horizontal position of the pixel being displayed. Its minimum output is also 0 mA . The REF CURSOR and DELTA CURSOR levels are voltages that offset the text output for the type of cursor being displayed (vertical TIME cursors or hori-
zontal VOLTS cursors). When straight text is to be displayed, dc levels for offsetting the vertical and horizontal text display outputs are added. Horizontal and vertical signals to be mixed for a particular readout are selected by the MIXO-3 outputs of latch U2411. The data is latched from the Readout Processor bus when $\overline{\mathrm{MIX} \mathrm{STB}}$ clock is generated by the Readout Processor.

## Output Buffers

The Output Buffers (U2416A and U2416D-vertical, and U2416B and U2416C-horizontal) are voltage follower circuits that mix the signals selected by the Readout Position Multiplexers and buffer them for application to the vertical delay line (RO VERT) and the Horizontal Preamplifier (RO HORIZ).


Figure 3-8. Character pixel arrangement.

The voltage at U2416 pin 14 depends on two things: the current from U2414 pin 13, and the voltage at U2414 pin 3. The possible displays are given in Table 3-29.

The voltage at U2416 pin 8 depends on two things: the current from U2415 pin 13, and the voltage at U2415 pin 3. The possible conditions are shown in Table 3-30.

## SWITCH BOARD AND INTERFACE (Diagram 10)

The front-panel LEDs that backlight the switches and panel labels are schematically arranged in a matrix of eight rows and six columns. The front-panel switches are arranged in a matrix of 16 rows and six columns. Each LED and switch is connected to a distinct row/ column intersection, with a column of LEDs and a column of switches being common and enabled by the same signal.

At intervals of about 2 ms , a column of LEDs is refreshed (turned on or off) and the status (open or
closed) of the connected column of switches is read. All six columns of LEDs and the six columns of switches are completely refreshed and checked every 12 ms . The timing is fast enough to prevent flicker of the LEDs and to catch a push button closure.

## LED Refresh

Assume LED column ASO is being refreshed. First, the LED Cathode Register, U2524, is loaded with a data byte from the Measurement Processor. That byte defines the LEDs that are on for that column, and the outputs of Cathode Driver U2525 for the "on" LEDs are low. Then, a high on the DO bit of the Measurement Processor Data Bus is latched into LED Anode Register U2523 with the $\overline{L E D ~ A N O D E ~ C L K ~ s i g n a l . ~}$ That high turns on the associated Darlington transistor (Q2506 for the ASO column), and the LEDs in that column that also have their cathodes low from U2525 are turned on.

Table 3-29
Display Possibilities

| Readout Type | U2414-3 | U2414-13 |
| :--- | :--- | :---: |
| Stationary Text | 0.6 V | U2412 output |
| Horizontal Ref Cursor | REF CURSOR | U2412 output |
| Horizontal Delta Cursor | DELTA CURSOR | U2412 output |
| Vertical Ref Cursor | 0.6 V | U2413 output |
| Vertical Delta Cursor | 0.6 V | U2413 output |

Table 3-30
Possible Signal Conditions to U2416

| Readout Type | U2415-3 | U2415-13 |
| :--- | :---: | :---: |
| Stationary Text | 2.0 V | U2413 output |
| Horizontal Ref Cursor | 2.0 V | U2413 output |
| Horizontal Delta Cursor | 2.0 V | U2413 output |
| Vertical Ref Cursor | REF CURSOR | 0 mA |
| Vertical Delta Cursor | DELTA CURSOR | 0 mA |

## Switch Reading

At the same time the ASO LED column is refreshed, the connected ASO switch column is pulled high through CR2006. The switch status (low for open or high for closed) for the active switch column is parallel loaded into the Switch Board Shift Registers (U2001 and U2002). This switch status data is then shifted out serially (by 15 SW BD SR SHIFT clocks) to the Measurement Processor on the SW BD DATA return line. The position of a high in the serial data stream, and knowing the active column, tells the Measurement Processor the switch in column ASO that is closed (the CH 1 VOLTS/DIV setting). Columns with push-button switches may or may not have a switch closed. A switch closure is interpreted by the Measurement Processor, and any new operating conditions needed (determined from the firmware routines called up to handle a particular switch closure) are set up.

At the next 2 ms interrupt, the Measurement Processor loads new data into Cathode Register U2524 to enable the LED rows, and the column is advanced to enable the A01 column for refresh and switch reading. The process described is continual while the oscilloscope is on.

Part of the Measurement Processor routine stores the new front panel settings in the System RAM each time a change is made. At power on (after being turned off), the stored front-panel settings are recalled from the System RAM to return the oscilloscope to the same operating state that existed at power off (with some exceptions).

## Diagnostic

When the Measurement Processor is running the register checks during the DIAGNOSTIC, it can check the condition of registers U2001 and U2002. Serial data is placed on the ASO line from the DO bit of the Measurement Processor data bus. That data is serially shifted through the two registers to the SW BD DATA return line. The Measurement Processor compares the returned data stream with what was sent. A difference in the data bits shows an error; a correct comparison passes the test.

## ADC AND DAC SYSTEM (Diagram 11)

The ADC and DAC System permits the Measurement Processor to provide analog control voltages to the circuitry under its control and to find out certain analog voltage levels that it must have to do its control and measurement functions.

## Pot Multiplexer Latch

Latch U2313 latches data from the data bus (D0-D7) to control multiplexers U2308 and U2309 on this diagram and U506 on Diagram 7.

## Front-Panel Control Multiplexers

Multiplexers U2308 and U2309, controlled by the Measurement Processor via Pot Mux Latch U2313, select the front-panel control levels that are compared with the output of the D-to-A Converter (U2302). The result of that comparison is sent via signal line AD COMP to the Measurement Processor (U2501, Diagram 8).

## Input Data Latches

Binary data bytes to be converted to analog voltages are loaded into two latches (U2300 and U2301). Data Latch U2301 latches data to the DAC Multiplexer (U2303). Data Latch U2300 latches data to D-to-A Converter (U2302).

## Digital-to-Analog Converter

The D-to-A Converter (U2302), using 12 data bits, can produce 4096 discrete output signal current levels from 0 to 2 mA . Signal current flows through R2303 to the +2.5 V reference voltage. The resulting voltage drop across the resistor moves the voltage at pin 3 of voltage follower U2314 away from +2.5 V toward 0 V and below. When there is 0 mA output, the voltage at pin 3 is +2.5 V . At maximum output current, the voltage at pin 3 is -2.5 V . Voltage follower U2314 buffers the voltage and applies it to the input pin of analog multiplexer U2303, where it is directed to the control circuit selected by the Measurement Processor.

A-TO-D CONVERSION. The output from U2314 is also applied to comparator U2306. When analog-todigital conversion is being done, the Measurement Processor drives the DAC to produce comparison voltage levels in a binary search pattern. The output of U2306 is monitored to determine the smallest DAC input change that will produce an output change from the comparator. That value is then used as the digital representation of the analog voltage applied to the other pin of the comparator from the output of Multiplexer U2308 or U2309. Signals on that multiplexed line are the front panel potentiometers' wiper voltages and the probe-code levels.

## Sample-and-Hold Circuits

The analog voltages from the multiplexer U2303 remain stable only for the short period of time that the DAC is at a fixed output level. Control voltages to the analog circuitry must remain constant except
for changes to the control settings. Those control voltages are held constant between refreshes by sample-and-hold circuits formed by a capacitor (to hold the voltage) and a voltage follower (to buffer the voltage held by capacitor). The voltage follower circuits are provided by the operational amplifiers of U2304 and U2305. Each of the hold voltages are protected from humidity degradation of the voltage follower's high-impedance input by an active guard shield on the circuit board. Extra noise filtering for two of the control voltages (REF DELAY and DELTA DELAY) is provided by using an RC pi-type filter input circuit to the voltage follower.

## POWER SUPPLY (Diagram 12)

The Power Supply (Diagram 12) provides the various low-voltages needed to operate the 2246 1Y and 2246 Mod A and the high-voltage required by the cathode-ray tube (crt). The supply circuitry is arranged in the following functional blocks: AC Input, Primary Power Rectifier, Start-Up circuit, Preregulator Control circuit, Preregulator Power Switching circuit, Inverter Control circuit, Inverter Power Switching circuit, Low-Voltage Secondary Supplies, and High-Voltage Supply (see Figure 3-9).

Ac power via the power cord is rectified and filtered by the Primary Power Rectifier to supply the dc voltage to Preregulator circuitry. The output voltage level from the Primary Power Rectifier depends on the ac supply voltage level and may vary between about 125 V and 350 V . This unregulated, filtered, dc voltage is supplied to the Preregulator Start-Up circuit and the Preregulator Switching circuit. The Preregulator Power Switching circuit supplies +44 Vdc output power to drive the Inverter Power Switching circuit.

The $+44 \vee$ Preregulator output voltage is switched by the Inverter Power Switching circuit to produce an alternating current through the primary of the Inverter power transformer. The current source to the Inverter switching transistors is monitored and regulated by the Inverter Control circuit to maintain a constant output voltage level across the transformer secondaries.

The Low-Voltage Secondary Supplies rectify and filter the low-voltage secondary ac voltages to provide the dc power requirements for the instrument. Two other secondary windings on the Inverter Power Transformer are used in the High-Voltage Supply, a high-voltage winding and a crt filament winding. Voltage from the high-voltage winding is further multiplied and converted to dc voltage for the crt anode, cathode, and intensity-grid voltages.

Both overvoltage and overcurrent protection are provided to protect the oscilloscope circuitry from further damage if a circuit component fails.

## Ac Input

Applied source voltage is input to the Primary Power Rectifier via surge protection circuitry and noise filtering circuitry. A sealed line filter (FL2201), L2207, L2208, C2214, C2213, C2216, C2215, R2260, R2227, and R2228 form a low-pass filter designed to prevent transmission of high-frequency noise signals either into or out of the instrument. Bleeder resistor R2215 across the input line filter drains off any charge retained by the capacitors in the input circuitry when the power is disconnected. Thermistor RT2201 prevents a sudden rush of input current into the rectifier and filter capacitor, C2202, when the power switch is turned on. The thermistor presents a relatively high resistance when cold, then quickly reduces to a low value when warmed up. Varistor VR2204 acts as a surge limiter to reduce the effects of any power line surges that may damage the input circuit components. The varistor is a voltage-sensitive device that quickly reduces its resistance value when its voltage limits are exceeded. Line fuse F2201 protects the instrument from additional damage in case of of a severe short in the power supply.

## Primary Power Rectifier

Rectification of the input ac source voltage is done by rectifiers CR2231-CR2234. Simple capacitive filtering of the rectifier output is done by C2202. The filtered output voltage may range between about 125 Vdc and 350 Vdc , depending on the amplitude of the ac input voltage. A line trigger signal is picked off by T2206 for use when the Trigger SOURCE is set to LINE. Bleeder resistor R2256 drains off the charge on C2202 when the instrument is turned off.

## Start-Up Circuit

The Start-Up circuit provides the operating supply voltage to the Preregulator. At power on, C2204 in the Start-Up circuit begins charging through R2203 and R2204 from the output of the Primary Power Rectifier. When the voltage across C2204 reached 20 V , the voltage at the base of Q2204 is about 6.8 V . This base voltage level causes Q 2204 to conduct (there is a 6.2 V zener diode in the emitter path), and Q2211 also is then biased on. Positive feedback to the base of Q2204 (from the collector of Q2211 through R2220) then keeps both transistors on. The dc voltage to U2201 (Vcc) for start up (and continued running after start up) is provided by the charge on C2204 via Q2211.


Figure 3-9. Power Supply block diagram.

With U2201 on and drawing current from C2204, the voltage across C2204 begins to fall. If the Preregulator output rises to +44 V before the voltage across C2204 falls to 10 V , then CR2202 becomes forward biased, and current pulses are supplied by a winding (pins 8 and 9) on T2203 to keep C2204 charged (and U2201 operating).

If the Preregulator output does not rise to +44 V within the time it takes to discharge C2204 below 10 V (about $1 / 10$ of a second), the voltage at the base of Q2204 will drop too low for the feedback voltage to keep it on. That will cause Q2211 to also shut off. The start-up cycle repeats when the voltage across C2204 again reaches 20 V (recharging from the output of the Primary Power Rectifier output via R2203 and R2204). Continued failure of the Preregulator to start up and the repeated attempts to do so is called the "Chirp" mode. Zener diode VR2206 prevents the voltage across C2204 from exceeding about 30 V if no start-up attempt occurs.

## Preregulator Control Circuit

The Preregulator Control IC, U2201, is a pulse-width modulator used to control the on time of Preregulator Switching FET Q2201. It contains an oscillator, comparators, voltage and current error amplifiers, and logic circuitry that controls its operation. The modulated output pulses drive switching transistor Q2201 through a buffer amplifier composed of Q2202 and Q2203. Pulse width (the time that FET Q2201 is on) is inversely proportional to the control voltage at pin 3 of U2201 (i.e., a lower voltage at pin 3 makes the pulse width wider to keep Q2201 on longer.

Pin 7 of U2201 is the IC ground reference, and it is tied directly to the +44 V output voltage. Therefore, the Preregulator IC and the Start-Up circuitry operating potentials "float" on the regulated output voltage (developed across C2203).

Pin 2 of U2201 is the current-summing node to the voltage-error amplifier. The error amplifier will try to keep the voltage on pin 2 equal to the voltage on pin 1 (the +44 V supply voltage). The error amplifier maintains pin 2 at $+44 \vee$ by raising (or lowering as necessary) the voltage at pin 3. This raises (or lowers) the voltage across C2203 so that less (or more) current will be drawn out of the current summing node.

The major current injected into the summing node is from the regulated 5 V output, from pin 12 of U2201, via R2212. That current is about 0.6 mA . The current through R2206 adds to the current shunted by the Preregulator Output Voltage Control transistor, Q2208, to produce about 0.6 mA to keep the current
into and out of the summing node balanced. The actual current through R2206 is the output voltage ( +44 V across C2203) divided by the resistance value of R2206 ( $100 \mathrm{k} \Omega$ ) or about 0.4 mA .

SOFT START. At the initial turn-on of the instrument, C2203 is discharged. If no action were taken to prevent it, the initial charging current to that capacitor would exceed safe limits. To avoid such a problem, a "soft start" of the charging path is done.

At turn-on, the +5 V output of U2201 steps to +5 V immediately. $\mathrm{A}+5 \mathrm{~V}$ pulse is coupled to pin 4 of U2201 via C2207. This pin is the "dead time control" input, and when it is high, the dead time between switching pulses to Q2201 is increased to $100 \%$. Switching transistor Q2201 does not turn on, and no charging of C2203 occurs. Then, as C2207 charges, the voltage on pin 4 begins to decrease toward the ground reference value (on pin 7). This decreases the dead time, allowing increasingly wider conduction pulses to occur.

The on-time gradually increases until the charging current is limited by the internal current limit amplifier of U2201. At that point, the Preregulator is acting as a current source. When the voltage across C2203 reaches +44 V , the voltage error amplifier starts to limit the output, and the Preregulator has reached its operating level and acts as a voltage source.

CURRENT LIMIT. The output current of the Preregulator switching FET, Q2201, is limited to a safe value. If the current exceeds 2.4 amperes, the voltage dropped across R2201 causes pin 14 of U2201 (one input of the current limit amplifier) to exceed the voltage on pin 13 of U2201 (the other input pin of the current limit amplifier). The output of the current limit amplifier then goes high, raising the voltage on pin 3 of U2201. Increased voltage on pin 3 narrows the width of the turn-on pulses to switching FET Q2201 and limits the output current.

Usually, with a circuit failure, the excess loading remains, and the pulses remain narrow. The Preregulator Control IC then shuts down because the charge on C2204 is not maintained via the Preregulator supply winding on T2203, and the Preregulator goes into the chirp mode (continual shut down and restart attempts).

OVERVOLTAGE CROWBAR. If the output voltage across C2203 exceeds about +51 V , VR2201 in the crowbar circuit conducts. The gate of SCR Q2206 then rises; and, if the rise is enough, the SCR latches on. When on, Q2206 shorts out C2203, and the current limit circuit causes the switching pulses to Q2201 to become very narrow. Preregulator IC U2201 then shuts down (as described in the Current Limit discussion). The Preregulator will attempt a
restart after about half a second, but will shut down again if the overvoltage condition continues (this is the "chirp" mode).

PREREGULATOR OUTPUT CONTROL. The voltage across the Inverter current source transistor, FET Q2214, is monitored by Q2208 (from the collector voltage of either Q2209 or Q2210). That voltage has to be maintained at the proper level to provide enough regulation room for the secondary supply voltages and still not dissipate more power than necessary in Q2214. If the voltage across Q2214 is too high, Q2209 is biased on harder and draws more current from the input summing node (pin 2 of U2201) of the voltage error amplifier in U2201, the Preregulator Control IC. The output of the error amplifier at pin 3 of U2201 then rises, and the width of the switching pulse to the Preregulator Switching circuit narrows to decrease the +44 V output.

The Inverter Control circuit (Q2212 and Q2213) senses the decreased voltage across the primary of the Inverter power transformer (T2204) and responds by driving Q2214, the Inverter currentsource transistor, harder; thereby decreasing the voltage across it.

Control response time in the feedback loop just described is long; but it does not need a fast response time, since the circuit only determines the power dissipation in Q2214. Compensation of the circuit to prevent oscillation is done by a low-pass filter ( 10 Hz cutoff) formed by C2238, R2205, and R2246.

## Preregulator Switching Circuit

The Preregulator Switching circuit provides the energy required to keep C2203 charged up to +44 V . Switching FET Q2201 is driven by the pulsewidth moclulated output of the Preregulator IC (U2201) via a buffer amplifier circuit. The Preregulator IC controls the on-time to maintain the voltage across C2203 at +44 V .

For the following discussion of the switching circuit, assume that Q2201 is off, C2201 is charged to the rectified line voltage ( 160 V from the Primary Power Rectifier), and the +44 V supply is up and driving a circuit load.

When the Preregulator IC turns on Q2201, the drain of Q2201 is immediately clamped to 44 V . This forces $116 \mathrm{~V}(160 \mathrm{~V}-44 \mathrm{~V}$ ) across pins 6 and 7 of T2203. Current begins increasing linearly in that coil as Q2201 supplies current to the +44 V supply. With the one end of C2201 clamped to +44 V , and C2201 being charged to +160 V , the other end of C 2201 is pushed down with the anode of CR2201 going to $-116 \vee(44 \vee-160 \mathrm{~V})$. This places $116 \vee(0-$

116 V ) across pins 1 and 2 of T2203 and current begins increasing linearly in that coil, also flowing through Q2201 to the +44 V supply. After a time determined by Preregulator IC U2201, the drive signal to Q2201 is switched low, and the switching FET is turned off.

The current flowing in both coils of T2203 must continue as the magnetic field collapses, but it cannot flow through Q2201. The only available path is through CR2201 (previously biased off). The polarity reversal of the voltage across T2203 that occurs forward biases CR2201, and permits the energy in the magnetic field to be released to the +44 V supply.

When CR2201 is forward biased its cathode is clamped to the +44 V supply level. With C2201 still charged to +160 V (the supply voltage), its positive end is pushed up to $204 \mathrm{~V}(44 \mathrm{~V}+160 \mathrm{~V})$. Now there is $-44 \mathrm{~V}(160 \vee-204 \mathrm{~V})$ across the coil of T2203 from pin 7 to pin 6 and $-44 \mathrm{~V}(0-44 \mathrm{~V})$ from pin 2 to pin 1 (see Figure 3-10). Since C2201 is in parallel with C2202 for dc voltages (coils are shorts to dc), the dc voltage across C2201 can change very little. The capacitance of C2201 is large enough that the charging and discharging currents do not have enough time to change the voltage across C2201 in normal operation.


Figure 3-10. Preregulator switching waveforms.

The two coils of T2203 need not be coupled magnetically for the circuit to operate. Both coils are wound on the same core for convenience. Transformer action is minimal because the waveforms impressed across both coils are nearly identical.

After a time controlled by the Preregulator IC (the dead time), the on-time cycle for Q2201 repeats. On time depends on the line voltage level; a higher
line voltage level means a shorter on time of Q2201 is needed to maintain +44 V across C2203.

## Inverter Power Switching Circuit

The Inverter Power Switching circuit is composed of switching transistors Q2209 and Q2210, current source transistor Q2214, inverter power transformer T2204, base-drive transformer T2205, and associated components. Current supplied by the +44 volts output from the Preregulator circuit is alternately switched through each side of the centertapped primary of T2204 to drive the loads on the secondary windings of the inverter transformer.

INVERTER STARTER. As the Preregulator turns on, the +44 V supply increases from 0 V . The increasing voltage forward biases CR2236 and charges C2248 through the base-emitter junctions of Q2209 and Q2210. Current is drawn through each side of T2204, from the center tap, as the transistors conduct. One of the two transistors will have a slightly higher gain than the other, and its collector voltage will decrease more than the other. The voltage difference across the primary of T2204 also appears across the primary winding of T2205, and a feedback voltage is induced in the secondary winding of T2204. The polarity of the transformer is such that the conduction of the higher gain transistor is reinforced (positive feedback), and that transistor quickly saturates while the other is cut off. One end of the primary of T2204 is driven toward ground while the other end is opened. After about half a second, C2248 charges up, CR2236 becomes reverse biased, and that path for current through the conducting transistor is blocked.

If the Inverter Power Switching circuit stops, the Inverter Starter circuit will not restart it until C2248 is discharged. Furthermore, C2248 will not discharge until the +44 V supply falls.

INVERTER POWER SWITCHING. Switching is started by one or the other of either Q2209 or Q2210 conducting more that the other, and circuit action biases the other one off. Assume for this discussion that Q2210 is biased on and Q2209 is off. Current flows through current-source FET Q2214, ontransistor Q2210, and half of the primary of T2204 (pins 9 and 11). The voltage drop across currentsource transistor Q2214 holds the emitter voltage of Q2209 and Q2210 at 3 V . Voltage across pins 9 and 11 is therefore $41 \vee(44 V-3 V)$.

Through autotransformer action, 41 V is induced in the other half of the primary winding of T2204 from pin 8 to the center-tap pin. That voltage adds to the 41 V from pins 9 to 11 to produce a potential of 82 volts across the primary of switching transformer

T2205. Current rapidly ramps up through the primary of T2205 and induces a positive feedback base current in one-half of its center-tapped secondary that keeps Q2210 turned on. Current in the other half of the secondary biases on CR2227 to prevent a high reverse base-to-emitter voltage from being developed across Q2209.

After about $25 \mu \mathrm{~s}$, the current through the primary of T2205 saturates the magnetic core and the primary impedance of the transformer drops to a low value. When saturation occurs, the impedance presented by L2206 by comparison to that of T2205 is large, and most of the voltage applied from the secondary of T2204 is then dropped across L2206. The secondary voltage of T2205 drops to zero, and with no base-drive current to Q2210, that transistor switches off.

With both Q2209 and Q2210 off, the magnetic energy stored in the primary of T2205 and in L2206 causes current to flow in the primary of T2204, reversing the voltage polarity on this winding. The voltage reversal is not instantaneous because of the parasitic capacitance of the T2204 windings. When the reverse voltage gets high enough, base current flows to Q2209 and that transistor turns on. The cycle then repeats with current flow through T2204 reversed. This process induces ac in the various secondary windings of the inverter power transformer.

INVERTER CONTROL LOOP. Whenever either Q2209 or Q2210 is on, the collector voltage of the on transistor forward biases either CR2205 (if Q2209 is on) or CR2204 (if Q2210 is on). Capacitor C2219 is then charged to nearly the same voltage that is applied to the center tap of the primary winding of Inverter Transformer T2204.

A resistive voltage divider formed by R2239, R2238, and potentiometer R2252 (+7.5 V ADJUST) applies a fraction of the voltage across C2219 to the base of Q2213 (one-half of a differential amplifier formed by Q2212 and Q2213). The voltage on the base of Q2213 is compared to a voltage on the base of Q2212 that is referenced back to the +44 V center tap voltage of T2204. If the collector voltage of the conducting inverter switching transistor (Q2009 or Q2210) is not the correct level (about 3 V ), the gate voltage of current-source FET Q2214 will be raised or lowered as needed to correct the error.

## Low-Voltage Secondary Supplies

The low-voltage power supply circuitry on the pin 12 to pin 22 and pin 13 to pin 15 secondary windings of
the Inverter power transformer consist of rectifier and filter components only. All the regulation is done by the Preregulator and Inverter Control circuitry in the primary side of the transformer. Both half-wave and full-wave rectifiers are used, and either simple capacitor or capacitive-input Pl filter circuits are used. Rectifier and filter type used for each of the secondary voltages depends on the load requirement. A single 130 Vac output from pin 12 of T2204 supplies the drive to the Z -Axis dc restorer circuitry. Power for the blower fan is supplied by the -15 V power supply line.

The center-tapped secondary winding from pins 13 to 15 of T2204 is used for the +5 V and -5 V supplies. Both are full-wave rectified and filtered using capaci-tive-input Pl filters.

## High-Voltage Supply

The high-voltage power supply uses two secondary windings of T2204: one for high-voltage multiplier U2230 and the other for the crt filament. Flying leads from the top of the transformer make the circuit connections into the high-voltage circuitry. The crt filament winding consists of a few turns of insulated wire.

The high-voltage winding attaches directly to the HV Multiplier. Outputs from HV Multiplier U2230 are the 13.3 kV to the crt anode via a high-voltage-insulated connecting lead and the -2.7 kV . supplied to the crt cathode, focus grid, and intensity grid. The -2.7 kV supply is filtered by a two-section capacitive input RC
filter. A neon lamp across the second section of the filter provides protection against arcing if there is a failure that can cause a large difference of potential to develop between the crt heater and cathode circuits.

## MAIN BOARD POWER DISTRIBUTION (Diagram 13)

The Main Board Power Distribution diagram schematically displays the distribution paths and decoupling circuits for the low voltages from the Power Supply. The supply and ground connections to the various integrated circuits in the instrument are also shown. Use this diagram to aid circuit tracing when trying to locate a power supply loading problem associated with the Main Board.

## PROCESSOR BOARD POWER DISTRIBUTION (Diagram 14)

The continuing power distribution from the Main Board to the top board (Processor Board, A16) is depicted in the Processor Board Power Distribution schematic diagram. Use Diagram 14 to aid in locating power supply loading problems that are isolated to the Processor Board.

## INTERCONNECTION DIAGRAM (Diagram 15)

Circuit board interconnections with the plug, jack, pin numbers, and signal names shown are found in schematic Diagram 15. The diagram is useful in checking continuity of cable runs and signal paths from board to board through the instrument.

# PERFORMANCE CHECK PROCEDURE 

## INTRODUCTION

## PURPOSE

This Performance Check Procedure verifies the Performance Requirements of the 2246 1Y or 2246 Mod A as listed in the Specification (Section 1) and helps determine the need for readjustment. These checks may also be used as an acceptance test or as a preliminary troubleshooting aid.

You do not have to remove the wrap-around cabinet from the 22461 Y or 2246 Mod A to do this procedure. All checks can be made with controls and connectors accessible from the outside.

## TEST EQUIPMENT REQUIRED

Table 4-1 lists all the test equipment required to do the Performance Check Procedure. Test equipment specifications described are the minimum necessary to provide accurate results. For test equipment operating information, refer to the appropriate test equipment instruction manual.

When you use equipment other than that recommended, you may have to make some changes to the test setups. If the exact example equipment in Table 4-1 is not available, use the Minimum Specification column to determine if any other available test equipment might be adequate to do the check.

## PERFORMANCE CHECK INTERVAL

To ensure instrument accuracy, check the performance of the 22461 Y or 2246 Mod A after every 2000 hours of operation, or once each year if used infrequently. If the checks indicate a need for readjustment or repair, refer the instrument to a qualified senvice person.

NOTE
The silver-oxide batteries used in the 22461 Y (instruments serial numbered B714999 and below) MUST BE REPLACED AT LEAST EVERY 12 MONTHS to ensure proper operation of the instrument.

## PREPARATION

This procedure is divided into subsections to let you check individual sections of the instrument when it is not necessary to do the complete Performance Check. An Equipment Required block at the beginning of each subsection lists the equipment from Table 4-1 that is needed to do the checks in that subsection.

The initial front-panel control settings at the beginning of each subsection prepare the instrument for the first step of the subsection. Do each of the steps in a subsection completely and in order to ensure the correct control settings for steps that follow. To ensure performance accuracies stated in the Specification (Section 1), let the instrument warm up for 20 minutes and run the SELF CAL MEASUREMENTS routine.

To run the SELF CAL MEASUREMENTS routine:
Press the top and bottom menu-item select buttons to display the SERVICE MENU. Underline and select SELF CAL MEASUREMENTS. Press RUN to start the routine, then QUIT to return to the normal oscilloscope mode.

> NOTE

Performance accuracies are ensured only when the SELF CAL MEASUREMENTS are done AFTER the 20-minute warmup.

Table 4-1
Test Equipment Required

| Item and Description | Minimum Specification | Use | Example of Test Equipment |
| :---: | :---: | :---: | :---: |
| Leveled Sine-Wave Generator | Frequency: 250 kHz to above 150 MHz . Output amplitude: variable from 10 mV to $5 \mathrm{~V} \mathrm{p}-\mathrm{p}$. Output impedance: $50 \Omega$. Amplitude accuracy: constant within $1.5 \%$ of reference frequency to 100 MHz . | Vertical, horizontal, triggering, measurement bandwidth, and Z-Axis checks and adjustments. | TEKTRONIX SG 503 Leveled Sine-Wave Generator. ${ }^{\text {a }}$ |
| Calibration Generator | Standard-amplitude signal levels (dc and square wave): 5 mV to 50 V. Accuracy: $\pm 0.25 \%$. High-amplitude signal levels: 1 V to 60 V . <br> Repetition rate: 1 kHz . Fast-rise signal level: 1 V . Repetition rate: 1 MHz . Rise time: 1 ns or less. Flatness: $\pm 0.5 \%$. | Signal source for gain and transient response checks and adjustments. | TEKTRONIX PG 506A Calibration Generator. ${ }^{\text {a }}$ |
| Time-Mark Generator | Marker outputs: 5 ns to 0.5 s. Marker accuracy: $\pm 0.1 \%$. Trigger output: 1 ms to $0.1 \mu \mathrm{~s}$ timecoincident with markers. | Horizontal checks and adjustments. Display adjustment. Time cursor checks. | TEKTRONIX TG 501A Time-Mark Generator. ${ }^{\text {a }}$ |
| Function Generator | Range: less than 1 Hz to 100 kHz ; sinusoidal output; amplitude variable up to greater than 10 V $p-p$ open circuit with dc offset adjust. | Low-frequency checks, X-Y phase check. | TEKTRONIX FG 502 Function Generator. ${ }^{\text {a }}$ |
| Coaxial Cable (2 required) | Impedance: $50 \Omega$. <br> Length: 42 in. <br> Connectors: BNC. | Signal interconnection. | Tektronix Part Number 012-0057-01. |
| Precision Coaxial Cable | Impedance: $50 \Omega$. Length: 36 in. Connectors: BNC. | Used with PG 506A Calibration Generator and SG 503 Sine-Wave Generator. | Tektronix Part Number 012-0482-00. |

[^9]Table 4-1 (cont)

| Item and Description | Minimum Specification | Use | Example of Test Equipment |
| :---: | :---: | :---: | :---: |
| Termination <br> (2 required) | Impedance: $50 \Omega$. Connectors: BNC. | Signal termination. | Tektronix Part Number 011-0049-01. |
| 10X Attenuator | Ratio: 10X. <br> Impedance: $50 \Omega$. <br> Connectors: BNC. | Triggering checks. | Tektronix Part Number 011-0059-02. |
| $2 \times$ Attenuator | Ratio: 2X. <br> Impedance: $50 \Omega$. <br> Connectors: BNC. | Triggering checks. | Tektronix Part Number 011-0069-02. |
| Adapter | Connectors: BNC male-to-miniature-probe tip. | Signal interconnection. | Tektronix Part Number 013-0084-02. |
| Alignment Tool | Length: 1-in shaft. <br> Bit size: 3/32 in. <br> Low capacitance; insulated. | Adjust TRACE ROTATION pot. Adjust variable capacitors and resistors. | Tektronix Part Number 003-0675-00. |
| Test Oscilloscope | Bandwidth: 20 MHz . | Z-Axis response adjustment. | TEKTRONIX 2246A. |
| Dual-Input Coupler | Connectors: BNC female-to-dual-BNC male. | Signal interconnection. | Tektronix Part Number 067-0525-02. |
| T-Connector | Connectors, BNC. | Signal interconnection. | Tektronix Part Number 103-0030-00. |
| Precision Normalizer | Input resistance: $1 \mathrm{M} \Omega$. Input capacitance: 20 pF . | Input capacitance adjustments. | Tektronix Part Number 067-1129-00. |
| TV Signal Generator | Provide composite TV video and line sync signals. | Check TV Trigger circuit. | Tektronix 067-0601-00. <br> Calibration fixture with 067-5002-00 (525/60) and 067-5010-00 (1201/60) plug-ins. |
| Digital Multimeter (DMM) | Dc volts range: 0 to 140 V . Dc voltage accuracy $\pm 0.15 \%$. $41 / 2$ digit display. | Power supply voltage checks and adjustments. | Tektronix DM 501A Digital Multimeter. ${ }^{\text {a }}$ |
| BNC Coupling Capacitor | $0.047 \mu \mathrm{~F}$. | $\mathrm{CH} 1 / \mathrm{CH} 2$ Voltmeter DC rejection ratio check. | $\begin{aligned} & \text { TEKTRONIX } \\ & \text { 015-0221-00 } \\ & \text { Coupling Capacitor } \end{aligned}$ |

${ }^{\text {a }}$ Requires a TM500-series power module.
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## DISPLAY

Equipment Required (See Table 4-1)
Time-mark generator
$50 \Omega$ BNC termination
$50 \Omega$ BNC coaxial cable

## 1. TRACE ROTATION

a. Set:

READOUT (Intensity)
A INTEN
Vertical MODE
CH 1 VOLTS/DIV
CH 1 COUPLING
A/B SELECT
Trigger MODE
Trigger SOURCE
Trigger CPLG
Trigger SLOPE
Trigger HOLDOFF
Trigger LEVEL.
Horizontal MODE
Horizontal POSITION
A SEC/DIV
Measurements

FOCUS
SCOPE BW
b. Position trace vertically to the center graticule line.

For a viewable readout
For a viewable trace
CH 1
0.1 V

AC
A Trigger
AUTO LEVEL
VERT
DC
$\mathcal{I}$ (positive)
Min
12 o'clock
A
12 o'clock
$2 \mu \mathrm{~s}$
All off (press
CLEAR DISPLAY three times)
For best defined display Off
c. CHECK-trace rotation control range is adequate to align trace with center graticule line using a small straight-bladed alignment tool.
d. ADJUST-trace parallel to center horizontal graticule line.
2. Geometry
a. Connect time-mark generator (TG 501) to CH 1 via a $50 \Omega$ BNC coaxial cable and a $50 \Omega$ BNC termination.
b. Set generator for $0.2 \mu \mathrm{~s}$ time marks.
c. Position the bottom of the CH 1 signal below the bottom graticule line.
d. CHECK-deviation of any vertical line within the center eight horizontal divisions does not exceed 0.1 division (half a minor division).
e. Set CH 1 COUPLING to GND.
f. Position trace slowly from the bottom graticule line to the top graticule line while making the following check.
g. CHECK-bowing or tilt of baseline trace doesn't exceed 0.1 division (half a minor division) within the eight vertical divisions.
h. Disconnect test signal from the 22461 Y or 2246 Mod A.

## VERTICAL

## Equipment Required (See Table 4-1)

Leveled sine-wave generator
Calibration generator
Function generator
$50 \Omega$ BNC coaxial cable
$50 \Omega$ precision BNC coaxial cable
$50 \Omega$ termination
Adapter BNC-male-to-miniature probe tip
Dual-input coupler

## 1. Input COUPLING Functional Check

a. Set:

READOUT (Intensity)
A INTEN

Vertical MODE
CH 1 and CH 2
VOLTS/DIV
CH 1 and CH 2 Input COUPLING
A/B SELECT
Trigger MODE
Trigger SOURCE
Trigger CPLG
Trigger SLOPE
Trigger LEVEL
Trigger HOLDOFF
Horizontal POSITION
Horizontal MODE
SEC/DIV
FOCUS

Measurements

## SCOPE BW

CH 2 INVERT

For a viewable readout
For a viewable trace
CH 1 and CH 2

1 V

DC
A Trigger
AUTO LEVEL VERT
DC
r (positive)
12 o'clock
Min
12 o'clock
A
0.5 ms

For best defined display
All off (press CLEAR DISPLAY three times)
Off
Off
b. Set Vertical MODE to $\mathrm{CH} 1(\mathrm{CH} 2$ off).
c. Connect calibration generator (PG 506) STD AMPL output to the CH 1 input via a $50 \Omega \mathrm{BNC}$ coaxial cable.
d. Set calibration generator to STD AMPL and AMPLITUDE to 5 V .
e. Position the bottom of the signal to the center horizontal graticule line.
f. Set CH 1 input COUPLING to AC .
g. CHECK-display is roughly centered about the center horizontal graticule line.
h. Move the test signal to the CH 2 input.
i. Set Vertical MODE to CH 2 (CH 1 off).
j. Repeat the procedure for CH 2.
k. Disconnect the test signal from the 22461 Y or 2246 Mod A.
2. CH 1 and CH 2 VOLTS/DIV Trace Shift
a. Set:

CH 1 and CH 2 Vertical MODE On
CH 1 and CH 2 VOLTS/DIV 2 mV
CH 1 and CH 2 Input COUPLING GND SCOPE BW On
b. Set Vertical MODE to CH 1 ( CH 2 off).
c. Position trace to center horizontal graticule line.
d. Switch CH 1 VOLTS/DIV through all positions from 2 mV to 5 V .
e. CHECK—trace shift does not exceed 0.2 division between steps.
f. Set Vertical MODE to CH 2 (CH 1 off).
g. Position CH 2 trace to the center horizontal graticule line.
h. Switch CH 2 VOLTS/DIV through all positions from 2 mV to 5 V .
i. CHECK-trace shift does not exceed 0.2 division between steps.
3. CH 3 and CH 4 VOLTS/DIV Trace Shift
a. Set Vertical MODE to $\mathrm{CH} 3(\mathrm{CH} 2$ off).
b. Position trace to the center horizontal graticule line.
c. Switch CH 3 VOLTS/DIV between 0.1 V and 0.5 V .
d. CHECK-trace shift does not exceed one division.
e. Set Vertical MODE to CH 4 ( CH 3 off).
f. Position trace to the center horizontal graticule line.
g. Switch CH 4 VOLTS/DIV between 0.1 V and 0.5 V .
h. CHECK-trace shift does not exceed one division.
4. CH 1 and CH 2 VAR VOLTS/DIV Trace Shift
a. Set:
Vertical MODE
CH 1 (CH 4 off) $\mathrm{CH} 1 \mathrm{VOLTS} / \mathrm{DIV}$ 2 mV
b. Position trace to center graticule line.
c. Set CH 1 VAR VOLTS/DIV fully CCW.
d. CHECK-trace shift does not exceed one division.
e. Set:

CH 1 VAR VOLTSIDIV
Vertical MODE
CH 2 VOLTSIDIV
Detent (calibrated) CH 2 (CH 1 off) 2 mV
f. Position trace to center graticule line.
g. Set CH 2 VAR VOLTS/DIV fully CCW.
h. CHECK-trace shift does not exceed one division.
i. Set CH 2 VAR VOLTS/DIV to detent (calibrated) position.

## 5. CH 1 and CH 2 Input COUPLING Trace Shift

a. Position trace to center graticule line.
b. Set CH 2 Input COUPLING to DC.
c. CHECK-trace shift does not exceed 0.25 division.
d. Set:

| Vertical MODE | $\mathrm{CH} 1(\mathrm{CH} 2$ |
| :--- | :--- |
| CH 1 Input COUPLING | off $)$ |
|  | GND |

e. Position trace to center graticule line.
f. Set CH 1 Input COUPLING to DC .
g. CHECK-trace shift does not exceed 0.25 division.
6. CH 2 INVERT Trace Shift
a. Set:
Vertical MODE to CH 2 CH 2 Input Coupling
(CH 1 off) GND
b. Position trace to center horizontal graticule line.
c. Set CH 2 INVERT On.
d. CHECK-trace shift does not exceed one division.
e. Set:
CH 2 INVERT
Off
CH 2 COUPLING
DC

## 7. CH 1 and CH 2 VAR VOLTS/DIV Range

a. Set Vertical MODE to CH 1.
b. Position CH 1 trace to the center horizontal graticule line.
c. Connect calibration generator (PG 506) STD AMPL output to the CH 1 input via $50 \Omega$ precision BNC coaxial cable. Set calibration generator to STD AMPL and AMPLITUDE to 50 mV .
d. Set:
CH 1 VOLTS/DIV
CH 1 VAR VOLTSIDIV

$$
\begin{aligned}
& 10 \mathrm{mV} \\
& \text { Fully CCW }
\end{aligned}
$$

e. CHECK-the signal amplitude is two divisions or less.
f. Set:

CH 1 VAR VOLTS/DIV
Vertical MODE CH 2 VOLTS/DIV

Detent (calibrated) $\mathrm{CH} 2(\mathrm{CH} 1$ off) 10 mV
g. Position CH 2 trace to the center horizontal graticule line.
h. Move the test signal to the CH 2 input.
i. Set CH 2 VAR VOLTS/DIV fully CCW.
j. Repeat the CHECK procedure for CH 2 .
k. Set CH 2 VAR VOLTS/DIV to detent (calibrated) position.
8. Low-Frequency Linearity
a. Set:

Vertical MODE CH 1 VOLTS/DIV SCOPE BW

CH 1 (CH 2 off) 10 mV On
b. Set calibration generator AMPLITUDE to 20 mV .
c. Move the test signal to the CH 1 input.
d. Position the top of the signal to top graticule line.
e. Check the signal amplitude is between 1.9 and 2.1 divisions.
f. Set bottom of the signal to bottom graticule line.
g. Check the signal amplitude is between 1.9 and 2.1 divisions.
h. Repeat the procedure for CH 2.

## 9. CH 1 and CH 2 Vertical Deflection Accuracy

a. Set CH 2 VOLTS/DIV to 2 mV .
b. Set calibration generator AMPLITUDE to 10 mV .
c. Position the trace two graticule lines below the center graticule line.
d. CHECK - all positions of the VOLTS/DIV settings for correct signal-to-graticule accuracy, using the settings in Table 4-2 for the checks.
e. Set calibration generator to Std Ampl output, 10 mV .
f. Move the test signal to the CH 1 input.
g. Set:

| Vertical MODE | $\mathrm{CH} 1(\mathrm{CH} 2$ off $)$ |
| :--- | :--- |
| CH 1 VOLTS/DIV | 2 mV |

h. Position the trace two graticule lines below the center graticule line.
i. Repeat CHECK procedure for CH 1 .

Table 4-2
Signal-to-Graticule Accuracy

| VOLTS/DIV <br> Setting | Calibration <br> Generator <br> AMPLITUDE Setting | Deflection <br> Accuracy <br> in divisions) |
| :---: | :---: | :---: |
| 2 mV | 10 mV | 4.90 to 5.10 |
| 5 mV | 20 mV | 3.92 to 4.08 |
| 10 mV | 50 mV | 4.90 to 5.10 |
| 20 mV | 100 mV | 4.90 to 5.10 |
| 50 mV | 200 mV | 3.92 to 4.08 |
| 0.1 V | 500 mV | 4.90 to 5.10 |
| 0.2 V | 1 V | 4.90 to 5.10 |
| 0.5 V | 2 V | 3.92 to 4.08 |
| 1 V | 5 V | 4.90 to 5.10 |
| 2 V | 10 V | 4.90 to 5.10 |
| 5 V | 20 V | 3.92 to 4.08 |

10. CH 3 and CH 4 Vertical Deflection Accuracy
a. Set:
Vertical MODE
$\mathrm{CH} 3(\mathrm{CH} 1$ off $)$ $\mathrm{CH} 3 \& \mathrm{CH} 4$ VOLTS/DIV 0.1 V
b. Position the trace two graticule lines below the center horizontal graticule line.
c. Move CH 1 test sefup to the CH 3 input.
d. Set calibration generator AMPLITUDE to 0.5 V .
e. CHECK - the signal amplitude is between 4.90 and 5.10 divisions.
f. Move the test signal to the CH 4 input.
g. Set Vertical MODE to CH 4 (CH 3 off).
h. Position the trace two graticule lines below the center horizontal graticule line.
i. Repeat CHECK for CH 4 .
j. Set CH 3 and CH 4 VOLTS/DIV to 0.5 V .
k. Set calibration generator AMPLITUDE to 2 V .
I. CHECK-the signal amplitude is between 3.92 and 4.08 divisions.
m. Set Vertical MODE to CH 3 ( CH 4 off).
n. Move the test signal to the CH 3 input.
o. Repeat CHECK procedure for CH 3 .
p. Disconnect the test setup from the 22461 Y or 2246 Mod A.
11. ADD Mode and CH 2 INVERT Deflection Accuracy
a. Set:

| Vertical MODE | ADD (all others <br> off) |
| :--- | :--- |
| CH 1 and CH 2 VOLTS/DIV | 0.1 V |
| CH 1 and CH 2 Input |  |
| COUPLING | DC |

b. Connect calibration generator STD AMPL output to the CH 1 and CH 2 inputs via $50 \Omega$ precision BNC coaxial cable and a BNC dual-input coupler.
c. Set the calibration generator AMPLITUDE to 0.2 V .
d. Position the ADD signal to the center of the crt graticule with the CH 1 and CH 2 POSITION controls.
e. CHECK-that the ADD signal amplitude is between 3.92 and 4.08 divisions.
f. Set CH 2 INVERT On.
h. CHECK-the ADD signal amplitude is 0.08 division (less than half a minor graticule division) or less excluding trace width (sweep will free run).
g. Disconnect the test setup from the 22461 Y or $2246 \operatorname{Mod} A$.

## 12. Vertical POSITION Range (all channels)

a. Set:

| A SEC/DIV | 0.1 ms |
| :--- | :--- |
| CH 1 Vertical MODE | $O n$ (ADD off) |
| CH 1 VOLTS/DIV | $1 \vee$ |
| CH 2 INVERT | $O f f$ |
| SCOPE BW | Off |
| CH 1 and CH 2 Input |  |
| COUPLING | AC |

b. Connect leveled sine-wave generator (SG 503) output to the CH 1 and CH 2 inputs via a $50 \Omega$ BNC coaxial cable, a $50 \Omega$ BNC termination, and a BNC dual-input coupler.
c. Position trace to center horizontal graticule line.
d. Set leveled sine-wave generator output for twodivision signal at 50 kHz .
e. Set:
CH 1 VOLTS/DIV
0.1 V
CH 1 POSITION
Fully CW
f. CHECK-that the bottom of the waveform is above the center horizontal graticule line.
g. Set CH 1 POSITION fully CCW.
h. CHECK-that the top of the waveform is below the center horizontal graticule line.
i. Set:

| CH 1 POSITION | 12 o' clock $^{\prime}$ |
| :--- | :--- |
| Vertical MODE | $\mathrm{CH} 2(\mathrm{CH} 1$ off $)$ |
| CH 2 POSITION | Fully CW |

j. CHECK-that the bottom of the waveform is above the center horizontal graticule line.
k. Set CH 2 POSITION fully CCW.
I. CHECK-that the top of the waveform is below the center horizontal graticule line.
m. Set CH 2 POSITION to 12 o'clock.
n. Move the BNC dual-input coupler from the CH 1 and CH 2 inputs to the CH 3 and CH 4 inputs.
o. Set:

| Vertical MODE | $\mathrm{CH} 3(\mathrm{CH} 2$ off $)$ |
| :--- | :--- |
| CH 3 and CH 4 | 0.1 V |
| VOLTS/DIV | Fully CW |
| CH 3 POSITION |  |

p. CHECK-that the bottom of the waveform is above the center graticule line.
q. Set CH 3 POSITION fully CCW.
r. CHECK-that the top of the waveform is below the center graticule line.
s. Set:
CH 3 POSITION
12 o'clock Vertical MODE CH 4 (CH 3 off)
t. Repeat the procedure for CH 4 .
u. Set CH 4 POSITION to 12 o'clock.
v. Disconnect the test setup from the 22461 Y or $2246 \operatorname{Mod} A$.
13. CH 1 to CH 2 Signal Delay Match
a. Set:

Vertical MODE
CH 1 and CH 2
Input COUPLING
CH 1 and CH 2
VOLTS/DIV
SEC/DIV
Trigger SOURCE
0.1 V

CH 1 and CH 2 (CH 4 off)

DC

20 ns
CH 3
b. Superimpose the CH 1 and CH 2 traces at the $100 \%$ graticule marking.
c. Connect calibration generator (PG 506) FAST RISE, rising-edge signal to the CH 1 and CH 2 inputs via a $50 \Omega$ precision BNC coaxial cable, a $50 \Omega$ BNC termination, and a BNC dual-input coupler.
d. Connect calibration generator TRIG OUT signal to the CH 3 input via a $50 \Omega$ BNC coaxial cable and a $50 \Omega$ BNC termination.
e. Set the calibration generator to FAST RISE and adjust PULSE AMPLITUDE for five divisions of signal amplitude at 1 MHz .
f. Position the rising edges of the superimposed waveforms horizontally to the center vertical graticule line.
g. Set $\times 10$ MAG On (for $2 \mathrm{~ns} /$ div sweep speed).
h. CHECK-that the leading edges of the two waveforms have less than 0.2 horizontal division separation at the center graticule line, excluding trace width.

## 14. CH 1 to CH 4 Signal Delay Match

a. Set Vertical MODE to CH 1 and CH 4 ( CH 2 off).
b. Move the CH 2 signal to the CH 4 input connector.
c. Superimpose the CH 4 waveform on the CH 1 waveform.
d. CHECK-that the leading edges of the two waveforms have less than 0.4 horizontal division separation at the center graticule line, excluding trace width.

## 15. CH 3 to CH 4 Signal Delay Match

a. Set:

| Vertical MODE | CH 3 and CH 4 <br> (CH 1 off) |
| :--- | :--- |
| Trigger SOURCE | CH 2 |

b. Move the CH 3 trigger signal to the CH 2 input and the CH 1 signal to the CH 3 input.
c. Superimpose CH 3 and CH 4 waveforms at the center graticule line, using the HORIZONTAL POSITION control, if necessary.
d. CHECK-that the leading edges of the two waveforms have less than 0.2 horizontal division separation at the center graticule line.
e. Disconnect the test setup.
16. CH 1 and CH 2 Vertical Bandwidth
a. Set:

| X10 MAG | Off |
| :--- | :--- |
| Vertical MODE | CH 1 (CH 3 and |
| SEC/DIV | $\mathrm{CH} 4 \mathrm{off})$ |
| CH 1 VOLTS/DIV | 0.1 ms |
| CH 1 and CH 2 Input | 5 mV |
| COUPLING |  |
| Trigger SOURCE | DC |
| Horizontal POSITION | VERT |
|  | 12 o'clock |

b. Connect leveled sine-wave generator (SG 503) output to the CH 1 input via a $50 \Omega$ precision BNC coaxial cable and a $50 \Omega$ BNC termination.
c. Set the Leveled Sine-Wave Generator output for a six-division signal amplitude at 50 kHz .
d. Set the generator Frequency Range and Frequency Variable controls for a 100 MHz output signal.
e. CHECK-the displayed signal amplitude is 4.2 divisions or more.
f. Repeat the frequency setup and CHECK procedure for VOLTS/DIV settings of 50 mV and 0.5 V .
g. Move the test signal to the CH 2 input.
h. Set:

Vertical MODE
CH 2 VOLTS/DIV

CH 2 (CH 1 off) 5 mV
i. Repeat the complete Bandwidth check procedure for Channel 2.
17. CH 3 and CH 4 Vertical Bandwidth
a. Set:

Vertical MODE
CH 3 (CH 2 off)
CH 3 and CH 4 VOLTS/DIV 0.1 V
b. Connect leveled sine-wave generator (SG 503) output to the CH 3 input via a $50-\Omega$ precision BNC coaxial cable and a $50-\Omega$ BNC termination.
c. Set the generator output for a six-division signal display at 50 kHz .
d. Set the generator Frequency Range and Frequency Variable controls for a 100 MHz output frequency.
e. CHECK-that the signal display amplitude is 4.2 divisions or more.
f. Repeat the procedure for 0.5 VOLTS/DIV setting.
g. Move the test signal to the CH 4 input.
h. Set Vertical MODE to CH 4 ( CH 3 off).
i. Repeat the procedure for CH 4.
j. Disconnect the test setup.

## 18. CH 1 and CH 2 Aberrations

a. Set:

```
VERT MODE
CH }1\mathrm{ VOLTS/DIV
CH 1 COUPLING
A SEC/DIV
CH 1 (CH 4 off)
5 mV
DC
20ns
```

b. Connect calibration generator (PG 506) FAST RISE $-\Gamma$ output to the CH 1 input via a $10 \mathrm{X}, 50-\Omega$ attenuator, a $50-\Omega$ precision BNC coaxial cable, and a $50-\Omega$ BNC termination.
c. Position top of waveform to $100 \%$ graticule marking.
d. Adjust calibration generator PULSE AMPLITUDE for five divisions of signal display.
e. CHECK-that aberrations are $\leq 0.25$ division, peak-to-peak.
f. Repeat steps $c$. through e. for VOLTS/DIV settings of 10 mV through 0.2 V . Remove the 10 X , $50-\Omega$ attenuator when necessary to maintain a five-division display.
g. Repeat the procedure for CH 2 .
h. Disconnect the test setup.
19. SCOPE BW (Bandwidth Limit) Accuracy
a. Set:

Vertical MODE
CH 1 VOLTS/DIV SCOPE BW A SEC/DIV

CH 1 (CH 2 off) O On 0.1 ms
b. Connect leveled sine-wave generator (SG 503) output to the CH 1 input via a $50-\Omega$ precision BNC coaxial cable and a $50-\Omega$ BNC termination.
c. Set leveled sine-wave generator (SG 503) output for a six-division signal amplitude at 50 kHz .
d. Set the leveled sine-wave generator Frequency Range and Frequency Variable controls to produce a signal display amplitude of 4.2 divisions.
e. CHECK-that the sine-wave generator output frequency is between 17 MHz and 23 MHz .
f. Disconnect the test setup.

## 20. Common-mode Rejection Ratio

a. Connect leveled sine-wave generator (SG 503) output to the CH 1 and CH 2 input connectors via a $50-\Omega$ precision BNC coaxial cable, a $50-\Omega$ BNC termination, and a BNC dual-input coupler.
b. Set the leveled sine-wave generator output for an eight-division signal-display amplitude at 50 kHz .
c. Set:

| ADD MODE | On |
| :--- | :--- |
| CH 2 VOLTS/DIV | 10 mV |
| CH 2 INVERT | On |
| CH 1 Vertical MODE | Off |
| SCOPE BW | Off |

d. Adjust CH 1 or CH 2 VAR VOLTS/DIV for smallest signal amplitude (as needed).
e. Set the leveled sine-wave output frequency to 10 MHz .
f. Set:

| CH 1 Vertical MODE | On |
| :--- | :--- |
| ADD MODE | Off |

g. Set the leveled sine-wave output amplitude for an eight-division display.
h. Set the Vertical MODE to $A D D$ (CH 1 off).
i. CHECK-the signal is less than 0.3 division in amplitude.
j. Set the leveled sine-wave generator frequency to 100 MHz .
k. Set VERTICAL MODE to CH 1 (ADD off).
I. Set the leveled sine-wave output amplitude for an eight-division display.
m. Set the VERTICAL MODE to ADD (CH 1 off).
n. CHECK-the signal is less than 0.8 division in amplitude.
o. Disconnect the test setup.
p. Set both CH 1 and CH 2 VAR controls to their calibrated (fully clockwise in detent) positions.
q. Disconnect the test setup.

## 21. Channel Isolation

a. Set:

Vertical MODE
CH 2 INVERT
CH 1 and CH 2
(ADD off)
Off
$\mathrm{CH} 1, \mathrm{CH} 2, \mathrm{CH} 3$, and CH 4 VOLTS/DIV 0.1 V
Trigger SOURCE
CH 1
b. Connect the leveled sine-wave generator (SG 503) output to the CH 1 input via a $50-\Omega$ precision BNC coaxial cable and a $50-\Omega$ BNC termination.
c. Set the leveled sine-wave generator (SG 503) output for a five-division signal display amplitude at 100 MHz .
d. Set $\mathrm{CH} 2, \mathrm{CH} 3$, and CH 4 Vertical MODE On (CH 1 off).
e. CHECK-display amplitude is 0.1 division or less, excluding trace width, on the $\mathrm{CH} 2, \mathrm{CH} 3$, and CH 4 traces.
f. Move sine-wave generator signal to the CH 2 input.
g. Set:
$\mathrm{CH} 1, \mathrm{CH} 3$, and
CH 4 Vertical MODE Trigger SOURCE

On (CH 2 off) CH 2
h. CHECK-display amplitude is 0.1 division or less, excluding trace width, on the $\mathrm{CH} 1, \mathrm{CH} 3$, and CH 4 traces.
i. Move sine-wave generator signal to the CH 3 input.
j. Set:
$\mathrm{CH} 1, \mathrm{CH} 2$, and CH 4
Vertical MODE
On (CH 3 off)
Trigger SOURCE
CH 3
k. CHECK-display amplitude is 0.1 division or less, excluding trace width, on the $\mathrm{CH} 1, \mathrm{CH} 2$, and CH 4 traces.
I. Move sine-wave generator signal to the CH 4 input.
m. Set:

| $\mathrm{CH} 1, \mathrm{CH} 2$ 2, and CH 3 |  |
| :--- | :--- |
| Vertical MODE | On (CH 4 off) |
| Trigger SOURCE | CH 4 |

$\mathrm{CH} 1, \mathrm{CH} 2$, and CH 3
Trigger SOURCE CH 4
n. CHECK-display amplitude is 0.1 division or less, excluding trace width, on the $\mathrm{CH} 1, \mathrm{CH} 2$, and CH 3 traces.
o. Disconnect the test setup.
22. AC-Coupled Lower -3 dB Point
a. Set:
A SEC/DIV
50 ms
Vertical MODE
Trigger SOURCE
CH 1 (all others off) VERT
Trigger MODE
NORM
b. Connect function generator (FG 502) output to the CH 1 input via a $50-\Omega$ BNC coaxial cable and a $50-\Omega$ BNC termination.
c. Set the function generator output controls to produce a six-division sine-wave display at 10 Hz (with no dc offset).

## NOTE

$$
\begin{aligned}
& \text { It may be necessary to adjust the } \\
& \text { TRIGGER LEVEL control to obtain a } \\
& \text { triggered display }
\end{aligned}
$$

d. Set CH 1 Input COUPLING to AC.
e. CHECK-display amplitude is 4.2 division or more.
f. Set Vertical MODE to CH 2 ( CH 1 off).
g. Repeat the procedure for CH 2 .
h. Disconnect the test equipment from the 22461 Y or 2246 Mod A.
23. Vertical ALT and CHOP Modes
a. Set:

Vertical MODE

CH 1 and CH 2
VOLTS/DIV
CH 3 and CH 4
VOLTSIDIV
CH 1 and CH 2 Input
COUPLING
Horizontal MODE
SEC/DIV
Trigger MODE
$\mathrm{CH} 1, \mathrm{CH} 2$, CH 3 , CH 4 , ALT (CHOP off)

10 mV
0.1 V

DC
A
1 ms AUTO LEVEL
b. Position all traces for two divisions of separation with the CH 1 trace near the top; then in order down the graticule area with the CH 4 trace near the bottom.
c. Set SEC/DIV to 10 ms .
d. CHECK-that four traces are sweeping across the screen alternately.
e. Set CHOP Vertical MODE On.
f. CHECK-that four traces are sweeping across the screen simultaneously.

## 24. BEAM FIND Functional Check

a. Push BEAM FIND in and hold.
b. CHECK-the signal is visible and compressed fully within the graticule area as the horizontal and vertical position controls are rotated through their ranges.
c. Release the BEAM FIND button.
d. Set all Vertical and Horizontal POSITION controls at the 12 o'clock position.

## 25. A and B Trace Separation

a. Set:

| A SEC/DIV | 1 ms |
| :--- | :--- |
| Vertical MODE | CH 1 (others |
|  | off) |
| Horizontal MODE | ALT |
| B SEC/DIV | 0.5 ms |
| A/B SELECT | B |
| B Trigger MODE | RUNS AFTER |
| TRACE SEP | Fully CW |

b. Position the CH 1 trace below the center horizontal graticule line to display the separated $B$ trace.
c. CHECK-for at least four divisions of upward trace separation between the B trace and the A trace.
d. Set TRACE SEP fully CCW.
e. Position the CH 1 trace above the center horizontal graticule line to display the separated $B$ trace.
f. CHECK-for at least four divisions downward trace separation of the B trace from the A trace.

## TRIGGERING

```
Equipment Required (See Table 4-1)
    Leveled sine-wave generator
    50 \Omega BNC coaxial cable
    2X BNC attenuator
    Dual-input coupler
```

Function generator
10X BNC attenuator
$50 \Omega$ BNC termination
TV signal generator

## 1. 500 Hz Trigger Sensitivity

a. Set:

READOUT (Intensity)
A INTEN
Vertical MODE
CH 1 and CH 2 input
COUPLING
CH 1 VOLTS/DIV
SCOPE BW
Horizontal MODE
A SEC/DIV
A/B SELECT
Trigger MODE
Trigger SOURCE
Trigger CPLG
Trigger SLOPE
Trigger HOLDOFF
FOCUS
Measurements

Horizontal POSITION

For a viewable readout
For a viewable trace
CH 1

DC
0.1 V

On
A
2 ms
A Trigger
AUTO LEVEL
VERT
AC
$\Gamma$ (positive)
Min
For best defined display All off (press
CLEAR DISPLAY three times) 12 o'clock
b. Connect function generator (FG 502) output to the CH 1 input via a $50-\Omega$ BNC coaxial cable, and a $50-\Omega$ BNC termination.
c. Set function generator (FG 502) output to produce a 7.0 division sine-wave display at 500 Hz .
d. Add a 10 X and a 2 X BNC attenuator before the $50-\Omega$ BNC termination (for a 0.35 division display).

NOTE
The Trigger LEVEL control may be used to obtain a stable display.
e. CHECK-that the display is stably triggered with DC, HF REJ, and AC Trigger CPLG; and that the display will not trigger on NOISE REJ or LF REJ Trigger CPLG.
f. Set:

Trigger CPLG DC
A/B SELECT B Trigger
Trigger MODE
Trigger SOURCE
Trigger SLOPE
Horizontal MODE
B SEC/DIV
deLAY Time

B INTEN

## NOTE

It may be necessary to adjust the Trigger
LEVEL control to obtain a display.
g. CHECK—that using the Trigger LEVEL control the display can be stably triggered in DC, HF REJ, and AC Trigger CPLG; and that the display cannot be triggered in NOISE REJ or LF REJ Trigger CPLG.
h. Disconnect the test setup from the CH 1 input.
2. 500 kHz Trigger Sensitivity
a. Set:

| SCOPE BW | Off |
| :--- | :--- |
| Horizontal MODE | A |
| A/B SELECT | A Trigger |
| A SEC/DIV | $2 \mu \mathrm{~s}$ |

b. Connect leveled sine-wave generator (SG 503) output to the CH 1 input via a $50-\Omega \mathrm{BNC}$ coaxial cable and a $50-\Omega$ BNC termination.
c. Set leveled sine-wave generator output to produce a 7.0 division sine-wave display amplitude at 500 kHz .
d. Add a 10 X and a 2 X BNC attenuator before the $50-\Omega$ BNC termination (for a 0.35 division display amplitude).
e. CHECK-that the display cannot be triggered in either HF REJ of NOISE REJ CPLG.
f. Set:
Horizontal MODE
A/B SELECT
B
B SEC/DIV
B Trigger
$1 \mu \mathrm{~s}$
g. CHECK-that the display cannot be triggered in HF REJ or NOISE REJ CPLG by adjusting the Trigger LEVEL control.
3. 25 MHz Trigger Sensitivity
a. Set:

| Horizontal MODE | A |
| :--- | :--- |
| A/B SELECT | A Trigger |
| Trigger CPLG | DC |
| A SEC/DIV | 50 ns |

b. Remove the 10X and 2X BNC attenuators from the signal path.
c. Set leveled sine-wave generator output to produce a 7.0 division display amplitude at 25 MHz .
d. Add a 10 X and a 2 X BNC attenuator before the $50-\Omega$ BNC termination.
e. CHECK-that the display is stably triggered in DC, LF REJ, and AC Trigger CPLG; the display is not triggered in NOISE REJ and HF REJ Trigger CPLG settings.
f. Set:

| Trigger CPLG | AC |
| :--- | :--- |
| Horizontal MODE | B |
| A/B SELECT | B Trigger |
| B SEC/DIV | 20 ns |

g. CHECK-that using the Trigger LEVEL control the display can be stably triggered in DC, LF REJ, and AC Trigger CPLG; the display cannot be triggered in NOISE REJ and HF REJ Trigger CPLG settings.
h. Set leveled sine-wave generator (SG 503) to produce a 1.4 division display at 25 MHz .
i. CHECK-that the display can be stably triggered with NOISE REJ Trigger CPLG but does not trigger with HF REJ CPLG.
j. Set:

| Horizontal MODE | A |
| :--- | :--- |
| A/B SELECT | A Trigger |

k. CHECK-that the display is stably triggered with NOISE REJ Trigger CPLG but does not trigger with HF REJ CPLG. (The Trigger LEVEL control may be adjusted to improve display stability in NOISE REJ CPLG.)
4. 100 MHz Trigger Sensitivity
a. Set Trigger CPLG to DC.
b. Set leveled sine-wave generator to produce a 1.0 division display at 100 MHz .
c. CHECK-that the display is stably triggered in DC, LF REJ, and AC Trigger CPLG; the display is not triggered in HF REJ Trigger CPLG.
d. Set:

| Horizontal MODE | B |
| :--- | :--- |
| A/B SELECT | B Trigger |

e. CHECK-that using the Trigger LEVEL control the display can be stably triggered in DC, LF REJ, and AC Trigger CPLG; the display cannot be triggered in HF REJ Trigger CPLG.
f. Set:

| Horizontal MODE | A |
| :--- | :--- |
| Vertical MODE | CH 2 (CH 1 off) |
| CH 2, CH 3, and CH 4 |  |
| VOLTS/DIV | $0.1 \vee$ |
| A/B SELECT | A Trigger |
| Trigger CPLG | DC |

g. Move test signal from CH 1 to the CH 2 input.
h. Set leveled sine-wave generator output to produce a 1.0 division display amplitude at 100 MHz .
i. CHECK-that a stable display can be obtained. (The Trigger LEVEL control may be adjusted to improve the display stability.)
j. Repeat procedure for the CH 3 and CH 4 (turn on the appropriate Vertical MODE and move the test signal as required).
5. 100 MHz NOISE REJ Trigger Sensitivity
a. Move test signal to the CH 1 input.
b. Set Vertical MODE to CH 1 (others off).
c. Remove the $2 \times$ BNC attenuator from the test signal path.
d. Set Leveled Sine-Wave Generator output for a 2.2 division display amplitude at 100 MHz .
e. CHECK-that the display is stably triggered with NOISE REJ Trigger CPLG.

## 6. Single Sweep Mode

a. Set:

| Horizontal MODE | A |
| :--- | :--- |
| A SEC/DIV | $10 \mu \mathrm{~s}$ |
| A/B SELECT | A Trigger |

b. Remove the $10 X$ and $2 X$ BNC attenuators from the test signal path.
c. Set leveled sine-wave generator output to produce a 7.0 division display amplitude at 50 kHz .
d. Add a 10 X and a $2 \times$ BNC attenuator before the $50-\Omega$ BNC termination. (Display should stably trigger with AUTO LEVEL finding the correct trigger level setting.)
e. Set:

A Trigger MODE
NORM
CH 1 Input COUPLING
GND Trigger MODE SGL SEQ
f. CHECK-that the Trigger READY LED turns on and remains on.
g. Set:
A INTEN
3/4 fully CW
CH 1 Input COUPLING
DC (see
CHECK below)
h. CHECK-that the TRIG'D LED flashes, and the READY LED turns off after a single sweep and readout display occurs when the Input COUPLING switches to DC.
7. Trigger LEVEL Control Range
a. Set:

Trigger MODE
AUTO (not AUTO LEVEL)
Trigger LEVEL A INTEN

Fully CCW For a good viewing intensity
b. Remove 10X and $2 \times \mathrm{BNC}$ attenuators from the test signal path.
c. Increase leveled sine-wave generator output level until a stably triggered display is just obtainable.
d. Set Trigger LEVEL fully CW.
e. Set leveled sine-wave generator output for a stable display (if necessary).
f. Set CH 1 VOLTS/DIV to 1 V .
g. CHECK-that the CH 1 signal display amplitude is four divisions or more (peak-to-peak). Note that the signal is not triggered.
h. Disconnect the test setup from the 22461 Y or 2246 Mod A.
8. TV Field Trigger Sensitivity
a. Set:

Vertical MODE
$\mathrm{CH} 2(\mathrm{CH} 1$ off) CH 2 VOLTS/DIV SEC/DIV
Trigger SLOPE
Trigger MODE
Trigger LEVEL
c. Set:
CH 2 INVERT
Off
Trigger SLOPE $\neg$ (negative)
d. CHECK-that a stable display is obtained.
e. Set CH 2 VAR VOLTS/DIV to Detent Position (calibrated).
f. Disconnect the TV signal generator from the 2246 1Y or 2246 Mod A.

## 10. Line Trigger Functional Check

a. Set:

CH 2 VOLTS/DIV

CH 2 Input COUPLING
A SEC/DIV
Trigger MODE
Trigger SOURCE
Trigger CPLG
0.1 V (without
a 10 X probe
attached)
DC
5 ms
AUTO LEVEL
LINE
DC
b. Connect a 10 X probe to the CH 2 input connector.
c. Attach the probe tip to a length of wire at least 4 in. long. Hold the wire near the instrument power cord.
d. CHECK-that the display can be triggered in both $\sim$ (positive) and $\tau$ (negative) slopes.
e. Disconnect the test setup.

## HORIZONTAL

Equipment Required (See Table 4-1)
Time-mark generator
$50 \Omega$ BNC coaxial cable
$50 \Omega \mathrm{BNC}$ termination

1. $A$ and $B$ Sweep Length
a. Set:

READOUT (Intensity)
A INTEN
Vertical MODE
CH 1 and CH 2
Input COUPLING
CH 1 VOLTS/DIV
Horizontal MODE
A SEC/DIV
Horizontal POSITION
A/B SELECT
Trigger MODE
Trigger SOURCE
Trigger CPLG
Trigger SLOPE
Trigger HOLDOFF
Trigger LEVEL
Measurements

FOCUS

For a viewable readout
For a viewable trace
CH 1 (CH 2 off)
DC
0.5 V

A
2 ms
12 o'clock
A Trigger
AUTO LEVEL
VERT
AC
$\Gamma$ (positive)
Min
12 o'clock
All off (press
CLEAR
DISPLAY three times)
For best defined display
b. Connect time-mark generator (TG 501) to the CH 1 input via a $50-\Omega$ BNC coaxial cable and a $50-\Omega$ BNC termination.
c. Set generator for 2 ms time marks.
d. CHECK-sweep length of the A trace is greater than 10 divisions.
e. Set:

Horizontal MODE B
B SEC/DIV
1 ms
A/B SELECT Trigger MODE $K O R$ DELAY Control

B INTEN
f. CHECK-the Delay Time readout is $? 0.000 \mathrm{~ms}$, and the B Sweep length is greater than 10 divisions.
2. Horizontal POSITION Range
a. Set:
Horizontal MODE
A
Horizontal POSITION
Fully CW
b. CHECK-that the start of trace positions past the center vertical graticule line.
c. Set Horizontal POSITION fully CCW.
d. CHECK-that the 11 th time marker is positioned to the left of the center vertical graticule line.
3. VAR SECIDIV Range
a. Set:
SEC/DIV
1 ms
SEC/DIV VAR
Fully CCW Horizontal POSITION

$$
12 \text { o'clock }
$$

b. Set time mark generator for 5 ms time marks.
c. CHECK-the time-mark spacing is equal to or less than two divisions.
d. Set SEC/DIV VAR fully CW (calibrated detent).

## 4. Magnifier Registration

a. Set $\times 10 \mathrm{MAG}$ on.
b. Position the rising edge of a time marker to the center vertical graticule line.
c. Set $\times 10 \mathrm{MAG}$ off.
d. CHECK-for less than 0.5 division horizontal trace shift.

## 5. $A$ and $B$ Timing Accuracy and Linearity

a. Set A SEC/DIV to 20 ns .
b. Set time-mark generator for 20 ns time marks.
c. Position the time marker peaks vertically to the center horizontal graticule line (allows use of the minor division graticule markings as an aid in making the accuracy checks).
d. Position the second time marker to the second vertical graticule line.
e. CHECK-that the tenth marker is within $\pm 0.16$ division of the tenth graticule line.
f. CHECK-that in any group of three time markers, having the first and third markers aligned with their respective graticule lines, the second marker is within $\pm 0.05$ division of its graticule line.
g. Repeat the procedure for all other SEC/DIV settings. Use Table 4-3, Settings for Timing Accuracy Checks, for the SEC/DIV and timemark generator settings.
h. Set SEC/DIV to 20 ns .
i. Set time-mark generator for 20 ns time marks.
j. Set:

| Horizontal MODE | B |
| :--- | :--- |
| B INTEN | For a viewable |
|  | display |

k. Repeat the CHECK procedure for all the B SEC/ DIV settings.
6. A and B Magnified Timing Accuracy
a. Set time-mark generator for 5 ns time marks.
b. Set:

| Horizontal MODE | A |
| :--- | :--- |
| A SEC/DIV | 20 ns |
| Horizontal MODE | B |
| Horizontal POSITION | 120 'clock |
| X10 MAG | On (for |
|  | $2 \mathrm{~ns} / \mathrm{div}$ sweep |
|  | speed) |
| CH 1 VOLTS/DIV | 0.5 V (use |
|  | 0.2 V for the |
|  | 5 ns time |
|  | markers if |
|  | necessary) |

c. Align the rising edge a time marker with the second vertical graticule line (center the display vertically).
d. CHECK-that the rising edge of the fourth displayed time marker to the right of the one positioned in step c. crosses the center horizontal graticule line at between 8.27 divisions to 8.73 divisions.
e. Set SEC/DIV to 5 ns .
f. Align a time marker to the second vertical graticule line.
g. CHECK-that the tenth displayed time marker is within 0.24 division (left or right) of the tenth graticule line.
h. Repeat the timing checks for all SEC/DIV settings between 10 ns and 0.5 s . Use the SEC/DIV and Time Mark Generator X10 MAG settings given in Table 4-3.

Table 4-3
Settings for Timing Accuracy Checks

| SECIDIV Setting |  | Time-Mark Setting |  |
| :---: | :---: | :---: | :---: |
| Normal | X10 MAG | Normal | X10 MAG |
| 20 ns 50 ns | 2 ns <br> 5 ns | 20 ns 50 ns | $\begin{aligned} & 5 \mathrm{~ns} \\ & 5 \mathrm{~ns} \end{aligned}$ |
| 0.1 ms | 10 ns | 0.1 ns | 10 ns |
| $0.2 \mu \mathrm{~s}$ | 20 ns | $0.2 \mu \mathrm{~s}$ | 20 ns |
| $0.5 \mu \mathrm{~s}$ | 50 ns | $0.5 \mu \mathrm{~s}$ | 50 ns |
| $1 \mu \mathrm{~s}$ | $0.1 \mu \mathrm{~s}$ | $1 \mu \mathrm{~s}$ | $0.1 \mu \mathrm{~s}$ |
| $2 \mu \mathrm{~s}$ | $0.2 \mu \mathrm{~s}$ | $2 \mu \mathrm{~s}$ | $0.2 \mu \mathrm{~s}$ |
| $5 \mu \mathrm{~s}$ | $0.5 \mu \mathrm{~s}$ | $5 \mu \mathrm{~s}$ | $0.5 \mu \mathrm{~s}$ |
| $10 \mu \mathrm{~s}$ | $1 \mu \mathrm{~s}$ | $10 \mu \mathrm{~s}$ | $1 \mu \mathrm{~s}$ |
| $20 \mu s$ | $2 \mu s$ | $20 \mu \mathrm{~s}$ | $2 \mu s$ |
| $50 \mu \mathrm{~s}$ | $5 \mu \mathrm{~s}$ | $50 \mu \mathrm{~s}$ | $5 \mu \mathrm{~s}$ |
| 0.1 ms | $10 \mu \mathrm{~s}$ | 0.1 ms | $10 \mu \mathrm{~s}$ |
| 0.2 ms | $20 \mu s$ | 0.2 ms | $20 \mu \mathrm{~s}$ |
| 0.5 ms | $50 \mu \mathrm{~s}$ | 0.5 ms | $50 \mu \mathrm{~s}$ |
| 1 ms | 0.1 ms | 1 ms | 0.1 ms |
| 2 ms | 0.2 ms | 2 ms | 0.2 ms |
| 5 ms | 0.5 ms | 5 ms | 0.5 ms |
| A Sweep only |  |  |  |
| 10 ms | 1 ms | 10 ms | 1 ms |
| 20 ms | 2 ms | 20 ms | 2 ms |
| 50 ms | 5 ms | 50 ms | 5 ms |
| 0.1 s | 10 ms | 0.1 s | 10 ms |
| 0.2 s | 20 ms | 0.2 s | 20 ms |
| 0.5 s | 50 ms | 0.5 s | 50 ms |

i. Set:

## Horizontal MODE

 SEC/DIV
## A

2 ns (with $\times 10$ MAG on)
j. Set time-mark generator for 5 ns time marks.
k. Repeat the magnified accuracy and linearity for the A Sweep at all SEC/DIV settings.
7. Delay Time Jitter
a. Set:

| X10 MAG | Off |
| :--- | :--- |
| A SEC/DIV | 1 ms |
| Horizontal MODE | ALT |
| SEC/DIV | $0.5 \mu \mathrm{~s}$ |

b. Set time-mark generator for 1 ms time marks.
c. Position the intensified dot to the leading edge of the 10th time marker to display the rising edge on the B Trace (using the $k$ OR DELAY control).
d. Set:
Horizontal MODE
B INTEN

## B

 Fully CW(maximum intensity)
e. CHECK-that the jitter on the leading edge does not exceed one division over a two-second interval. Disregard slow drift.
8. Delay Time Accuracy
a. Set:

Horizontal MODE
B SEC/DIV
TRACE SEP

CH 1 POSITION

## ALT

$10 \mu \mathrm{~s}$
Fully CCW (maximum downward position)
To display both the ALT and the $B$ Delayed Traces
b. Position the first time marker on the ALT trace to first vertical graticule line (left-most edge).
c. Position the intensified zone to full left position (counterclockwise rotation of the $k-$ OR DELAY control).
d. CHECK-that the readout is $? 0.000 \mathrm{~ms}$.
e. Position the intensified zone to the second time marker and align the leading edge of the time marker displayed on the B Trace to the left-most (first) graticule line. Using the Readout Accuracy Limits given in Table 4-4, check the delay time accuracy.
f. Repeat the procedure for the third through 10th time markers.

Table 4-4
Delay Time Accuracy

| Time Marker | Readout Accuracy Limits |
| :---: | :---: |
| 1st | $? 0.000 \mathrm{~ms}$ |
| 2nd | 0.975 ms to 1.025 ms |
| 3rd | 1.970 ms to 2.030 ms |
| 4th | 2.965 ms to 3.035 ms |
| 5th | 3.960 ms to 4.040 ms |
| 6th | 4.955 ms to 5.045 ms |
| 7th | 5.950 ms to 6.050 ms |
| 8th | 6.945 ms to 7.055 ms |
| 9th | 7.940 ms to 8.060 ms |
| 10th | 8.935 ms to 9.065 ms |

## 9. Delay Time Position Range

a. Set time-mark generator for 0.1 ms .
b. Set:

| A SEC/DIV | 1 ms |
| :--- | :--- |
| B SEC/DIV | $5 \mu \mathrm{~s}$ |
| $k-$ OR DELAY control | $C C W$ to $? 0.000$ |

c. CHECK-that the intensified zone is positioned at or before the second time mark.
d. Turn the $k-$ OR DELAY control clockwise until the delay readout stops increasing (largest number).
e. CHECK-that the intensified dot is positioned at or after the 99th time marker (located at a Delay Time of 9.9 ms ).
f. Disconnect the time-mark generator from the 2246 1Y or 2246 Mod A.

## 10. X-Axis Gain Accuracy

a. Set:

| Horizontal MODE | $\mathrm{X}-\mathrm{Y}$ |
| :--- | :--- |
| Vertical MODE | CH 2 (CH 1 off) |
| CH 1 and CH 2 |  |
| VOLTS/DIV | 10 mV |
| CH 1 Input COUPLING | DC |
| CH 2 Input COUPLING | GND |

b. Connect calibration generator STD AMPL output to the CH 1 input via a $50-\Omega$ precision BNC coaxial cable.
c. Set calibration generator AMPLITUDE to 50 mV .
d. CHECK-X-Axis amplitude is between 4.85 and 5.15 horizontal divisions.
e. Disconnect calibration generator.
11. $X-Y$ Phase Difference
a. Set:

Horizontal MODE
A
Vertical MODE
CH 1 (CH 2 off)
CH 1 Input COUPLING
DC
b. Connect function generator (FG 502) output to the CH 1 input via a $50-\Omega$ BNC coaxial cable and a $50-\Omega \mathrm{BNC}$ termination.
c. Set function generator output for six divisions of signal display amplitude at 100 kHz .
d. Set:

$$
\begin{array}{ll}
\text { Horizontal MODE } & \text { X-Y } \\
\text { CH } 1 \text { Input COUPLING } & \text { GND }
\end{array}
$$

e. Position dot to graticule center.
f. Set CH 1 Input COUPLING to DC.
g. CHECK-ellipse opening at the center is 0.3 division or less, measured horizontally.
h. Disconnect function generator.
12. X-Axis Bandwidth
a. Connect the leveled sine-wave generator (SG 503) output to the CH 1 input via a $50-\Omega$ precision BNC coaxial cable and a $50-\Omega$ BNC termination.
b. Set:

CH 1 VOLTS/DIV 10 mV Vertical MODE CH 2 (CH 1 off).
c. Set leveled sine-wave generator for six divisions of $50-\mathrm{kHz}$ signal.
d. Set leveled sine-wave output to 3 MHz .
e. CHECK-X-Axis display is 4.2 horizontal divisions or more.
f. Disconnect the test equipment from the 22461 Y or 2246 Mod A.

# TIME AND CURSORS MEASUREMENTS 

## Equipment Required (See Table 4-1)

Time mark generator
$50 \Omega$ BNC coaxial cable

Calibration generator
$50 \Omega$ BNC termination

## 1. $K$ SEC $\rightarrow$ and $k 1 /$ SEC $\rightarrow 1$ Cursor Accuracy

a. Set:

READOUT (Intensity)
A INTEN
Vertical MODE
CH 1 VOLTS/DIV
CH 1 and CH 2
Input COUPLING
Horizontal MODE
A SEC/DIV
A/B SELECT
Trigger MODE
Trigger CPLG
Trigger SOURCE
Trigger SLOPE
Trigger HOLDOFF
CH 2 INVERT
SCOPE BW
FOCUS

For a viewable readout
For a viewable trace
CH 1
(all others off)
0.5 V

DC
A
1 ms
A Trigger
AUTO LEVEL DC VERT
$\Gamma$ (positive)
Min
Off
Off
For best
defined display
b. Connect time-mark generator (TG 501) output via a $50-\Omega$ BNC coaxial cable and a $50-\Omega$ BNC termination to the CH 1 input.
c. Set time-mark generator for 1 ms time markers.
d. Position first time marker horizontally to the first vertical graticule line (left-most edge of the graticule).
e. Press TIME button to display the TIME menu.
f. Press $k$ SEC $\rightarrow 1$ menu button to turn on time cursors.
g. Position the reference cursor to the second time marker and the delta cursor to the tenth time marker.
h. CHECK-that the readout is 7.940 ms to 8.060 ms .
i. Press the TIME button to display the TIME menu.
j. Set $k 1 /$ SEC $\rightarrow 1$ on.
k. CHECK-that the readout is 124 Hz 126 Hz .
2. Delta Time Accuracy
a. Set A TIME/DIV to $1 \mu \mathrm{~s}$.
b. Set the time-mark generator for $1-\mu \mathrm{s}$ time marks.
c. Position the trace to the upper portion of the crt display and align the time markers horizontally with the graticule.
d. Set:

| Horizontal MODE | ALT |
| :--- | :--- |
| B TIME/DIV | 50 ns |
| B TRIGGER MODE | RUNS AFTER |
| $\kappa$ SEC $\rightarrow 1$ | On |

e. Center the first intensified zone over the time marker corresponding to the second graticule line, and the second intensified zone over the time marker corresponding to the tenth graticule line.
f. Position the $B$ sweep display to the lower portion of the crt using the TRACE SEP control, and use the $\rightarrow 1$ control to align the delayed sweep so that the selected time markers overlap each other exactly.
g. CHECK-that the readout is between $7.910 \mu \mathrm{~s}$ and $8.090 \mu \mathrm{~s}$.

## 3. $k$ PHASE $\rightarrow$ Cursor Accuracy

a. Set generator for 1 ms time marks.
b. Set:

```
Horizontal MODE A
A TIME/DIV 1 ms
```

c. Set TIME menu on.
d. Press $k$ PHASE $\rightarrow 1$ menu selection to display the $k$ PHASE $\rightarrow 1$ and $k$ SET $360^{\circ} \rightarrow 1$ menu choices.
e. Set $\leftarrow \operatorname{SET} 360^{\circ} \rightarrow 1$ on.
f. Position the first time marker to first graticule line. Then position the Reference cursor to the leading edge of the second time marker and the delta cursor to the leading edge of the tenth time-marker.
g. SET $k$ PHASE $\rightarrow$ on.
h. Position delta cursor to the leading edge of the sixth time marker.
i. CHECK-that the readout is between 177.9 and 182.1 degrees.
j. Disconnect time-mark generator.
4. $k$ VOLTS $\rightarrow$ Cursor Accuracy
a. Set:

| CH 1 VOLTSIDIV | 0.1 V |
| :--- | :--- |
| SEC/DIV | 0.5 ms |
| CURSORS Menu | On |
| $\leftarrow$ VOLTS $\rightarrow 1$ CURSORS | On |

b. Connect calibration generator (PG 506) STD AMPL output to the CH 1 input via a $50-\Omega$ precision BNC coaxial cable.
c. Set calibration generator to STD AMPL and AMPLITUDE to 0.5 V .
d. Position bottom of the signal to the second horizontal graticule line from the bottom.
e. Position the reference cursor to the bottom of the signal and the delta cursor to the top of the signal (both cursors move with the $k-$ OR DELAY control).
f. CHECK-that the readout is between 0.495 V and 0.505 V .
5. It VOLTS $\rightarrow 1$ Cursor Accuracy
a. Select MEASUREMENTS CURSORS menu, then select $i t$ VOLTS $\rightarrow$ CURSORS.
b. Position $\rightarrow$ control to align the delta cursor with the top of the waveform.
c. CHECK-that the readout is between 0.495 V to 0.505 V , and none of the cursors move when the $K O R$ DELAY control is rotated.

## 6. Tracking Cursors Position Accuracy

a. Press CLEAR DISPLAY (press twice).
b. Set:

| CH 1 VOLTS/DIV | 0.1 V |
| :--- | :--- |
| MEASUREMENTS CURSORS |  |
| Menu | On |
| AUTO TRACKING MENU | On |
| TRACK TRIG LVL | On |
| TRACK $A$ | On |
| MENU | Off |
| Trigger MODE | AUTO (not |
|  | AUTO LEVEL) |

c. Connect calibration generator Std Ampl output via a $50-\Omega$ BNC cable to the CH 1 input.
d. Set calibration generator for Std Ampl output of 0.5 V .
e. Adjust Trigger LEVEL control to align trigger level cursor with the bottom of the signal.
f. CHECK-the readout is $0.000 \mathrm{~V} \pm 0.005 \mathrm{~V}$, and the GND cursor is aligned with the bottom of the signal.
g. Set trigger level cursor to align with the top of the signal.
h. CHECK-the readout is between 0.475 V and 0.525 V .
i. Press CLEAR DISPLAY.
j. Disconnect test equipment if ending here.

## CH1/CH2 VOLTMETER MEASUREMENTS

## Equipment Required (See Table 4-1)

Calibration generator
Leveled sine-wave generator
Function generator
$50 \Omega$ BNC coaxial cable
$50 \Omega$ BNC termination

## 1. DC Volts Accuracy

a. Set:

READOUT (Intensity)
A INTEN
Vertical MODE
CH 1 VOLTS/DIV
CH 2 INVERT
SCOPE BW
CH 1 Input COUPLING
Horizontal MODE
A SEC/DIV
A/B SELECT
Trigger MODE
Trigger CPLG
Trigger SOURCE
Trigger SLOPE
Trigger HOLDOFF
CH1/CH2 VOLTMETER
FOCUS
Horizontal POSITION

For a viewable readout For a viewable trace
CH 1 50 mV Off
Off
GND A
1 ms
A Trigger AUTO LEVEL DC VERT $\Gamma$ (positive) Min DC
For best defined display 12 o'clock
b. CHECK-ground readout is $0.0 \mathrm{mV} \pm 1.2 \mathrm{mV}$.
c. Set calibration generator (PG 506) internal Square Wave/DC switch to DC.

## NOTE

The PG 506 must be removed from the TM power supply to make the change to dc output from the generator. Turn the power off before removing or inserting any plug-in from the TM power supply
d. Connect the calibration generator Std Ampi output to the CH 1 input via a $50-\Omega$ precision BNC coaxial cable.
e. Set calibration generator for Std Ampl output of 50 mV dc.
f. Set:
CH 1 VOLTS/DIV 10 mV CH 1 Input COUPLING DC
g. CHECK-the readout is between 49.0 mV and 51.0 mV .
h. Set $\mathrm{CH} 1 \mathrm{VOLTS} / \mathrm{DIV}$ to 0.1 V .
i. Set calibration generator for Std Ampl output 0.5 V .
j. CHECK-the readout is between 0.495 V and 0.505 V .
k. Set CH 1 VOLTS/DIV to 1 V .
I. Set calibration generator for Std Ampl output of 5 V.
m. CHECK-the readout is between 4.95 V and 5.05 V .
n. Disconnect Std Ampl signal from the CH 1 input.
2. DC Volts Normal Mode Rejection Ratio
a. Set SEC/DIV to 5 ms .
b. Connect function generator (FG 502) output to the CH 1 input via a $50-\Omega$ BNC coaxial cable and BNC coupling capacitor.
c. Set function generator for a six-division sinewave display amplitude at 50 Hz (with CH 1 VOLTS/DIV at 1 V ).
d. Set $\mathrm{CH} 1 \mathrm{VOLTS} / \mathrm{DIV}$ to 0.2 V .
e. CHECK-that the readout is less than $\pm 0.018 \mathrm{~V}$.
f. Disconnect the function generator signal from the 2246 1Y or $2246 \operatorname{Mod} A$.
3. +Peak, -Peak, Peak-to-Peak Volts Accuracy
a. Set:

| Vertical MODE | $\mathrm{CH} 2(\mathrm{CH} 1$ off) |
| :--- | :--- |
| CH 2 VOLTS/DIV | 10 mV |
| CH 2 Input COUPLING | DC |
| $\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER | +PEAK |

b. Set the calibration generator (PG 506) internal Square Wave/DC Switch for a square-wave output signal.

## NOTE

It is necessary to remove the PG 506 from the TM power supply module to set the internal Square Wave/DC switch to square-wave output. Turn the power off before removing or inserting any plug-in from the TM power supply
c. Connect calibration generator Std Ampl output to the CH 2 input via a $50-\Omega$ precision BNC coaxial cable.
d. Set calibration generator for Std Ampl output of 50 mV dc.
e. CHECK-that the readout is between 47.0 mV and 53.0 mV .
f. Set SCOPE BW on.
g. CHECK-the readout is between 47.7 mV and 52.3 mV .
h. Set:
CH 2 INVERT
On $\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER -PEAK.
i. CHECK-the readout is between -47.7 mV and -52.3 mV .
j. Set SCOPE BW Off.
k. CHECK-the readout is between -47.0 mV and -53.0 mV .

1. Set:
$\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER CH 2 INVERT

## PK-PK

 Offm . CHECK-the readout is between 46.5 mV and 53.5 mV .
n. Disconnect calibration generator.
4. $25 \mathrm{MHz}+$ Peak, - Peak, and Peak-to-Peak Volts Accuracy
a. Connect leveled sine-wave generator (SG 503) output to the CH 2 input via a $50-\Omega \mathrm{BNC}$ coaxial cable and a $50-\Omega$ BNC termination.
b. Set CH 2 VOLTS/DIV to 20 mV .
c. Set leveled sine-wave generator output for a readout of $100.0 \mathrm{mV} \pm 0.5 \mathrm{mV}$ at 50 kHz .
d. Set leveled sine-wave generator output for 25 MHz .
e. CHECK-the readout is between 95.0 mV and 105.0 mV .
f. Set $\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER to -PEAK.
g. CHECK-the readout is between -46.0 mV and -54.0 mV .
h. Set $\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER to +PEAK.
i. CHECK-the readout is between 46.0 mV and 54.0 mV .
5. 100 MHz +Peak, -Peak, and Peak-to-Peak Volts Accuracy
a. Set leveled sine wave generator (SG 503) output frequency to 100 MHz .
b. CHECK-the readout is between 34.4 mV and 54.0 mV .
c. Set $\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER to -PEAK.
d. CHECK-the readout is between -34.4 mV and -54.0 mV .
e. Set $\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER to PK-PK.
f. CHECK-the readout is between 69.7 mV and 107.0 mV .
g. Disconnect the leveled sine-wave signal from the 22461 Y or 2246 Mod A.

## 6. Gated Volts Accuracy

a. Set:

| A SEC/DIV | 0.5 ms |
| :--- | :--- |
| CH 2 VOLTS/DIV | 10 mV |

b. Set $\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER to GATED +PEAK.
c. Connect calibration generator (PG 506) Std Ampl output to the CH 2 input via a $50-\Omega$ precision BNC coaxial cable. Set the generator to Std Ampl output, 50 mV .
d. Set delta TIME POSITION to minimum intensified zone width.
e. CHECK-that the width of the dot is less than 0.2 division.
f. Set the intensified dot to a positive peak of the displayed waveform.
g. CHECK-the readout is between 47.0 mV and 53.0 mV .
h. Disconnect the test signal from the 22461 Y or $2246 \operatorname{Mod} A$.

## EXTERNAL Z-AXIS, PROBE ADJUST AND FRONT-PANEL SETUP FUNCTIONS

## Equipment Required (See Table 4-1)

Calibration generator
Two $50 \Omega$ BNC coaxial cables
$50-\Omega$ Precision BNC coaxial cable

BNC T-connector
Test oscilloscope with a 10X probe

1. Check External Z-Axis Input
a. Set:

| READOUT (Intensity) | For a viewable <br> readout <br> For a viewable <br> trace |
| :--- | :--- |
| A INTEN | CH 1 |
|  | $1 \vee$ |
| Vertical MODE | Off |
| CH 1 VOLTS/DIV | Off |
| CH 2 INVERT | DC |
| SCOPE BW | A |
| CH 1 Input COUPLING | 0.5 ms |
| Horizontal MODE | A Trigger |
| A SEC/DIV | AUTO LEVEL |
| A/B SELECT | DC |
| Trigger MODE | VERT |
| Trigger CPLG | $-\quad$ (positive) |
| Trigger SOURCE | All off (press |
| Trigger SLOPE | CLEAR DISPLAY |
| MEASMN'TS | 3 times) |
|  | Min |
| Trigger HOLDOFF | For best |
| FOCUS | defined display |
|  | 12 o'clock |

b. Connect calibration generator (PG 506) Std Ampl output to the CH 1 and the EXT Z-AXIS inputs via a $50-\Omega$ precision BNC coaxial cable, a BNC T-connector, and a $50-\Omega$ BNC coaxial cable. Set generator to Std Ampl output, 5 V .
c. Set A INTEN to maximum intensity.
d. CHECK-waveform display intensity starts decreasing at 1.8 V or less and the waveform is completely blanked out at 3.8 V .
e. Set $A \operatorname{INTEN}$ to midrange.
f. Disconnect the test equipment from the 22461 Y or $2246 \operatorname{Mod} A$.
2. PROBE ADJUST Output
a. Set:

| CH 1 Vertical MODE | 10 mV |
| :--- | :--- |
| SEC/DIV | 0.2 ms |
| SCOPE BW | $O n$ |

b. Connect a 10 X probe to the CH 1 input connector and connect the probe tip to the 22461 Y or 2246 Mod A PROBE ADJUST output. (When using Tektronix coded probes the readout changes to .1V.)
c. CHECK-For a 5-division vertical display of PROBE ADJUST square-wave signal (squarewave period is typically 1 ms , within $25 \%$ ).

## 3. Run MAKE FACTORY SETTINGS Routine

a. Press the top and bottom Menu-Select buttons to display the SERVICE MENU.
b. Press the down-arrow menu button three times and press SELECT to display the INTERNAL SETTINGS MENU.
c. Press the down-arrow menu button once and press RUN to run the MAKE FACTORY SETTINGS routine.
d. When the routine is finished, press the CLEAR DISPLAY button to return to the normal oscilloscope mode.

THIS COMPLETES THE PERFORMANCE CHECK PROCEDURE.

# ADJUSTMENT PROCEDURE 

## INTRODUCTION

## IMPORTANT-PLEASE READ BEFORE USING THIS PROCEDURE


#### Abstract

PURPOSE

This Adjustment Procedure returns the instrument to conformance with the Performance Requirements as listed in the Specification Tables in Section 1. Adjustments should be done only after the checks in the Performance Check Procedure (Section 4) have indicated a need for a readjustment of the instrument.


## TEST EQUIPMENT REQUIRED

The test equipment listed in Table 4-1 (section 4) is all the equipment required to complete the Adjustment Procedure in this section and the Performance Check Procedure in Section 4. Test equipment specifications described in Table 4-1 are the minimum necessary to provide accurate results; you must use Equipment that meets or exceeds these specifications. Detailed operating instructions for test equipment are not given in this procedure; if more operating information is required, refer to the appropriate test equipment instruction manual.

When equipment other than that recommended is used, control settings of the test setup may need to be altered. If the exact item of equipment given as an example in Table 4-1 is not available, first check the Use column to verify use of this item. Then use the Minimum Specification column to determine whether other available test equipment might work.

## LIMITS AND TOLERANCES

The limits and tolerances stated in this procedure are instrument specifications only if they are listed in the Performance Requirements column of the Table 1-1, Electrical Characteristics. Tolerances given are applicable only to the instrument under adjustment and do not include test equipment error. Adjustments must be made at an ambient temperature between $+20^{\circ} \mathrm{C}$
and $+30^{\circ} \mathrm{C}$, and the instrument must have had a warm-up period of at least 20 minutes.

## PARTIAL PROCEDURES

This procedure is divided in subsections to permit adjustment of individual sections of the instrument (except the Power Supply) whenever a complete readjustment is not required. For example, if only the Vertical section fails to meet the Performance Requirements (or has had repairs made or components replaced), it can be readjusted with little or no effect on other sections of the instrument. However, if the Power Supply section has undergone repairs or adjustments that change the absolute value of any of the supply voltages, a complete readjustment of the instrument is required.

At the beginning of each subsection is a list of the initial front-panel control settings required to prepare the instrument for Step 1 in that subsection. Each succeeding step within a subsection should then be done completely and in the sequence presented to ensure that control settings will be correct for steps that follow.

## INTERNAL ADJUSTMENTS AND ADJUSTMENT INTERACTION

Do not preset any internal controls, since that may make it necessary to recheck or readjust a major portion of the instrument when only a partial check or adjustment might otherwise have been required. To avoid unnecessary recheck and readjustment, change an internal control setting only when a Performance Characteristic cannot be met with the original setting. When independently changing the setting of any internal control, always check Table $5-1$ for possible interacting adjustments that might be required.

Specific interactions are called out within certain adjustment steps to indicate that the adjustments must be repeated until no further improvement is noted.

## PREPARATION FOR ADJUSTMENT

It is necessary to remove the cabinet to do the Adjustment Procedure. See the cabinet removal instructions in the Maintenance section of this manual.

All test equipment items required to do the complete Adjustment Procedure are described in Table $4-1$ at the beginning of Section 4, Performance Check Procedure. The specific items of equipment needed to do each subsection in this procedure are listed at the beginning of that subsection.

Connect the test equipment and the 2246 1Y or $2246 \mathrm{Mod} A$ to an appropriate ac-power source and allow 20 minutes warmup before making any adjustments.

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## POWER SUPPLY, DISPLAY, AND Z-AXIS

Equipment Required (See Table 4-1):

Digital Multimeter (DMM)
Leveled Sine-Wave Generator
Test Oscilloscope w/10X Probe
$50 \Omega$ Coaxial Cable
$50 \Omega$ Termination

## See the ADJUSTMENT LOCATIONS section

 at the back of this manual for locations of test points and adjustments.
## INITIAL CONTROL SETTINGS

## 1. Set:

| Vertical MODE | CH 1 |
| :--- | :--- |
| CH 1 COUPLING | DC |
| VOLTS/DIV | 0.1 V |
| Vertical POSITION |  |
| Controls | 12 o' clock $^{\text {Horizontal MODE }}$ |
| A/B SELECT | A |
| SEC/DIV | 0.1 ms |
| Trigger LEVEL | $120^{\prime}$ clock |
| HOLDOFF | min (CCW) |
| SLOPE | - |
| Trigger MODE | AUTO LEVEL |
| Trigger SOURCE | CH 1 |
| Trigger COUPLING | DC |
| MEASUREMENTS | OFF |
| MENU Displays | OFF |
| A INTEN | $10 o^{\prime}$ clock |
| READOUT | $120^{\prime}$ clock |
| FOCUS | for well defined display |
| SCALE ILLUM | fully CCW |

## PROCEDURE

## 1. Power Supply DC Levels (R2252)

a. Connect a Digital Multimeter (DMM) negative lead to chassis ground. Connect positive lead to
first test point listed in Table 5-2 (all test points on J1204, Main board).
b. CHECK-Voltage reading is within the range given in Table 5-2.
c. Move DMM positive lead to each of the other supply voltages in Table 5-2 and check that voltage ranges are within limits.

## NOTE

If all supply voltages are within the limits given in Table 5-2, it is not necessary to adjust the power supply. If voltages are not within limits, you will have to adjust the +7.5 V supply, recheck the other voltages, and continue with a complete readjustment of the instrument.
d. Connect a Digital Multimeter (DMM) negative lead to chassis ground and positive lead to $+7.5 \vee$ test point (J2104-8).
e. ADJUST-+7.5 $\vee$ ADJ (R2252) for $+7.5 \vee$ and check that all supply voltages in Table 5-2 are within limits. The $+7.5 \vee$ Adjustment is accessible through the right side frame.
f. Disconnect Digital Multimeter.

Table 5-2
Power Supply Voltage Limits

| Nominal <br> Supply <br> Voltage | Test Point <br> $(+$ lead $)$ | Limits <br> $\left(0^{\circ} \mathrm{C}\right.$ to $\left.58^{\circ} \mathrm{C}\right)$ <br> Min <br> Max |
| :---: | :---: | :---: |
| +7.5 V | $\mathrm{~J} 1204-8$ | +7.42 to +7.57 |
| +130 V | $\mathrm{~J} 1204-11$ | +127.17 to +135.03 |
| +59 V | $\mathrm{~J} 1204-10$ | +55.74 to +59.18 |
| +15 V | $\mathrm{~J} 1204-7$ | +14.65 to +15.55 |
| +5.0 V | $\mathrm{~J} 1204-1,2$ | +5.05 to +5.25 |
| -5.0 V | $\mathrm{~J} 1204-5$ | -5.14 to -5.35 |
| -7.5 V | $\mathrm{~J} 1204-9$ | -7.48 to -7.78 |
| -15 V | $\mathrm{~J} 1204-6$ | -15.57 to -16.53 |

2. Grid Bias (R2719)
a. SET:

| HORIZ MODE | X-Y |
| :--- | :--- |
| CH 1 VOLTS/DIV | $5 V$ |
| CH 1 COUPLING | GND |
| BW LIMIT | On |
| A INTEN | Fully CCW (off) |
| B INTEN | Fully CCW (off) |
| READOUT (Intensity) | Fully CCW (off) |
| SCALE ILLUM | Fully CCW (off) |

b. ADJUST - GRID BIAS (R2719) for a visible dot.
c. Position dot just off center screen with vertical or horizontal POSITION controls.
d. Set FOCUS control for a well defined dot.
e. ADJUST-GRID BIAS (R2719) so that dot is no longer visible.

## 3. Astigmatism (R2788)

a. Set:

| HORIZ MODE | A |
| :--- | :--- |
| A INTEN | $10 o^{\prime}$ clock |
| READOUT (Intensity) | $12 o^{\prime}$ clock |
| SEC/DIV | $5 \mu \mathrm{~s}$ |
| VAR SEC/DIV | Detent (fully CW) |
| CH 1 VOLTS/DIV | 10 mV |
| CH 1 COUPLING | DC |

b. Connect Leveled Sine-Wave Generator output to the CH 1 input connector via a $50 \Omega$ BNC coaxial ca-
ble and a $50 \Omega$ BNC termination. Set for a 5-division display at 50 kHz .
c. ADJUST-ASTIG (R2788) together with front-panel FOCUS control for best overall resolution of the sine-wave display.

DISCONNECT: Leveled Sine-Wave Generator.

## 4. Trace Rotation (Front Panel)

a. Set CH 1 COUPLING to GND.
b. Position trace to center horizontal graticule line and beginning of trace to first vertical graticule line.
c. ADJUST-TRACE ROTATION (front panel) to align trace parallel with center horizontal graticule line.
5. Geometry (R2784)
a. Set:

| CH 1 VOLTS/DIV | 0.1 V |
| :--- | :--- |
| CH 1 COUPLING | DC |
| SEC/DIV | $50 \mu \mathrm{~s}$ |
| READOUT (intensity) | Fully ccw (off) |

b. Connect Time Mark Generator to CH 1 via $50 \Omega$ cable and $50 \Omega$ termination. Display $10 \mu$ s time marks.
c. Position base trace below bottom graticule line.
d. ADJUST-GEOM (R2784) for minimum bowing of time marks across the full graticule area. Vertical bowing of time mark across screen should be no more than 0.1 division.
e. Disconnect Time Mark Generator.
6. Z-Axis Response (C2704) on instruments with serial numbers of below B110100 for the 2246 MOD A or below B710100 for the 2246 1Y
a. Set:

| READOUT (Intensity) | 12 o'clock |
| :--- | :--- |
| SEC/DIV | $5 \mu \mathrm{~s}$ |
| READOUT (Intensity) | Fully CCW (off) |

b. Connect test oscilloscope with 10X probe to R 2718 (either side).
c. ADJUST-Z AXIS RESP (C2704) for flattest response possible of the signal displayed by the test scope.
d. Disconnect test oscilloscope.
7. Readout Jitter (R724)
a. Set:

READOUT (intensity) SEC/DIV
CH 1 COUPLING
A INTEN
b. Connect calibration generator (PG 506) STD AMPL output to CH 1 input via $50 \Omega$ Precision coaxial cable. Set generator for STD AMPL and 1 volt.
c. ADJUST-Readout Jitter (R724) for the least amount of jitter or sway in the readout.
d. Disconnect calibration generator.

## VERTICAL

Equipment Required (See Table 4-1):
Calibration Generator (PG506)
Leveled Sine-Wave Generator $50 \Omega$ Coaxial Cable
$50 \Omega$ Termination
Precision Normalizer (20 pF)

See the ADJUSTMENT LOCATIONS section
at the back of this manual for locations of test points and adjustments.

## INITIAL CONTROL SETTINGS

1. Set:

Vertical MODE
INPUT COUPLING
VOLTSIDIV
Vertical POSITION
Controls
Horizontal MODE
A/B SELECT
SEC/DIV
Trigger LEVEL
HOLDOFF
SLOPE
Trigger MODE
Trigger SOURCE
Trigger COUPLING
MEASUREMENTS
MENU Displays
A INTEN
READOUT
FOCUS
SCALE ILLUM

> CH 1 and CH 2 DC 0.1 V 12 o'clock $^{\prime}$ A A 0.1 ms 12 o'clock $^{\text {min (CCW) }}$ $\square$ AUTO LEVEL VERT DC OFF OFF 10 o'clock 12 o'clock for well defined display fully CCW

## PROCEDURE

1. Vertical Output Gain (R703) and Readout Vertical Centering (R260)
a. Run ADJUST Vertical OUTPUT routine.

Select SERVICE MENU. Simultaneously press the top and bottom menu-item select buttons. Select

INTERNAL SETTINGS MENU. Press down-arrow button to underline selection, then press SELECT button. Run ADJUST Vertical OUTPUT routine. Press down-arrow button three times to underline selection, then press RUN button.

## NOTE

For this adjustment, the $22461 y$ or 2246 Mod A must be placed in the "normal" operating position to avoid incorrect alignment due to effects of the earth's magnetic field.
b. ADJUST-VO GAIN (R703) and RO VERT CENTERING (R260) alternately until dashed lines produced by the diagnostics are aligned with dotted lines on the graticule.
c. Press END button to end the ADJUST Vertical OUTPUT routine. Then press QUIT button to quit the Service Menu and return to the normal oscilloscope display.
2. CH 1 Step Balance (R12)
a. Set:

| Vertical MODE | CH 1 (CH 2 off $)$ |
| :--- | :--- |
| CH 1 COUPLING | GND |
| BW LIMIT | On |

b. Position trace to center of screen.
c. ADJUST-R12 (CH 1 STEP BALANCE) so the trace does not move vertically while switching CH 1 VOLTS/DIV switch from 10 mV to 50 mV . .

## 3. CH 2 Step Balance (R22)

a. Set:

```
Vertical MODE CH 2 COUPLING
```

b. Position trace to center of graticule.
c. ADJUST-R22 (CH 2 STEP BALANCE) so that trace does not move vertically while switching CH 2 VOLTS/DIV switch from 10 mV to 50 mV .
4. CH 3 Step Balance (R141)
a. Set Vertical MODE to $\mathrm{CH} 3(\mathrm{CH} 2$ off).
b. Position trace to center of graticule.
c. ADJUST-R141 (CH 3 STEP BALANCE) so that trace does not move vertically while switching CH 3 VOLTS/DIV switch from 0.1 V to 0.5 V .

## 5. CH 4 Step Balance (R161)

a. Set Vertical MODE to $\mathrm{CH} 4(\mathrm{CH} 3$ off).
b. Position trace to center of graticule.
c. ADJUST-R161 (CH 4 STEP BALANCE) so that trace does not move vertically while switching CH 4 VOLTS/DIV switch from 0.1 V to 0.5 V .
6. CH 1 MF/LF Gain (R13) and Compensation (C1)
a. Set:

| Vertical MODE | $\mathrm{CH} 1(\mathrm{CH} 4$ off $)$ |
| :--- | :--- |
| CH 1 VOLTS/DIV | 50 mV |
| CH 1 COUPLING | DC |
| SEC/DIV | $50 \mu \mathrm{~s}$ |

b. Connect calibration generator to CH 1 input via $50 \Omega$ BNC coaxial cable and $50 \Omega$ BNC termination. Set generator for HIGH AMPL. Set Period to 10 kHz and adjust Pulse Amplitude for a 5-division display.
c. ADJUST-R13 (CH $1 \mathrm{MF} / \mathrm{LF}$ GAIN) and $\mathrm{C} 1(\mathrm{CH} 1$ MF/LF COMP) for the flattest response (within 0.25 division).

## 7. CH 1 Input Capacitance (C114)

a. Set:

| CH 1 VOLTS/DIV | 10 mV |
| :--- | :--- |
| SEC/DIV | 0.2 ms |

10 mV SEC/DIV 0.2 ms
b. Add precision normalizer between termination and CH 1 input connector. Set calibration generator $\mathrm{Pe}-$ riod to 1 kHz and adjust Pulse Amplitude for 5-division display.
c. ADJUST-C114 (CH 1 INPUT CAPACITANCE) for best flat top.
d. Remove precision normalizer from the input cable.

## 8. CH 1 Input Compensation X10 (C11)

a. Set:
CH 1 VOLTS/DIV
0.1 V
SEC/DIV $50 \mu \mathrm{~s}$
b. Set calibration generator Period to 10 kHz and adjust Pulse Amplitude for a 5-division display.
c. ADJUST-C11 (CH 1 INPUT COMP X10) for flattest response (within 0.25 division).

## 9. CH 1 Input Compensation $\times 100$ (C10)

a. Set CH 1 VOLTS/DIV to 1 V .
b. Set calibration generator amplitude for a 5-division display.
C. ADJUST-C10 (CH 1 INPUT COMP X100) for flattest response (within 0.25 division).

## 10. CH 1 Gain (R211)

a. Set calibration generator to STD AMPL and Amplitude to 50 mV . Remove $50 \Omega$ termination from input cable.
b. Set:
CH 1 VOLTS/DIV 10 mV SEC/DIV 0.2 ms
c. ADJUST-CH 1 GAIN (R211) for exactly a 5-division display amplitude.
11. $\mathrm{CH} 2 \mathrm{MF} / \mathrm{LF}$ Gain (R23) and Compensation (C2)
a. Set:

| Vertical MODE | $\mathrm{CH} 2(\mathrm{CH} 1$ off $)$ |
| :--- | :--- |
| CH 2 VOLTS/DIV | 50 mV |
| CH 2 COUPLING | DC |
| SEC/DIV | $50 \mu \mathrm{~s}$ |

b. Move calibration generator signal to CH 2 input. Add $50 \Omega$ termination.
c. Set calibration generator for HIGH AMPL. Set Period to 10 kHz and adjust Pulse Amplitude for 5-division display.
d. ADJUST-R23 (CH 2 MF/LF GAIN) and C2 (CH 2 MF/LF COMP) for flattest response (within 0.25 division).
12. CH 2 Input Capacitance (C124)
a. Set:
CH 2 VOLTS/DIV 10 mV SEC/DIV 0.2 ms
b. Add precision normalizer between termination and CH 2 input connector. Set calibration generator Period to 1 kHz and adjust Pulse Amplitude for 5 -division display.
c. ADJUST-C124 (CH 1 INPUT CAPACITANCE) for best flat top.
d. Remove precision normalizer from the input cable.
13. CH 2 Input Compensation X 10 (C21)
a. Set:
CH 2 VOLTS/DIV
0.1 V
SEC/DIV
$50 \mu \mathrm{~s}$
b. Set calibration generator Period to 10 kHz and adjust Pulse amplitude for a 5 -division display.
c. ADJUST-C21 (CH 2 INPUT COMP X10) for flattest response (within 0.25 division).
14. CH 2 Input Compensation $\times 100$ (C20)
a. Set CH 2 VOLTS/DIV to 1 V .
b. ADJUST- $\mathrm{C} 20(\mathrm{CH} 2$ INPUT COMP $\times 100)$ for flattest response (within 0.25 division).
15. CH 2 Gain (R221)
a. Set calibration generator to STD AMPL and Amplitude to 50 mV . Remove $50 \Omega$ termination from the input cable.
b. Set:

$$
\begin{array}{ll}
\text { CH } 2 \text { VOLTS/DIV } & 10 \mathrm{mV} \\
\text { SEC/DIV } & 0.2 \mathrm{~ms}
\end{array}
$$

c. ADJUST-CH2 GAIN (R221) for exactly a 5-division display amplitude.
16. CH $3 \mathrm{MF} / \mathrm{LF}$ Compensation (C134)
a. Set:

| Vertical MODE | CH 3 |
| :--- | :--- |
| CH 3 VOLTS/DIV | 0.1 V |
| SEC/DIV | $50 \mu \mathrm{~s}$ |

b. Move calibration generator signal to CH 3 input. Add $50 \Omega$ termination.
C. Set calibration generator for HIGH AMPL. Set Period to 10 kHz and adjust Pulse Amplitude for a 5-division display.
d. ADJUST-C134 (CH $3 \mathrm{MF} / \mathrm{LF}$ COMP) for flattest response.

## 17. CH 3 Gain (R231)

a. Set:

| CH 3 VOLTS/DIV | 0.5 V |
| :--- | :--- |
| SEC/DIV | 2 ms |

b. Set calibration generator for STD AMPL. Set amplitude to 2 V . Remove $50 \Omega$ termination.
c. ADJUST - CH 3 GAIN (R231) for exactly 4-division display amplitude.

## 18. CH 4 MF/LF Compensation (C154)

a. SET:

| Vertical MODE | CH 4 (CH 3 off) |
| :--- | :--- |
| CH 4 VOLTS/DIV | 0.1 V |
| SEC/DIV | $50 \mu \mathrm{~s}$ |

b. Move calibration generator signal to CH 4 input. Add $50 \Omega$ termination.
c. Set calibration generator for HIGH AMPL. Set Period to 10 kHz and adjust Pulse Amplitude for a 5 -division display.
d. ADJUST-C154 (CH 4 MF/LF COMP) for flattest response.
19. CH 4 Gain (R241)
a. Set:

| CH 4 VOLTS/DIV | 0.5 V |
| :--- | :--- |
| SEC/DIV | 0.2 ms |

b. Set calibration generator for STD AMPL. Set amplitude to 2 V . Remove $50 \Omega$ termination.
c. ADJUST-CH 4 GAIN (R241) for exactly a 4-division display amplitude.
d. Disconnect calibration generator from CH 4.
e. CHECK-MEASUREMENT BANDWIDTH and SELF CHARACTERIZATION procedure and adjust as necessary.
20. Delay-line HF Compensation (R272, R273, R275, C274, C273)
a. Set:
Vertical MODE
CH 1 VOLTSIDIV
CH 1 (CH 4 off)
SEC/DIV
50 mv
BW LIMIT
20 ns
Off
b. Connect calibration generator positive-going FAST RISE OUTPUT to the CH 1 input via a $50 \Omega$ precision coaxial cable and a $50 \Omega$ termination.
c. Set calibration generator for FAST RISE at 1 MHz and adjust Pulse Amplitude for a 5 -division display.
d. Position the top of display to the center horizontal graticule line.
e. ADJUST-DLY LINE COMP adjustments (R272, R273, R275) for flattest response and (C274, C273) for sharpest front corner with minimum overshoot. Figure 5-1 shows areas of the waveform affected by each adjustment.

## NOTE

It may be necessary to adjust for a small amount of overshoot on the front corner of the pulse in order to meet the bandwidth check (step 23). Total overshoot and aberrations must be less than 0.25 division, peak-to-peak.


Figure 5-1. Areas of waveform affected by HF compensation.
21. CH 3 HF Compensation (C138)
a. Set:

Vertical MODE
CH 3 (CH 1 off) CH 3 VOLTS/DIV 0.5 V
b. Move calibration generator signal to CH 3 . Set Pulse Amplitude to maximum to obtain about 2.5 division display.
c. ADJUST-C138 (CH 3 HF COMP) for flattest response.
22. CH 4 HF Compensation (C158)
a. Set:
Vertical MODE
CH 4 (CH 3 off)
CH 4 VOLTS/DIV
0.5 V
b. Move calibration generator signal to CH 4 .
c. ADJUST-C158 (CH 4 HF COMP) for flattest response.
d. Disconnect calibration generator.
23. CH 1 and CH 2 Bandwidth Check
a. Set:

Vertical MODE CH 1 VOLTS/DIV SECIDIV

CH 1 (CH 4 off)
5 mV
0.1 ms
b. Connect leveled sine-wave generator output to the CH 1 input via a $50 \Omega$ precision coaxial cable and a $50 \Omega \mathrm{BNC}$ termination.
c. Set leveled sine-wave generator output for a six-division signal amplitude at 50 kHz .
d. CHECK-the displayed signal amplitude is at least 4.2 divisions as the generator frequency is increased to 100 MHz .

## NOTE

If the displayed signal amplitude drops below 4.2 divisions at any frequency below 100 MHz , readjust Delay-Line HF Compensation (step 20) with increased front-corner overshoot.
e. Repeat the frequency setup and CHECK procedure for VOLTS/DIV settings of 50 mV and 0.1 V .
f. Move the test signal to the CH 2 input.
g. Set:
Vertical MODE
CH 2 VOLTS/DIV
CH 2 (CH 1 off)
10 mV
h. Repeat the complete bandwidth check procedure for Channel 2.
24. CH 3 and CH 4 Bandwidth Check
a. Set:

Vertical MODE
CH 3 ( CH 2 off)
CH 3 VOLTS/DIV
0.1 V
b. Move the leveled sine-wave generator signal to the CH 3 input.
c. Set the generator output for a six-division signal display at 50 kHz .
d. CHECK-the displayed signal amplitude is at least 4.2 divisions as the generator frequency is increased to 100 MHz .
e. Move the test signal to the CH 4 input.
f. Set:
Vertical MODE
CH 4 (CH 3 off) CH 4 VOLTS/DIV 0.1 V
g. Repeat the procedure for CH 4.
h. Disconnect leveled sine-wave generator.

## HORIZONTAL

## Equipment Required (See Table 4-1):

Time-Mark Generator
Calibration Generator
$50 \Omega$ Coaxial Cable
$50 \Omega$ Termination

See the ADJUSTMENT LOCATIONS section
at the back of this manual for locations of test points and adjustments.

## INITIAL CONTROL SETTINGS

1. Set:

| Vertical MODE | CH 1 and CH 2 |
| :--- | :--- |
| INPUT COUPLING | DC |
| VOLTS/DIV | 0.1 V |
| Vertical POSITION |  |
| Controls | 12 o'clock |
| Horizontal MODE | A |
| A/B SELECT | A |
| SEC/DIV | 0.1 ms |
| Trigger LEVEL | 12 o'clock |
| HOLDOFF | min (CCW) |
| SLOPE | AUTO LEVEL |
| Trigger MODE | VERT |
| Trigger SOURCE | DC |
| Trigger COUPLING | OFF |
| MEASUREMENTS | OFF |
| MENU Displays | 10 o'clock |
| A INTEN | 12 o'clock |
| READOUT | for well defined |
| FOCUS | display |
|  | fully CCW |
| SCALE ILLUM |  |

## PROCEDURE

1. Horizontal X1 Gain (Timing) (R826)
a. Set:

## Vertical MODE

CH 1 (CH 2 off) CH 1 VOLTS/DIV 0.5 V
b. Connect time mark generator to CH 1 input via $50 \Omega$ BNC coaxial cable and $50 \Omega$ BNC termination. Set generator for 0.1 ms time marks.
c. Position peaks of the time markers to the center horizontal graticule line.
d. ADJUST-HORIZONTAL 1X GAIN (R826) for one time mark per division over the center 8 divisions.
2. Horizontal X10 Gain (Timing) (R825)
a. Set X10 MAG to ON.
b. Set time mark generator for $10 \mu \mathrm{~s}$ time marks.
c. Position display about center screen.
d. ADJUST-HORIZONTAL X10 GAIN (R825) for one time mark per division over the center 8 divisions.

## 3. Readout Horizontal Gain (R823) and MAG Registration (R809)

a. Set the time mark generator for 0.5 ms time marks.
b. Position the middle time marker to the center vertical graticule line using the Horizontal POSITION control.
c. Set X10 MAG to Off.
d. ADJUST - MAG REG (R809) to position the middle time marker to the center vertical graticule line.
e. Set X10 MAG to On.
f. CHECK - for no horizontal shift in the time marker.
g. Repeat parts $b$ through $f$ until no shift is noted.
h. Set:

| X10 MAG | Off |
| :--- | :--- |
| SEC/DIV | $50 \mu \mathrm{~s}$ |
| A INTEN | CCW (off) |

i. Select Time CURSORS. Press the TIME MEASUREMENT button and select $k-$ SEC $\rightarrow 1$ from the menu.
j. Rotate thek OR DELAY control to align the cursor to the second graticule line.
k. Rotate the $\rightarrow 1$ control so that the reading is $400.0 \mu \mathrm{~s}$.

1. ADJUST - Both the $k$ OR DELAY control and R823 so that the cursors are aligned exactly on the second and tenth graticule line.
m. Set A INTEN to 10 o'clock.

## 4. A $0.5 \mu \mathrm{~s}$ Timing (C314)

a. Set:

| X10 MAG | Off |
| :--- | :--- |
| SEC/DIV | $0.5 \mu \mathrm{~s}$ |
| B Trigger MODE | RUNS AFTER |
| B INTEN | 10 o'clock |

b. Set time mark generator for $0.5 \mu \mathrm{~s}$ time marks.
C. ADJUST-A $0.5 \mu$ S TIMING (C314) for one time mark per division over the center 8 divisions.
5. B $0.5 \mu \mathrm{~s}$ Timing (C329)
a. Set:

| Horizontal MODE | B |
| :--- | :--- |
| SEC/DIV (B) | $0.5 \mu \mathrm{~s}$ |

b. Set time mark generator for $0.5 \mu \mathrm{~s}$ time marks.
c. Rotate the $k$ OR DELAY control counterclockwise until the readout displays "?0.000 $\mu \mathrm{s}$ ".
d. ADJUST-B $0.5 \mu$ S TIMING (C329) for one time mark per division over the center 8 divisions.
6. 2-5 ns Timing (C807, C814)
a. Set:

| Horizontal MODE | A |
| :--- | :--- |
| SEC/DIV | 20 ns |
| X10 MAG | On |
| CH 1 COUPLING | 50 mV |
| CH 1 COU | AC |

b. Set time mark generator for 5 ns time marks.
c. ADJUST-2 NS TIMING (C807, C814) for 1 cycle per each 2.5 divisions over the center 8 divisions. See Figure 5-2. Use the vertical transition of the sine wave instead of the peaks for better accuracy.
d. INTERACTION - between C807 and C814. Readjust as necessary to make the timing at $2.5,5$, and 7.5 divisions within $\pm 0.2$ division (1 minor division).
e. Disconnect time mark generator.


Figure 5-2. 2-5 ns Timing.

## 7. $X$ Gain (R827)

a. Set:

| X10 MAG | Off |
| :--- | :--- |
| Horizontal MODE | $X-Y$ |
| Vertical MODE | CH 2 |
| CH 1 VOLTS/DIV | 10 mV |

b. Connect calibration generator STD AMPL output to CH 1 input via $50 \Omega$ coaxial cable. Set generator for STD AMPL. Set Amplitude to 50 mV .
c. ADJUST - X GAIN (R827) for 5 divisions of horizontal signal.
d. Disconnect calibration generator.

## MEASUREMENT BANDWIDTH AND SELF CHARACTERIZATION

Equipment Required (See Table 4-1):
Digital Multimeter (DMM)
$50 \Omega$ Coaxial Cable
Leveled Sine- Wave Generator

## See the ADJUSTMENT LOCATIONS section

 at the back of this manual for locations of test points and adjustments.
## INITIAL CONTROL SETTINGS

1. Set:

| Vertical MODE | CH 1 and CH 2 |
| :--- | :--- |
| INPUT COUPLING | DC |
| VOLTS/DIV | 0.1 V |
| Vertical POSITION |  |
| Controls | 12 o' $^{\prime}$ lock |
| SCOPE BW | Off |
| Horizontal MODE | A |
| A/B SELECT | A |
| SEC/DIV | 0.1 ms |
| Trigger LEVEL | $120^{\prime}$ clock |
| HOLDOFF | min (CCW) |
| SLOPE | - |
| Trigger MODE | AUTO LEVEL |
| Trigger SOURCE | VERT |
| Trigger COUPLING | DC |
| MEASUREMENTS | OFF |
| MENU Displays | OFF |
| A INTEN | $10 o^{\prime}$ clock |
| READOUT | $12 o^{\prime}$ clock |
| FOCUS | for well defined |
|  | display |
| SCALE ILLUM | fully CCW |

## PROCEDURE

1. Volts Cal (R920)
a. Connect digital multimeter (DMM) LO lead to chassis ground and the high lead to R921 pin 6.
b. ADJUST-VOLTS CAL (R920) so the DMM reads 0.250 V .
2. B Trigger Bandwidth (R455)
a. Set:

Vertical MODE CH 2 VOLTS/DIV SEC/DIV

CH 2 (CH 1 off) 20 mV
$50 \mu \mathrm{~s}$
b. Preset potentiometer R455 to midrange.
c. Run the SELF CAL routine. Press $\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER button and select SELF CAL.
d. Connect leveled sine-wave generator output to the CH 2 input connector via a $50 \Omega \mathrm{BNC}$ precision coaxial cable and a $50 \Omega$ BNC termination. Set generator for a 5 -division display at 50 kHz .
e. Set $\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER for PK-PK voltage measurement. Set output of Leveled Sine-Wave Generator for a peak-to-peak readout display of $100 \mathrm{mV} \pm 0.5 \mathrm{mV}$. Adjust generator to 115 MHz .
f. ADJUST-B TRIG BANDWIDTH (R455) for a peak-to-peak readout of $75 \mathrm{mV} \pm 0.5 \mathrm{mV}$.

## 3. Self Characterization

a. Run the SELF CAL MEASUREMENTS routine. Press both top and bottom menu-item select buttons. Select INTERNAL SETTINGS MENU, then

SELF CAL MEASUREMENTS. Press RUN to start the routine. When the routine is done, continue with part b. or press QUIT to return to normal oscilloscope mode.
b. Run MAKE FACTORY SETTINGS routine. Press the down-arrow button to select the INTERNAL

SETTINGS MENU. Press SELECT, then press the down-arrow button to select MAKE FACTORY SETTINGS. Press RUN to start the routine. When done, press QUIT to return to normal oscilloscope mode.

## MAINTENANCE

This section of the manual contains information for conducting preventive maintenance, troubleshooting, and corrective maintenance on the 2246 IY and 2246 Mod A Oscilloscope. General information regarding the care and handling of the semiconductor devices that can be damaged by static discharges is provided in "Static-Sensitive

Components." Routine cleaning instructions and visual inspection checking for defects are covered in "Preventive Maintenance." Internal testing capabilities and diagnostic test routines are included in the "Troubleshooting" part of this section. Circuit board removal procedures are included in the "Corrective Maintenance" part of this section.

## STATIC-SENSITIVE COMPONENTS

The following precautions are applicable when performing any maintenance involving internal access to the instrument.

## $\{$ CAUTION\}

Static discharge can damage any semiconductor component in this instrument.

This instrument contains electrical components that are susceptible to damage from static discharge. Table 6-1 lists the relative susceptibility of various classes of semiconductors. Static voltages of 1 kV to 30 kV are common in unprotected environments.

When performing maintenance, observe the following precautions to avoid component damage:

1. Minimize handling of static-sensitive components.
2. Transport and store static-sensitive components or assemblies in their original containers or on a metal rail. Label any package that contains static-sensitive components or assemblies.
3. Discharge the static voltage from your body by wearing a grounded antistatic wrist strap while handling these components. Servicing staticsensitive components or assemblies should be performed only at a static-free work station by qualified service personnel.

Table 6-1
Relative Susceptibility to Static-Discharge Damage

| Semiconductor Classes | Relative <br> Susceptibility <br> Levels a |
| :--- | :---: |
| MOS or CMOS microcircuits or <br> discretes, or linear microcircuits <br> with MOS inputs <br> (Most Sensitive) | 1 |
| ECL | 2 |
| Schottky signal diodes | 3 |
| Schottky TTL | 4 |
| High-frequency bipolar <br> transistors | 5 |
| JFET | 6 |
| Linear microcircuits | 7 |
| Low-power Schottky TTL | 8 |
| TTL <br> (Least Sensitive) | 9 |

${ }^{\mathrm{a}}$ Voltage equivalent for levels (voltage discharged from a $100-\mathrm{pF}$ capacitor through a resistance of $100 \Omega$ ):

```
1=100 to 500 V
2=200 to 500 V
3 =250\vee V
4=500V
4 = 400 to 600 V
```

$6=600$ to 800 V
$7=400$ to $1000 \vee$ (est)
$8=900 V$
$9=1200 \mathrm{~V}$
4. Keep anything capable of generating or holding a static charge off the work station surface.
5. Keep the component leads shorted together whenever possible.
6. Pick up components by their bodies, never by their leads.
7. Do not slide the components over any surface.
8. Avoid handling components in areas that have a floor or work-surface covering capable of generating a static charge.
9. Use a soldering iron that is connected to earth ground.
10. Use only approved antistatic, vacuum-type desoldering tools for component removal.

## PREVENTIVE MAINTENANCE

## INTRODUCTION

Preventive maintenance consists of cleaning, visual inspection, and checking instrument performance. Preventive maintenance on a regular basis may prevent instrument malfunction and improve instrument reliability. The required frequency of maintenance depends on the severity of the environment in which the instrument is used. An appropriate time to do preventive maintenance is just before instrument adjustment.

## INSPECTION AND CLEANING

Visually inspect and clean the $22461 Y$ or 2246 Mod A as often as operating conditions require. Accumulation of dirt in the instrument can cause overheating and component breakdown. Dirt on components acts as an insulating blanket, preventing efficient heat dissipation. It also provides an electrical conduction path that could result in instrument failure, especially under high-humidity conditions.

Do not use chemical cleaning agents which might damage the plastics used in this instrument. Use a nonresidue-type cleaner, preferably isopropyl alcohol or a solution of 1\% mild detergent with 99\% water. Before using any other type of cleaner, consult your Tektronix Service Center or representative.

## Exterior

INSPECTION. Inspect the external portions of the instrument for damage, wear, and missing parts; use Table 6-2 as a guide. Instruments that appear to have been dropped or otherwise abused should be checked thoroughly to verify correct operation and performance. Deficiencies that could cause personal injury or could lead to further instrument damage should be repaired immediately.


#### Abstract

Do not allow moisture to get inside the instrument during external cleaning. Use only enough liquid to dampen the cloth or applicator.


CLEANING. Loose dust on the outside of the instrument can be removed with a soft cloth or small softbristle brush. The brush is particularly useful on and around the controls and connectors. Remove remaining dirt with a soft cloth dampened in a mild detergent-and-water solution. Do not use abrasive cleaners.

Clean the light filters and the crt face with a soft lintfree cloth dampened with either isopropyl alcohol or a mild detergent-and-water solution.

## Interior

To access internal portions of the instrument for inspection and cleaning, refer to the Removal and Replacement Instructions in the Corrective Maintenance part of this section.

Table 6-2
External Inspection Checklist

| Item | Inspect For | Repair Action |
| :--- | :--- | :--- |
| Cabinet, Front Panel, and <br> Cover | Cracks, scratches, deformations, <br> damaged hardware or gaskets. | Touch up paint scratches and <br> replace defective components. |
| Front-panel controls | Missing, damaged, or loose knobs, <br> buttons, and controls. | Repair or replace missing or <br> defective items. |
| Connectors | Broken shells, cracked insulation, <br> and deformed contacts. Dirt in <br> connectors. | Replace defective parts. Clean or <br> wash out dirt. |
| Carrying Handle | Correct operation. | Replace defective parts. |
| Accessories | Missing items or parts of items, <br> bent pins, broken or frayed cables, <br> and damaged connectors. | Replace damaged or missing items, <br> frayed cables, and defective <br> parts. |

Table 6-3
Internal Inspection Checklist

| Item | Inspect For | Repair Action |
| :--- | :--- | :--- |
| Circuit Boards | Loose, broken, or corroded solder <br> connections. Burned circuit boards. <br> Burned, broken, or cracked <br> circuit-run plating. | Clean solder corrosion with an <br> eraser and flush with isopropyl <br> alcohol. Resolder defective con- <br> nections. Determine cause of <br> burned items and repair. Repair <br> defective circuit runs. |
| Resistors | Burned, cracked, broken, or <br> blistered. | Replace defective resistors. Check <br> for cause of burned component <br> and repair as necessary. |
| Solder Connections | Cold solder or rosin joints. | Resolder joint and clean with <br> isopropyl alcohol. |
| Capacitors | Damaged or leaking cases. <br> Corroded solder on leads or <br> terminals. | Replace defective capacitors. Clean <br> solder connections and flush with <br> isopropyl alcohol. |
| Semiconductor | Loosely inserted in sockets. <br> Distorted pins. | Firmly seat loose semiconductors. <br> Remove devices having distorted <br> pins. Carefully straighten pins (as <br> required to fit the socket), using |
| long-nose pliers, and reinsert |  |  |
| firmly. Ensure that straightening |  |  |
| action does not crack the pins, |  |  |
| causing them to break. |  |  |

INSPECTION. Inspect the internal portions of the 2246 1Y or 2246 Mod A for damage and wear, using Table 6-3 as a guide. Repair deficiencies immediately. The corrective procedure for most visible defects is obvious; however, particular care must be taken if heat-damaged components are found. Since overheating usually indicates other trouble in the instrument, it is important that the cause of overheating be corrected to prevent further damage.

If any electrical component is replaced, conduct a Performance Check for the affected circuit and for other closely related circuits (see Section 4 for the Performance Check). If repair or replacement work is done on any of the power supplies, conduct a complete Performance Check and, if so indicated, an instrument readjustment (see Section 5 for Adjustment Procedure).

To prevent damage from electrical arcing, ensure that circuit boards and components are dry before applying power to the instrument.

CLEANING. To clean the interior, blow off dust with dry, low-pressure air (approximately 9 psi). Remove any remaining dust with a soft brush or a cloth dampened with a solution of mild detergent and water. A cotton-tipped applicator is useful for cleaning in narrow spaces and on circuit boards. If these methods do not remove all the dust or dirt, the instrument may be spray washed using a solution of $1 \%$ mild detergent and $99 \%$ water as follows:

1. Remove covers and shields to reach parts to be cleaned (see Removal and Replacement Instructions).
2. Spray wash dirty parts with the detergent-andwater solution; then use clean water to thoroughly rinse them.
3. Dry all parts with low-pressure air.
4. Dry all components and assemblies in an oven or drying compartment using low-temperature $\left(125^{\circ} \mathrm{F}\right.$ to $150^{\circ} \mathrm{F}$ ) circulating air.

SWITCH CONTACTS. Switch contacts are permanently treated when assembled. Neither cleaning nor other preventive maintenance is necessary, unless the switch board is replaced or the switch assembly has remained disassembled for a long period of time.

## LUBRICATION

A regular periodic lubrication program for the instrument is not necessary. Most of the potentiometers used in this instrument are permanently sealed and generally do not require periodic lubrication. The backs of the front-panel knob guides have been lubricated when assembled and will require lubrication again only when the front panel assembly is replaced. Rotary switches are installed with proper lubrication when assembled and will require lubrication only when the rotor is replaced.

## SEMICONDUCTOR CHECKS

Periodic checks of the transistors and other semiconductors in the oscilloscope are not recommended. The best check of semiconductor performance is actual operation in the instrument.

## PERIODIC READJUSTMENT

To ensure accurate measurements, check the performance of this instrument every 2000 hours of operation, or if used infrequently, once each year. If you replace any components, it may be necessary to readjust the affected circuits. The silver-oxide battery used in the 2246 IY MUST be replaced annually. Refer to Corrective Maintenance in this section for more information on battery replacement.

Complete Performance Check instructions are given in Section 4 of this manual; adjustment instructions are given in Section 5. The Performance Check Procedure can be helpful in localizing certain troubles in the instrument. In some cases, minor problems may be revealed or corrected by readjustment. If only a partial adjustment is performed, see Table 5-1 (the interaction chart) for possible adjustment interaction with other circuits.

## TROUBLESHOOTING

## INTRODUCTION

Preventive maintenance performed on a regular basis should reveal most potential problems before an instrument malfunctions. However, should troubleshooting be required, the following information is provided to aid in locating a fault. In addition, the material presented in the Theory of Operation and Diagrams sections of this manual may be helpful while troubleshooting.

## TROUBLESHOOTING AIDS

## Diagnostic Firmware

This instrument contains built-in diagnostic routines that can aid in localizing failures. An automatic power-up self test checks the system RAM and ROM and readout interface circuitry. If a failure is detected, this information is presented in either of two ways: a flashing code display on the Trigger LEDs or, if the instrument is capable of presenting a readout, error messages in the crt display. In addition to the power-on testing, various diagnostic routines can be run from the service mode using the SERVICE MENU. (See Internal Testing Capabilities in this subsection for the details.)

## Schematic Diagrams

Complete schematic diagrams are located on tabbed foldout pages in the Diagrams section. Portions of circuitry mounted on each circuit board are enclosed by heavy black lines. The assembly number and name(s) of the circuit(s) are shown near the top or the bottom edge of each diagram.

Functional blocks on schematic diagrams are outlined with a wide gray line. Components within the outlined area perform the function designated by the block label. The Theory of Operation uses these functional block names when describing circuit operation as an aid in cross-referencing between the circuit description and the schematic diagrams.

Component numbers and electrical values of components in this instrument are shown on the schematic diagrams. Refer to the first page of the Diagrams section for the reference designators and symbols used to identify components. Important voltages and waveform reference numbers (enclosed in hexagonal-shaped boxes) are also
shown on each diagram. Waveform illustrations are located adjacent to their respective schematic diagram.

## Circuit Board Illustrations

Circuit board illustrations showing the physical location of each component are provided for use in conjunction with each schematic diagram. Each board illustration is found in the Diagrams section on the back of a foldout page, preceding the first related schematic diagram(s).

The locations of waveform test points are marked on the circuit board illustrations with hexagonal outlined numbers corresponding to the waveform numbers on both the schematic diagram and the waveform illustrations.

## Circuit Board Locations

The location of a circuit board within the instrument is shown on each foldout page along with the circuit board illustration.

## Circuit Board Interconnections

A circuit board interconnection diagram (schematic Diagram 16) is provided in the Diagrams section to aid in tracing a signal path or power source between boards. All wire, plug, and jack numbers are shown along with their associated wire or pin numbers and signal names.

## Power Distribution

Two Power Distribution diagrams (schematic Diagrams 14 and 15) are provided to aid in troubleshooting power supply problems. These diagrams show the components that the various voltages are applied to and the jumper connections and decoupling components used to apply the power to those circuits. Excessive loading on a power supply by a circuit fault may be isolated by disconnecting the appropriate jumpers.

## Grid Coordinate System

Each schematic diagram and circuit board illustration has a grid border along its left and top edges. A table located next to each schematic diagram lists the grid coordinates of each component shown in that diagram. To aid in physically locating
components on the circuit board, the table also lists the grid coordinates of each component in the circuit board illustration.

Near each circuit board illustration is an alphanumeric listing of all components mounted on that board. The second column in each listing identifies the schematic diagram in which each component can be found. These component-locator tables are especially useful when more than one schematic diagram is associated with a particular circuit board.

## Component Color Coding

Information regarding color codes and markings of resistors and capacitors is located in the colorcoding illustration (Figure 9-1) at the beginning of the Diagrams section.

RESISTOR COLOR CODE. Resistors used in this instrument are carbon-film, composition, or precision metal-film types. They are usually color coded with the EIA color code; however, some metal-film type resistors may have the value printed on the body. The color code is interpreted starting with the stripe nearest to one end of the resistor. Composition resistors have four stripes; these represent two significant digits, a multiplier, and a tolerance value. Metal-film resistors have five stripes representing three significant digits, a multiplier, and a tolerance value.

CAPACITOR MARKINGS. Capacitance values of common disc capacitors and small electrolytics are marked on the side of the capacitor body. White ceramic capacitors are color coded in picofarads, using a modified EIA code.

Dipped tantalum capacitors are color coded in microfarads. The color dot indicates both the positive lead and the voltage rating. Since these capacitors are easily destroyed by reversed or excessive voltage, be careful to observe the polarity and voltage rating when replacing them.

DIODE COLOR CODE. The cathode end of each glass-encased diode is indicated by either a stripe, a series of stripes or a dot. For most diodes marked with a series of stripes, the color combination of the stripes identifies three digits of the Tektronix Part Number, using the resistor color-code system. The cathode and anode ends of a metal-encased diode may be identified by the diode symbol marked on its body.

## Semiconductor Lead Configurations

Figure 9-2 in the Diagrams section shows the lead configurations for semiconductor devices used in the instrument. These lead configurations and case styles are typical of those used at completion of the instrument design. Vendor changes and performance improvement changes may result in changes of case styles or lead configurations. If the device in question does not appear to match the configuration shown in Figure 9-2, examine the associated circuitry or consult a manufacturer's data sheet.

## Multipin Connections

This instrument uses two types of cable connectors. The main type is an etched-circuit ribbon cable with pin connectors crimped directly to the end of the cable. The number one pin is indicated by a mark on the ribbon cable. The other type of connector is a plastic holder containing connectors crimped to the ends of individual wires. Orientation, where important, is indicated by a triangle (arrow).

## TROUBLESHOOTING EQUIPMENT

The equipment listed in Table 4-1 of this manual, or equivalent equipment, may be useful when troubleshooting this instrument.

## TROUBLESHOOTING TECHNIQUES

The following procedure is arranged in an order that enables checking simple trouble possibilities before requiring more extensive troubleshooting. The first two steps use diagnostic routines built into the operating system of the instrument.

The next four procedures are check steps that ensure proper control settings, connections, operation, and adjustment. If the trouble is not located by these checks, the remaining steps will aid in locating the defective component. When the defective component is located, replace it using the appropriate replacement procedure given under Corrective Maintenance in this section.

## $\{$ CAUTION\}

Before using any test equipment to make measurements on static-sensitive, currentsensitive, or voltage-sensitive components or assemblies, ensure that any voltage or current supplied by the test equipment does not exceed the limits of the component to be tested.

## 1. Power-up Tests

When the instrument power is applied, self tests are automatically run to verify proper operation of the system RAM and ROM and readout interface circuitry. If the power-up test fails, failure codes appear in the Trigger Mode LEDs to identify the general location of the fault. (See Power-Up Testing later in this section for failure-code information.)

## 2. Diagnostic Routines

Various diagnostic routines can be run from the service mode. The routines can be run at any time by displaying the SERVICE MENU and selecting the desired item from the menu using front panel pushbuttons.

Entry into the SERVICE MENU and its uses are explained in the Diagnostic Routines discussion later in this section.

## 3. Check Control Settings

Incorrect control settings can give a false indication of instrument malfunction. If there is any question about the correct function or operation of any control, refer to the Operating Information in the 2246 $1 Y$ and 2246 Mod A Operators Manual.

## 4. Check Associated Equipment

Before proceeding, ensure that any equipment used with the 22461 Y or $2246 \mathrm{Mod} A$ is operating correctly. Verify that input signals are properly connected and that the interconnecting cables are not defective. Check that the ac-power-source voltage to all equipment is correct.

## 5. Visual Check

## WARNING

To avoid electrical shock, disconnect the instrument from the ac power source before making a visual inspection of the internal circuitry.

Perform a visual inspection. This check may reveal broken connections or wires, damaged components, semiconductors not firmly mounted, damaged circuit boards, or other clues to the cause of an instrument malfunction.

## 6. Check Instrument Performance and Adjustment

Check the performance of either those circuits where trouble appears to exist or the entire instrument. An apparent trouble may be the result of misadjustment. The complete performance check is given in Section 4 of this manual and adjustment instructions are given in Section 5.

## 7. Isolate Trouble to a Circuit

To isolate problems, use any symptoms noticed when checking the instrument's operation to help localize the trouble to a particular circuit. For example, if the vertical deflection is incorrect on all channels, the problem is most likely from the delay line driver to the vertical output; if deflection is bad only on one channel, the problem is from the attenuator of that channel to the input of the delay line driver. The detailed block diagram shown in the foldout section may be used as an aid in determining signal flow and control line dependency for correct circuit operation. Refer to the troubleshooting hints given in Table 6-6 for diagnostic routine failures. Troubleshooting hints by diagram are given immediately following Table 6-6, and Table 6-9 may be used to aid in locating a problem in the measurement system.

## 8. Check Power Supplies

## WARNING

For safety reasons, an isolation transformer must be used between the ac power main and the instrument's ac power input whenever troubleshooting is done with the cabinet removed. This is especially important when working in the Preregulator and Inverter Power Supply sections of the instrument.

When trouble symptoms appear in more than one circuit, first check the power supplies; then check the affected circuits by taking voltage and waveform readings. Check first for the correct output voltage of each individual supply. These voltages are measured between J1204 (interface connector between power supply and main board) and ground (J1204 pin 4 or 8). See the associated circuit board illustration and Table 6-4.

Voltages levels may be measured either with a DMM or with an oscilloscope. Voltage ripple amplitudes must be measured using an oscilloscope. Use a 1 X probe with as short a ground lead as possible to minimize stray pickup.

## NOTE

Use 20 MHz bandwidth limiting on the test oscilloscope. A higher bandwidth may produce higher observed ripple levels.

If the power-supply voltages and ripple are within the listed ranges in Table 6-4, the supply can be assumed to be working correctly. If they are outside the range, the supply may be either misadjusted, operating incorrectly, or excessively loaded. The power supply adjustment procedure is given in the Power Supply, Display, and Z-Axis subsection of Section 5 (the Adjustment Procedure).

A defective component elsewhere in the instrument can create the appearance of a power-supply problem and may also affect the operation of other circuits. Use the power distribution diagrams (schematic Diagrams 14 and 15 in the foldouts) to aid in localizing a loading problem to a particular circuit.

## 9. Check Circuit Board Interconnections

After the trouble has been isolated to a particular circuit, again check for loose or broken connections, improperly seated semiconductors, and heat-damaged components.

## 10. Check Voltages and Waveforms

Often the defective component can be located by checking circuit voltages or waveforms. Typical voltages are listed on the schematic diagrams. Waveforms indicated on the schematic diagrams by hexagonal-outlined numbers are shown adjacent to the diagrams. Waveform test points are shown in the circuit board illustrations.

## NOTE

Voltages and waveforms indicated on the schematic diagrams are not absolute and may vary slightly between instruments. To establish operating conditions similar to those used to obtain these readings, see the Voltage and Waveform Setup Conditions preceding the waveform illustrations in the Diagrams section.

Note the recommended test equipment, front-panel control settings, voltage and waveform conditions, and cable-connection instructions. Any special control settings required to obtain a given waveform are noted under the waveform illustration. Volts/Div and Sec/Div settings of the test oscilloscope for a waveform are indicated in the waveform illustration.

Table 6-4
Power Supply Voltage and Ripple Limits

| Nominal Supply Voltage | ```Test Point (+ lead) Test Point (+ lead)``` | $\begin{gathered} \text { Limits }\left(-10^{\circ} \mathrm{C}\right. \\ \text { to } \left.55^{\circ} \mathrm{C}\right) \end{gathered}$ |  | P-P Ripple ${ }^{\text {a }}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min | Max | $60-150 \mathrm{~Hz}$ | $20-40 \mathrm{kHz}$ |
| +130 | J1204-11 | +127 | +135 | 70 mV | 70 mV |
| +58 | J1204-10 | +55 | +59 | 40 mV | 120 mV |
| +15 | J1204-7 | +14.6 | +15.6 | 8 mV | 4 mV |
| +7.5 | J1204-8 | +7.4 | +7.6 | 8 mV | 4 mV |
| $+5.0$ | J1204-1,2 | +5.0 | +5.3 | 30 mV | 20 mV |
| -5.0 | J1204-5 | -5.2 | -5.4 | 4 mV | 4 mV |
| -7.5 | J1204-9 | -7.5 | -7.8 | 4 mV | 4 mV |
| -15 unreg | J1204-6 | -15.5 | -16.6 | 10 mV | 100 mV |

${ }^{a}$ At rated load.

## 11. Check Individual Components

## WARNING

To avoid electric shock, always disconnect the instrument from the ac power source before removing or replacing components.

The following procedures describe methods of checking individual components. Two-lead components that are soldered in place are most accurately checked by first disconnecting one end from the circuit board. This isolates the measurement from the effects of the surrounding circuitry. See Figure 9-1 for component value identification and Figure $9-2$ for semiconductor lead configurations.

[^10]TRANSISTORS. A good check of a transistor is actual performance under operating conditions. A transistor can most effectively be checked by substituting a known-good component. However, be sure that circuit conditions are not such that a replacement transistor might also be damaged. If substitute transistors are not available, use a dynamic-type transistor checker for testing. Static-type transistor checkers are not recommended, since they do not check operation under simulated operating conditions.

When troubleshooting transistors in the circuit with a voltmeter, measure both the emitter-to-base and emitter-to-collector voltages to determine whether they are consistent with normal circuit voltages. Voltages across a transistor may vary with the type of device and its circuit function.

Some of these voltages are predictable. The emitter-to-base voltage for a conducting silicon transistor will normally range from 0.6 V to 0.8 V . The emitter-to-collector voltage for a saturated transistor is about 0.2 V . Because these values are small, the best way to check them is by connecting
a sensitive voltmeter across the junction rather than comparing two voltages taken with respect to ground. If the former method is used, both leads of the voltmeter must be isolated from ground.

If voltage values measured are less that those just given, either the device is shorted or no current is flowing in the external circuit. If values exceed the emitter-to-base values given, either the junction is reverse biased or the device is defective. Voltages exceeding those given for typical emitter-tocollector values could indicate either a nonsaturated device operating normally or a defective (opencircuited) transistor. If the device is conducting, voltage will be developed across the resistors in series with it; if open, no voltage will be developed across the resistors unless current is being supplied by a parallel path.


When checking emitter-to-base junctions, do not use an ohmmeter range that has a high internal current. High current may damage the transistor. Reverse biasing the emitter-to-base junction with a high current may degrade the current-transfer ratio (Beta) of the transistor.

A transistor emitter-to-base junction also can be checked for an open or shorted condition by measuring the resistance between terminals with an ohmmeter set to a range having a low internal source current, such as the $R \times 1 \mathrm{k} \Omega$ range. The junction resistance should be very high in one direction and much lower when the meter leads are reversed.

When troubleshooting a field-effect transistor (FET), the voltage across its elements can be checked in the same manner as previously described for other transistors. However, remember that in the normal depletion mode of operation, the gate-to-source junction is reverse biased; in the enhanced mode, the junction is forward biased.

INTEGRATED CIRCUITS. An integrated circuit (IC) can be checked with a voltmeter, test oscilloscope, or by direct substitution. A good understanding of circuit operation is essential when troubleshooting a circuit having IC components. Use care when checking voltages and waveforms around the IC so that adjacent leads are not shorted together. An IC test clip provides a convenient means of clipping a test probe to an IC.

When checking a diode, do not use an ohmmeter scale that has a high internal current. High current may damage a diode. Checks on diodes can be performed in much the same manner as those on transistor emitter-to-base junctions.

DIODES. A diode can be checked for either an open or a shorted condition by measuring the resistance between terminals with an ohmmeter set to a range having a low internal source current, such as the $R X$ $1 \mathrm{k} \Omega$ range. The diode resistance should be very high in one direction and much lower when the meter leads are reversed.

Silicon diodes should have 0.6 V to 0.8 V across their junctions when conducting; Schottky diodes about 0.2 V to 0.4 V . Higher readings indicate that they are either reverse biased or defective, depending on polarity.

RESISTORS. Check resistors with an ohmmeter. Refer to the Replaceable Electrical Parts list for the tolerances of resistors used in this instrument. A resistor normally does not require replacement unless its measured value varies widely from its specified value and tolerance.

INDUCTORS. Check for open inductors by checking continuity with an ohmmeter. Shorted or partially shorted inductors can usually be found by checking the waveform response when high-frequency signals are passed through the circuit.

CAPACITORS. A leaky or shorted capacitor can best be detected by checking resistance with an ohmmeter set to one of the highest ranges. Do not exceed the voltage rating of the capacitor. The resistance reading should be high after the capacitor is charged to the output voltage of the ohmmeter. An open capacitor can be detected with a capacitance meter or by checking whether the capacitor passes ac signals.

## 12. Repair and Adjust the Circuit

If any defective parts are located, follow the replacement procedures given under Corrective Maintenance in this section. After any electrical component has been replaced, the performance of that circuit and any other closely related circuit should be checked. Since the power supplies affect all circuits,
performance of the entire instrument should be checked if work has been done on the power supplies. Readjustment of the affected circuitry may be necessary. Refer to the Performance Check Procedure and the Adjustment Procedure, (sections 4 and 5) and to Table 5-1 (Adjustment Interactions).

## INTERNAL TESTING CAPABILITIES

The diagnostics built into the 2246 iY and 2246 Mod A permit the technician to test much of the digital circuitry and the digital-to-analog interface. The following text describes the testing capabilities of the Measurement Processor and the firmware controlled circuitry.

## Power-Up Testing

The systems shown in Figure 6-1 are tested at power-on. Failure codes appear in the Trigger MODE LEDs, with ON being shown as " $x$ " and OFF as " $O$ " in the figure. In the event of a display failure where error message cannot be displayed on the crt, the codes indicate a failure area to begin troubleshooting.


Figure 6-1. Power-on test failure codes.

Power-up tests performed are:

1. RAM diagnostics-failures indicated by flashing AUTO LEVEL lamp. Three diagnostics are run on all locations in the RAM:
a. Store and read 00.
b. Store and read FF.
c. Store and read pseudo-random pattern.
2. ROM diagnostics-failures indicated by flashing AUTO lamp. Tests all of ROM, except for ROM header. Runs proprietary version of CRCC test.
3. Readout interface diagnostics-failures indicated by flashing NORM lamp. Performs a marchingone test around the loop from D7-D0 to R7-R0 and back.

## SERVICE MODE

The service mode driver menu is accessed by pressing the top and the bottom menu-item buttons at the same time. The initial SERVICE MENU will be displayed as shown in Figure 6-2. Each service menu display has two parts; the part to the left is the service menu, and the part to the right is the modifier menu.


Figure 6-2. Initial SERVICE MENU.

Each service menu has a title and a number of selectable items in the menu. The title appears in the top line of the crt display, and the selectable items appear under the title, indented (see Figure 6-2). Menu choices that are names of submenus have a following slash (/), and when one is underlined, the word "SELECT" appears in the modifier menu list. When a choice with sub-menus is selected, the sub-menu choices are displayed on
the left side of the screen with the name of the selected sub-menu displayed in the top line.

A menu choice that has no following slash is an executable service routine. The routine may be run by underlining it and pressing the menu button next to the RUN label that appears in the modifier menu list. Executable servicing selections are: diagnostics that return either a pass message or a fail message along with service data; one-shot exercisers that carry out some service and immediately return to the menu; or regular exercisers that carry out a service while continuously displaying service data.

An underlined service-menu choice is available for selection. To select a menu item, use the buttons next to the up-arrow and down-arrow symbols to move the underline up or down in the SERVICE MENU. When the underline is below the sub-menu title, pressing the menu button next to up-arrow MENU label returns to the preceding menu containing that sub-menu (an up-menu operation).

Pressing the QUIT selection or CLEAR Display button at any time the choice is displayed will cause the scope to return to normal oscilloscope mode. If a service routine is operating that has an END menu selection displayed, pressing the button next to END exits the routine and returns to the selection menu (where QUIT is displayed). Routines that run once, return to the selection menu when finished.

The diagnostic tests in the SERVICE MENU may be run with a conditional setting that determines how many times the routine is done. The conditional MODE setting menu choice appears in the modifier menu when the DIAGNOSE choice in the SERVICE MENU is underlined (see Figure 6-3). One of the following mode types will be displayed:
ONCE, CONTINUOUS, UNTIL PASS, or UNTIL FAIL

Change the mode type displayed in the bottom line by pressing the MODE button. When ONCE is the mode, the diagnostic is run once, and the result is displayed. When CONTINUOUS is the mode, the diagnostics are run continuously. When UNTIL PASS is the mode, the diagnostics are run until they pass. When UNTIL FAIL is the mode, the diagnostics are run until they fail. In order to stop a diagnostic that is looping in the CONTINUOUS, UNTIL PASS, or UNTIL FAlL mode, press the HALT button. The diagnostic will stop and display the current status. When the status is displayed, press END to return to the SERVICE MENU choices.

## SERVICE MENU/

| PIAGNOS.E. | $\uparrow$ |
| :--- | :---: |
| CONFIGURE | $\downarrow$ |
| INTERNAL SETTINGS MENU/ | RUN |
| FRONT PANEL MENU/ |  |
| PROC BOARD MENU/ | QUIT |
| MAIN BOARD MENU/ | MODE: |
|  | ONCE |

Figure 6-3. SERVICE MENU with DIAGNOSE choice selected.

## Service Routines

Descriptions of the available service routines are given in Table 6-5. The complete SERVICE MENU has this structure:

```
SERVICE MENU/
    DIAGNOSE
    CONFIGURE
    SELF CAL MEASUREMENTS
    INTERNAL SETTINGS MENU/
    MAKE FACTORY SETTINGS
    LOAD STORE/RECALL SETUPS
    ADJUST VERTICAL OUTPUT
    EXERCISER MENU/
        FRONT PANEL MENU/
        EXERCISE POTS
        EXERCISE LEDS
        EXERCISE SWITCHES
        PROC BOARD MENU/
        A TO D MENU/
                EXERCISE DACS
                EXERCISE PORTS
        READOUT MENU/
                SHOW READOUT ROM HEADER
                EXERCISE RO INTERFACE
            EXERCISE TIME REF
            SHOW SYSTEM ROM HEADER
            SHOW AUTO RESTARTS
    MAIN BOARD MENU/
        SHIFT REGISTER MENU/
                EXERCISE SR 0
                EXERCISE SR 1
                EXERCISE SR 2
            EXERCISE VOLT REF
```

Table 6-5
SERVICE MENU Selections

| Menu Item | Action |
| :---: | :---: |
| DIAGNOSE | Runs all diagnostics in sequence, stopping at the first failed diagnostic. (See Table 6-6 for a diagnostic test failure troubleshooting guide.) <br> Diagnostics are: <br> RO (readout) INTERFACE <br> ROM RAM <br> SLIC CONTROL REG <br> SHIFT REGISTERS (in SR2, SRO, SR1, SR3 order) <br> DAC <br> Triggers |
| CONFIGURE | Configures the scope-mode operation of the instrument according to the users' wishes. Configuration is done by answering yes/no questions. The questions are: <br> KEEP MENU ON WHEN MEAS SELECTED? The menu remains displayed after a measurement function has been selected from one of the Measurements menus. |

Table 6-5 (cont)
SERVICE MENU Selections

| Menu Item | Action |
| :---: | :---: |
| CONFIGURE (cont) | RECALL ONLY (IN STORE/RECALL)? This feature is not available on the 22461 Y and 2246 Mod A. However, a selection must be made when stepping through the CONFIGURE menu. <br> KEEP MENU ON WHEN S/R SELECTED. This feature is not available on the 22461 Y and 2246 Mod A. However, a selection must be made when stepping through the CONFIGURE menu. <br> KEEP READOUT ON IN SGL SEQ? Selecting YES causes the readout to be on constantly when in SGL SEQ trigger mode. <br> Selecting NO causes the readout to flash on for a brief period after the signal display sequence has finished. |
| SELF CAL MEASUREMENTS | Self characterizes the gain and offset errors in the vertical system and time base so that they may be compensated for in the measurements. This should be run only after instrument is warmed up properly, although if desired, it can be used to compensate for an unusual operating temperature. |
| MAKE FACTORY SETTINGS | Resets the front panel settings to those shipped with the instrument. Used to produce a known setup condition. The following is a partial list of settings: |
| LOAD STORE/RECALL SETUPS | Not available on the 2246 1Y or 2246 Mod A. Pressing this selection has no effect on instrument operation. |
| ADJUST VERTICAL OUTPUT | Used to adjust the vertical output gain and centering (see Adjustment Procedure in Section 5). |
| EXERCISE POTS | Shows the name of the latest digitized potentiometer moved, along with its hexadecimal value (from FF to OO). Starts by showing the HORIZ POSITION and its value until another pot is adjusted. The FOCUS and SCALE ILLUM controls are not digitized and therefore are not checked with this exerciser. Pressing END exits the exerciser. |

Table 6-5 (cont)
SERVICE MENU Selections

| Menu Item | Action |
| :---: | :---: |
| EXERCISE LEDS | Uses the delay control to check for adjacent-row or adjacent-column shorts in the front panel board and for inoperative LEDs. The exerciser displays the circuit number and illuminates each LED as the control is rotated. Circuit numbers DS2016, DS2017, DS2018, DS2019, DS2024, DS2040, and DS2049 do not correspond to any LEDs on the front panel. Pressing END exits the excerciser. |
| EXERCISE SWITCHES | Shows the circuit number of the latest momentary-contact button pressed, or the name and position of the latest rotary switch turned. Pressing END exits the exerciser; pressing CLEAR MEAS'MT terminates the Service Program. |
| EXERCISE DACS | Attaches the DAC (U2302, Diagram 11) to a single sample-and-hold channel (through U2303), and outputs a sawtooth waveform to that channel. Select the channel by pressing STEP. This excerciser may be used to trace a sample-hold value through the system, with the DAC system operating in a non-multiplexed mode. Pressing END exits the exerciser. |
| EXERCISE PORTS | Continuously does analog-to-digital conversion on a single A-to-D port. Select the port by pressing STEP. The exerciser may be used to trace a single potentiometer wiper value or probe code value through the system by operating the A-to-D converter in a non-multiplexed mode. Pressing END exits the exerciser. |
| SHOW READOUT ROM HEADER | Shows the Readout ROM part number (U2408, Diagram 9) and its expected and actual checksum. |
| EXERCISE RO INTERFACE | Continuously echos a marching-bit value across the readout interface. This exerciser may be used to check the integrity of the Measurement-Processor-to-Readout-Processor communication system. Pressing END exits the exerciser. |
| SHOW SYSTEM ROM HEADER | Shows the system ROM part numbers (U2519, Diagram 8) and checksums of the installed firmware version. |
| EXERCISE TIME REF | Steps through the timing frequencies used to characterize the horizontal timing accuracy. Calibration periods are shown in the following table. |

Table 6-5 (cont)
SERVICE MENU Selections

| Menu Item | Action |
| :---: | :---: |
| SHOW AUTO RESTARTS | Shows the address being executed if a software error occurs that causes execution out of normal memory space. This is for factory use only and is of no use in field servicing of the instrument. If an AUTO RESTART is ever seen, record the address displayed and report it to a service center; the error address is cleared from memory when SHOW AUTO RESTARTS is exited. |
| EXERCISE SR 0 | Shifts alternate zeros and ones through Shift Register 0 (U171, U172, and U173, Diagram 1). This shift register sets Attenuator and Input Coupling relay positions and Vertical Preamplifier gain settings. |
| EXERCISE SR 1 | Shifts alternate zeros and ones through Shift Register 1 (U302 and U303, Diagram 5; U1103, Diagram 3). This shift register sets sweep speeds and auxiliary trigger settings (TV Trigger, Scope Bandwidth, X10 magnification, and $X-Y$ Mode). |
| EXERCISE SR 2 | Shift alternate zeros and ones through Shift Register 2 (U502, Diagram 4). |
| EXERCISE VOLT REF | Steps through all settings of the Voltage Reference Generator (U931, Diagram 7) that are used to calibrate the Volts Measurement system. For each setting, Channels 1 and 2 are placed into the gain configuration ( 2 mV through 50 mV ) that uses that setting. The voltage select lines (VOLTS CAL $2-0$ ) may be checked for activity, and the generated VOLTS CAL SIGNAL may be measured to check its values. |

## DIAGNOSE Tests

The complete DIAGNOSE routine may be called up by the service technician as needed to aid in
troubleshooting the instrument. Testing routines and troubleshooting information for use in the event of a failed test are given in Table 6-6.

Table 6-6
diagnose routines

| Error Label | Path, devices tested, and troubleshooting actions |
| :---: | :---: |
| INTERFACE ERROR | Measurement Processor to Readout Processor Communications. <br> This test rotates a 1 through the byte on the bus lines. The difference between WROTE and READ indicates which bit is stuck. |
|  | Devices to troubleshoot: <br> U2401, U2402, U2417C and D, and bus lines between Measurement Processor and Readout Processor. <br> Check U2501 pin 29 for enabling signal to U2402, and U2400 pin 22 for clock. |
| RAM ERROR | Writes and reads test bytes from the Readout RAM (U2406). <br> The difference between WROTE and READ data indicates a stuck bit. |
| (RO RAM) | PART NUM (Tektronix Part Number without dashes). EXPECTED CHECKSUM (hex number, 2 characters). ACTUAL CHECKSUM (hex number, 2 characters). <br> NOTE <br> Readout ROM is internal to the Readout Processor, U2400; a failure of this test may mean a bad Readout Processor. |

Table 6-6 (cont)
DIAGNOSE ROUTINES

| Error Label | Path, devices tested, and troubleshooting actions |
| :---: | :---: |
| REG SR 2 | Front Panel Potentiometer Multiplexer data path check.WROTE (hex data written, 1 character). <br> READ (hex data read, 1 character). <br> Device Tested: U502, Diagram 4.  |
|  | Troubleshooting checks: <br> Check pin 11 for correct clock. <br> Check pin 2 for data. <br> Check pin 12 for multiplexer output. |
| REG SR 0 | Attenuator and Preamplifier data path check. <br> Devices Tested: U171, U172, and U173 on Diagram 1. <br> NOTE <br> U171 and U172 have +15 V clocks and data; U173 has +5 V clocks and data. |
|  | Troubleshooting checks: <br> Check pin 3 of each device for correct clock. <br> Check pin 9 of each device for marching bit pattern. <br> Attenuator relay latches are driven and a clacking sound is heard. |
| REG SR 1 | Sweeps and Auxiliary Trigger data path check. <br> Devices Tested: <br> U302 and U303 on Diagram 3; U1103 on Diagram 3. Clock and data levels for U302 and U303 are +15 V ; they are +5 V for U 1103 . |
|  | Troubleshooting checks: <br> Check pin 3 of each device for correct clock. Check pin 9 of each device for marching bit pattern. |

Table 6-6 (cont)

## DIAGNOSE ROUTINES

| Error Label | Path, devices tested, and troubleshooting actions |
| :---: | :---: |
| REG SR 3 | Switch board data path check. <br> WROTE <br> (hex data written, 4 characters). <br> READ <br> (hex data read, 4 characters). <br> NOTE <br> There is no exerciser for SR 3, but it is included in "DIAGNOSE." <br> Devices Tested: U2001 and U2002, Diagram 10. |
|  | Troubleshooting Checks: <br> Check pin 10 for serial data in. Check pin 9 for serial data out. Check pin 2 for clock. |
| DAC ERROR 0 | The A-to-D system, Diagram 11, is not working correctly. Ground level was digitized out of the specified error limits. |
|  | Devices to troubleshoot: <br> U2515 and U2517, Diagram 8; U2306, U2302, U2300, U2313, and U2314, Diagram 11; U506, Diagram 7 |
| (Triggers) | The trigger diagnostic partially checks the Trigger SOURCE, Trigger CPLG, and Trigger SLOPE circuitry. |
|  | Error Messages: <br> time signal too small <br> AT A Trigger <br> A Trigger circuitry failed amplitude test. <br> TIME SIGNAL TOO SMALL <br> AT B Trigger <br> B Trigger circuitry failed amplitude test. <br> NO A Trigger FOR TIME <br> CAL SIGNAL <br> (2 digit code, see table) <br> Trigger never occurred. |

Table 6-6 (cont)
DIAGNOSE ROUTINES

| Error Label | Path, devices tested, and troubleshooting actions |  |
| :---: | :---: | :---: |
|  | Time Base Cal Signal | Code |
|  | $128 \mu \mathrm{~s}$ | 0 |
|  | 8.192 ms | 1 |
|  | $256 \mu \mathrm{~s}$ | 2 |
|  | 4.096 ms | 3 |
|  | 2.048 ms | 4 |
|  | $512 \mu \mathrm{~s}$ | 5 |
|  | 32.768 ms | 6 |
|  | 1.024 ms | 7 |
|  | $64 \mu \mathrm{~s}$ | 13 |
|  | $32 \mu \mathrm{~s}$ | 14 |
|  | Check U421, U431, U1106A, and | ciated |
|  | SLIC (Display Logic IC, U600) and outputs and level shifters, Diagram | (Trig |

## TROUBLESHOOTING HINTS BY DIAGRAM

## Vertical SELF CAL-Checks Cursor and Preamplifier Outputs

The circuitry listed below must be operational for Vertical SELF CAL to work. Troubleshoot these circuits if voltage measurements or tracking cursors are not correct.

1. DAC system (U2303, U2304, and U2305, Diagram 11).
2. Trigger Level Comparators (U431 and U421, Diagram 3).
3. VERT COMP feedback (U702, Diagram 2).
4. ECL-to-CMOS translators (Q604, Q605, Q606, Q607, Q602, and Q603) between U600 and U602 (Diagram 4).
5. Data to Measurement Processor (data bus and bus transceivers, Diagram 8).
6. VOLTS CAL signal (U931, Diagram 7).
7. Vertical Preamps (U210, U220, U230, and U240), Delay Line Drivers (Q250, Q251, Q252, and Q253), and Vertical Position Switching circuitry (U203, U801B, U201, U202, U280, Q284 and Q285, Diagram 2).

## HORIZONTAL SELF CAL-Checks Sweep Timing

1. Put the oscilloscope into Self Cal and check at U421A pin 4 (Diagram 3) for changing width calibration signals.
2. Run the "EXERCISE TIME REF" exerciser and check for correct TB CAL signal at U421A pin 4, Diagram 3 (see Table 6-5).

## Schematic Diagram 1-Vertical INPUTS

1. Run DIAGNOSE to check for shift register fallure.
2. Run the shift register exerciser for Shift Register 0 . Check for clock, data, and strobe signals. Check the shift register outputs.

## NOTE

The outputs of U171 and U172 are at 15 V ; the outputs from U173 are at 5 V .
3. Check the outputs of the relay driver transistor arrays (U174 and U175). When a transistor is blown in one of the arrays, the usual symptom is 8 V on its output.
4. Go to a known setup and check the outputs for correct levels (see Circuit Description in Section 3). The MAKE FACTORY SETTINGS selection under INTERNAL SETTINGS of the SERVICE MENU provides known control states.
5. Check relay contacts.
6. Follow the signal path and check for correct signal and gains. Put in a known signal for each attenuator setting and check at the Vertical Preamplifier inputs to determine if the signal path is ok. The front panel boards and the attenuator shield have to be removed to gain access to the solder side of the Main Board.
7. Check the channel input buffer amplifier (U112 or U122) output if the vertical deflection of either channel 1 or channel 2 is defective. If the buffer amplifier output is held at -6 V or a strange sawtooth signal is present, replace that buffer amplifier.
8. Check gains and offsets of the CH 3 or CH 4 input buffers (Q131 or Q151).

## Schematic Diagram 2-Vertical PREAMPLIFIERS, DELAY LINE DRIVERS and OUTPUT

Perform the following troubleshooting checks with no signal input.

1. Check both inputs of the delay line. If offset on either side, troubleshoot the offsetting side. Inputs to the bases of Q250 and Q251 should be at +7.5 V .
2. Differential voltage across the delay line should be $0 \mathrm{~V} \pm 0.5 \mathrm{~V}$.
3. Check signal gain through the Preamplifier ICs (U210, U220, U230, and U240). Gain is $10 \mathrm{mV} /$ division of input signal.
4. Check INVERT operation.
5. Check the operation of U260 if the inputs to delay line driver are not at 7.5 V . This operational amplifier is the bias stabilization circuit that compares the average dc level to +7.5 and moves the emitters (and therefore the bases) of Q250 and Q251 to return the inputs to 7.5 V .

## WARNING

Vertical output transistors Q701 and Q702 run extremely hot (in excess of $100^{\circ} \mathrm{C}$ ). Use care when probing in those areas to not touch the heat sinks or cases with bare fingers.

## WARNING

The vertical output amplifier runs hot. DO NOT touch it with bare fingers.


The metal tab on top of the vertical output amplifier IC (U701) is NOT ground. Do not connect a ground lead to it. Doing so may cause the IC to fail and usually causes R733 from pin 14 of U701 to the -5 V supply to open.
6. Avoid moving the package or flexing the leads of R708. If the resistor pack is moved excessively, its leads may break. The resistor pack will then have to be replaced.

## NOTE

The heat sinks on Q701 and Q702 may be removed for short periods of time to permit access for a test probe around the close-in circuitry. DO NOT leave them off for extended periods. Check that they are on all the way when replaced.
7. If the heat sinks on the output transistors shake loose, the plastic grommet inserted in the top of the heat sink prevents the sink from touching the metal cabinet. If the grommet is left out, then the metal cabinet may come in contact with the heat sink; and the transistor, the vertical output amplifier IC, and R733 will usually fail. If the heat sinks are removed during maintenance, they must fit tightly when replaced and the grommet must be checked.

## NOTE

The cases of Q701 and Q702 are the base leads of the transistors, not the collector as is usual for a TO5 case. Also, the tab marks the collector lead, not the emitter.
8. The vertical outputs to the crt may be momentarily shorted together to check for offsets in the crt. (This should center the vertical trace.)
9. The output at pins 6 and 7 of U701 may be shorted together to check for offsets in the Vertical Preamplifier. (This should bring the trace to within $+/-0.5$ division of center.)
10. Pins 18 and 19 of U701 may be shorted together to check for offsets from the delay line. (This should bring the trace to within $+/-1.5$ divisions of center.)
11. Shorting the bases of Q701 and Q702 together usually causes the vertical output circuit to oscillate.
12. Check the center lead of R708 for a voltage of about +60 V , and a common-mode voltage difference (between the two deflection plates) of about $0 \vee$ (when pins 6 and 7 of U701 are shorted together).
13. Check the operation of Vertical Comparator U702 by noting if the TRACK MEASUREMENT cursors are off screen when called up. (The Vertical Comparator circuit is enabled only during a vertical Self Cal.)

## Schematic Diagram 4-DISPLAY AND TRIGGER LOGIC AND PROCESSOR INTERFACE

1. Put the oscilloscope into A Horizontal Display mode with CH 1 and CH 2 Vertical modes on.
2. Check U600 vertical enables (CH 1 EN , pin 39; CH 2 EN, pin 38)
3. Probe U600 A TRIG selects (ATS 0, pin 31; ATS 1, pin 32; ATS 2, pin 33; A SLOPE, pin 30) and $B$ TRIG selects (BTS 0, pin 27; BTS 1, pin 28; BTS 2, pin 29; B SLOPE, pin 26) while making trigger source and slope changes on the front panel. (Probe the A select lines for A trigger changes and the $B$ select lines for $B$ trigger changes.)
4. Check the 10 MHz clock at U600 pin 1 and U600 power sources. Check the 1 kHz clock at pin 2.
5. Check communication lines ( $\overline{\operatorname{SLIC} R D}$, pin 8; SLIC WR, pin 3; ADDRO through ADDR3, pins 4 through 7, respectively; and MB DATA, pin 9) for activity while making front panel trigger-mode changes.
6. Check THO line, pin 17. Signal should go to a logic high then low again for every new
front-panel setup condition, such as changing trigger mode, vertical mode, etc.
7. Check TDI, U600, pin 10, for a CMOS-level switching signal.
8. Check TDO, U602, pin 30, for an ECL-level switching signal.
9. Check the A TRIG signal at U602 pin 7.
10. Check the A GATE signal at U602 pin 14. Vary the Holdoff control to see if the spacing between each A GATE pulse changes.
11. Check the Holdoff oscillator output at U600 pin 15. Vary the HOLDOFF control to see if the width of the oscillator pulses varies.

## Schematic Diagram 5-A AND B SWEEPS AND DELAY COMPARATORS

1. Check that the baseline voltage (level that is present during holdoff after retrace) of the $A$ and $B$ ramp signals is -2 V . (The baseline level is referenced to the output of U309B and controlled by Q302, Q303, and Q304 for the A sweep and Q315, Q316, and Q317 for the B sweep).
2. Check the Sweep End Comparators, U316, for correct output. The sweep should end at a maximum of 2.5 V . Check the outputs (pin 15 for the A Sweep and pin 2 for the B Sweep) for about 3.8 V (the middle of ECL transitions).
3. Place the oscilloscope in delay and delta delay and check the Delay Time Comparators for correct outputs (DLY END 1 and DLY END 0).
4. Check U301 for correct switching and delay level transfer. Vary the Delay Time and the Delta Delay time and check for smooth signal change at pins 12 and 13 of U 301 C . If not correct, troubleshoot DAC system or front panel controls.
5. Run diagnostics to check for Shift Register 1 (U302 and U303) failure.
6. Exercise SR 1 and check switching of U307. U308, U310, and U311.

## Schematic Diagram 6-HORIZONTAL OUTPUT AMPLIFIER

1. Turn off the READOUT and check the ramps for equal (but opposite) waveforms on each plate. (Run MAKE FACTORY SETTINGS under the INTERNAL SETTINGS MENU in the SERVICE MENU.)

DO NOT short the horizontal output leads together or to ground. This will cause the output amplifier FETs to fail.
2. The MOSFET output transistors (Q801 and Q802, left plate; Q805 and Q806, right plate) run hot. If either side is cold, it is defective.
3. If output is railed to one side or the other, check U801A and the common-mode feedback. This circuit is supposed to keep the outputs at about 70 V average to ground.

## NOTE

Pins 12 and 13 of U802 may be shorted together to determine if the unbalance is before or after the horizontal preamplifier (U802). DO NOT short to ground.
4. Check the A RAMP and B RAMP input signals ( $A$ Horizontal mode for A RAMP and B Horizontal mode for B RAMP). They start at -2 V and ramp up to about +2.5 V .
5. Check the RO HORIZ input for correct waveform.
6. Check for the $X$ AXIS input signal on pin 7 of U802 in $X-Y$ mode (a signal must be applied to the CH 1 input).
7. Check at the junction of R855 and R854 (the common-mode bias point of Q810 and Q809) for 9.5 V .
8. Check at the junction of R846 and R852 (the common-mode source voltage of Q802 and Q805) for 15 V .
9. Check at the junction of R845 and R847 (the common-mode collector voltage of emitter followers Q803 and Q807) for 24 V .
10. Check the HDO and HD1 signals to U802 (see Table 6-7 for display states).
11. The horizontal preamplifier, U802, runs warm to the touch, but not hot.

Table 6-7
Horizontal Display State Logic

| HD0 | HD1 | Display |
| :---: | :---: | :--- |
| 0 | 0 | Readout |
| 0 | 1 | A Sweep |
| 1 | 0 | B Sweep |
| 1 | 1 | $X-Y$ |

## Schematic Diagram 7-Z-AXIS, CRT, PROBE ADJUST, AND CONTROL MUX

1. Turn off the Readout (READOUT control fully CCW) and test the node between CR2703 and C2711 for correct Z-Axis waveform. Vary INTEN to check operation. (Readout signals add confusion to the waveforms.)
2. Check for correct auto-focus operation at the junction of CR2715 and the collector of Q2712. Circuit action is exactly opposite of the Z-Axis to obtain focus tracking.
3. Parts replaced in the CRT High Voltage circuit and Z-Axis are safety controlled parts. Replacements need to be exact. The power supply is capable of delivering more than 15 watts at high voltages.

## Schematic Diagram 8-MEASUREMENT PROCESSOR

1. Check U2501, pin 57 for a RESET condition. Processor will be in permanent reset condition if RESET is high. Check that RESET goes high then low again at power on.
2. Check $\overline{\text { SYS RESET }}$ at U2506C pin 8.
3. Check that U2502 pin 5 is low when RESET is high. (This signal prevents random RAM writes on power up and power down when the processor is being reset.)
4. Check the 8 MHz clock (CLK 8M) at U2501 pin 56.
5. Check address decoding. Using a data analyzer or word recognizer probe, set up to recognize the address that produces a selected enabling strobe from the address decoding circuitry. Observe that the strobe is produced when the correct address is output by the Measurement Processor. The easiest way to generate most addresses during normal operation is to change a front-panel setting. See Table 6-8 for the addresses.

Table 6-8
Measurement Processor $1 / 0$ Memory Map

| $\stackrel{\text { Address range }(\mathrm{A} 19-\mathrm{AO})}{\mathrm{L}} \mathrm{~B}$ |  |  |  |  | Signal name and description | Signal source |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0110 | 0000 | 0000 | OXXX | XXXX | LED CATH CLK - Latches cathode data to Front-Panel LEDs. | U2501 pin 25 (Diagram 8) |
| 0110 | 0000 | 0000 | $1 \times X X$ | $X X X X$ | LED ANODE CLK-Latches anode data to Front-Panel LEDs. | U2501 pin 27 <br> (Diagram 8) |
| 0110 | 0000 | 0001 | OXXX | $X X X X$ | $\overline{\text { RO BUF WR -Latches Readout }}$ Processor control datas. | U2501 pin 28 <br> (Diagram 8) |
| 0110 | 0000 | 0001 | $1 \times X X$ | XXXX | $\overline{\mathrm{RO}} \mathrm{BUF}$ RD-Enables Readout RAM data onto bus DO-D7 (used for diagnostics only). | U2501 pin 29 <br> (Diagram 8) |
| 0110 | 0000 | 0010 | OXXX | X000 | $\overline{\text { DAC LSB CLK }}$-Latches the least significant byte of data to the D-to-A Converter. | U2517 pin 14 (Diagram 8) |
| 0110 | 0000 | 0010 | OXXX | X001 | $\overline{\text { DAC MSB CLK -Latches the most }}$ significant byte of data to the D-to-A Converter. | U2517 pin 14 <br> (Diagram 8) |
| 0110 | 0000 | 0010 | OXXX | X010 | POT MUX CLK -Latches channel selection code for pot multiplexer. | U2517 pin 13 <br> (Diagram 8) |
| 100X | XXXX | XXXX | XXXX | $\times 000$ | $\overline{\text { MB CNTL WR }}$ - Write enables Processor Interface circuitry (Diagram 4). | U2518 pin 15 <br> (Diagram 8) |
| 100X | 0000 | XXXX | XXXX | X000 | Sets BEAM FIND (U503 pin 7) high ON. | U503 pin 7 <br> (Diagram 4) |
| 100X | XXXX | XXXX | XXXX | $\times 001$ | SW BD SR LOAD -Loads column data into switch board registers. | U2518 pin 14 <br> (Diagram 8) |
| 100x | XXXX | XXXX | XXXX | X010 | SW BD SR SHIFT -Shifts data in switch board registers to the SW BD DATA signal line. | U2518 pin 13 <br> (Diagram 8) |
| 100x | XXXX | XXXX | XXXX | X011 | $\overline{\text { SLIC WR }}$-Write to SLIC, U600 Diagram 4. | U2518 pin 12 <br> (Diagram 8) |

Table 6-8 (cont)
Measurement Processor I/O Memory Map

| $\underset{ }{\text { Address range }(\mathrm{A} 19-\mathrm{A} 0)} \underset{\longrightarrow}{\text { Binary }}$ |  |  |  |  | Signal name and description | Signal source |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 100X | XXXX | XXXX | XXXX | $\times 100$ | FLIC WR -Write to FLIC, U602 Diagram 4. | U2518 pin 11 <br> (Diagram 8) |
| 100x | 0001 | XXXX | XXXX | $\times 000$ | SR 0 CLK-Clock Shift Register 0. | U606F pin 12 <br> (Diagram 4) |
| 100x | 0010 | $X X X X$ | $X X X X$ | $\times 000$ | SR 1 CLK-Clock Shift Register 1. | U606B pin 4 (Diagram 4) |
| 100X | 0011 | XXXX | $X X X X$ | $\times 000$ | Sets BEAM FIND (503 pin 7) low (OFF). | U503 pin 7 <br> (Diagram 4) |
| 100x | 0011 | XXXX | XXXX | $\times 000$ | Sets MSEL (U602 pin 29) low (delta or long delay). | U503 pin 13 (Diagram 4) |
| 100X | 0100 | $X X X X$ | $X X X X$ | $\times 000$ | Sets MSEL (U602 pin 29) high reference or short delay). | U503 pin 13 (Diagram 4) |
| 100x | 0101 | XXXX | XXXX | X000 | Sets SR DATA signal line low. | U606C pin 6 (Diagram 4) |
| 100x | 0110 | $X X X X$ | XXXX | $\times 000$ | Sets SR DATA signal line high. | U606C pin 6 (Diagram 4) |
| 100x | 0111 | XXXX | $X X X X$ | $\times 000$ | Places SR 2 in shift mode (U502 pin 10, Diagram 4). | U2512 pin 5 <br> (Diagram 8) |
| 100X | $1 \times X X$ | XXXX | XXXX | $\times 000$ | A places SR 2 in load mode (U502 pin 10, Diagram 4). | U2512 pin 5 <br> (Diagram 8) |
| 100X | 1111 | XXXX | XXXX | X011 | TRIG CLK --Loads coupling data to triggers. | U600 pin 19 (Diagram 4) |
| 1010 | XXXX | XXXX | XXXX | XXXX | Chip enable for Measurement Processor RAM (U2521 pin 20). | U2501 pin 36 (Diagram 8) |

## Schematic Diagram 9—READOUT SYSTEM

1. Run EXERCISE POTS (under the EXERCISER and FRONT PANEL menus) and check the digitized front panel pots for proper operation. The name of the exercised pot is displayed in the readout along with its current hexadecimal value. The value range from 00 to FF and the displayed
value should change smoothly as the pot is rotated. EXERCISE POTS always displays the HORIZ POSITION pot when first called.The FOCUS and SCALE ILLUM controls are not digitized and therefore are not checked with this exerciser. Pressing END exits the exerciser.
2. Check Readout Request pulse ( $\overline{\mathrm{RO}} \mathrm{REQ}, ~ U 2410$ pin 14).
3. Check Readout Blanking signal ( $\overline{\text { RO BLANK }}$, U2410 pin 16).
4. Check activity of Readout Processor (U2400).
5. Check outputs of Vertical and Horizontal Readout DACs (U2412 and U2413, respectively).
6. Check outputs of Vertical and Horizontal Readout Mixers (U2416A and U2416B, respectively) and multiplexers (U2414 and U2415, respectively).

## Schematic Diagram 10-SWITCH BOARD AND INTERFACE

1. Run the EXERCISE SWITCHES exerciser and check each of the front panel switches for correct operation. The circuit number of the latest switch pressed is displayed in the readout. Pressing END exits the exerciser; pressing CLEAR MEAS'MT terminates the Service Program.
2. Run the EXERCISE LEDS exerciser and check that each of the front panel LEDS may be turned on. The circuit number of the lighted LED is displayed in the readout. Circuit numbers DS2016, DS2017, $D S 2018, ~ D S 2019, ~ D S 2024, ~ D S 2040$, and DS2049 do not correspond to any LEDs on the front panel. Pressing END exits the excerciser.

## Schematic Diagram 11-ADC, DAC SYSTEM

1. Run EXERCISE DACS (under EXERCISER, PROC BOARD, and A TO D menus) and probe the demultiplexed outputs of U2303 (Diagram 11) and each of the sample-and-hold circuits for proper operation.
2. Run EXERCISE PORTS (under EXERCISER, PROC BOARD, and A TO D menus) and trace the signal path of any problems with the A-to-D Converter.

## Schematic Diagram 12-POWER SUPPLY

## WARNING

For safety reasons, an isolation transformer must be connected whenever troubleshooting is done in the Preregulator and Inverter Power Supply sections of the instrument.

1. If the fuse blows, check that Q2201 is not shorted. (If a variac is available, slowly increase the line voltage from 0 V until the voltage across C 2202 is about 40 V . If the same voltage is across C2203, Q2201 is probably shorted.)
2. If the Preregulator fails to come up ( 44 V not present across C2203):
a. Check $+D C$ at the output of the line rectifier bridge (across C2202) for approximately (VacRms X 1.414).
b. Check the Start-up circuit. The voltage across C2204 should ramp up to about 20 V , at which point Q2204 and Q2211 turn on to supply voltage to pin 10 of U2201.
c. Check the Preregulator circuit. Voltage pulses with a repetition rate of about $25 \mu \mathrm{~s}$ should be present on pin 8 of U2201 whenever supply voltage is present on pin 10.
3. If the power supply is in the chirp mode (continually restarting and shutting down), excessive loading of the +44 V supply is probable.

## TROUBLESHOOTING MEASUREMENT ERRORS

When certain measurement malfunctions occur, the symptoms usually indicate the circuit components that may be causing the problems. Read the following text to become familiar with the terms used in describing a measurement failure problem and the setup conditions needed to determine the symptoms. Then use Tabie 6-9 to check for measurement malfunction symptoms and probable causes.

1. Verify all the following conditions and read the definitions before attempting to use Table 6-9 for locating the source of measurement-error problems.

## Conditions:

All vertical channels can be successfully displayed and positioned independently.

The $A$ and $B$ sweeps both free-run and trigger successfully.

Both A and B Trigger COUPLING and SOURCE operate properly.

Normal-appearing readout text and cursors can be displayed.

Definition of terms:
Type 1 volts measurements are:
$\leftarrow$ VOLTS $\rightarrow 1$ and $i \rightarrow$ VOLTS $\rightarrow$
Type 2 volts measurements are:
DC
Type 3 volts measurements are:
+PEAK, -PEAK, PK-PK
Type 4 volts measurements are:
GATED +PEAK, GATED -PEAK, and GATED PK-PK

Measurement value accuracy is the accuracy of number displayed in top line of readout on the crt.

Measurement cursor accuracy is the accuracy of the match between cursor position and the measurement value.

Trigger value accuracy is the accuracy of the number displayed on the trigger level cursor.

Trigger cursor accuracy is the accuracy of the match between cursor position the value displayed on the cursor.
$A T L$ is the $A$ trigger level measurement system.
B TL is the B trigger level measurement system.

Table 6-9
Measurement Error Troubleshooting Hints

| Circuit <br> Problem | Symptoms |
| :--- | :--- |
|  | VERTICAL INPUTS (schematic Diagram 1) |
| Ground relay stuck in <br> signal position | Gross value problems for Types 1-4 volts measurements. <br> Gross value problems A TL and B TL. |
|  | Test: Use "EXERCISE VOLT REF." Check that the ground relay is in <br> ground position. |
| Defective X10, X100, <br> X1, X2, X5 Relays <br> and Attenuators | Gross value problems for affected channel for Types 1-4 volts <br> measurements. |
| Gross value problems in A TL and B TL. |  |

Table 6-9, (cont) Measurement Error Troubleshooting Hints

| Circuit Problem | Symptoms |
| :---: | :---: |
| $\overline{\text { RO FREEZE }}$ line stuck high (U173-11) | Gross cursor problems for Types 2, 3, and 4 measurements. Gross cursor problems for A TL and B TL. <br> Gross valve problems with Type 1 measurements. |
|  | Test: Monitor during CH 1/CH 2 VOLTMETER menu SELF CAL. Signal line should reach TTL low. |
| VERTICAL PREAMP AND OUTPUT AMPLIFIER (schematic Diagram 2) |  |
| Preamp Trig Outputs Bad | Gross value problems for Types 2, 3, and 4 measurements for affected channel. |
|  | Test: Check B triggering on the affected channel. |
| Preamp Vert Outputs or Enable Bad | Gross cursor problems for Types 2, 3, and 4 measurement for affected channel. <br> Gross value accuracy problems for Type 1 measurements for affected channel. |
|  | Test: With only the affected channel selected for display, check that channel is shown and check gain accuracy. |
| VERT COMP output (U702-7) | Gross value problems with Type 1 measurements. <br> Gross cursor problems with Types 2, 3, and 4 measurements. Gross cursor problems with A TL and B TL. |
|  | Test: Lift W1101 and ground the base of Q703. VERT COMP should be toggling between TTL high and TTL low either with readout on or with two channels on (one at screen top, one at screen bottom). |
| A AND B TRIGGER SYSTEM (schematic Diagram 3) |  |
| A Trig Source <br> Multiplexer (U421A) | "SEARCH FAILED AT $5 \mu \mathrm{~s}$ " for "SELF CAL" on time measurements unless stuck in LINE. <br> Minor to gross value problems (depending on which source is stuck) with the A TL measurement. |
|  | Test: Set A Trigger SOURCE to VERT. <br> For each channel, display only that channel and check that the signal applied to the displayed channel appears at pin 25 of U421. |

Table 6-9, (cont)
Measurement Error Troubleshooting Hints

| Circuit Problem | Symptoms |
| :---: | :---: |
| A Trig Cplg Multiplexer(U421B) | May get "SEARCH FAlLED at . . ." for "SELF CAL" on time measurements if coupling is stuck in HF REF coupling. <br> Minor to gross value problems with the A TL if coupling is stuck in any position except DC. |
|  | Test: Switch between all A Trigger COUPLING settings with a 10 kHz square wave connected to CH 1 input; use CH 1 for the A Trigger SOURCE and set A Trigger SLOPE to $\neg$. <br> Check signal at U421 pin 25 (square edge for DC or NOISE REJ; rounded corner for HF REJ; spiked corner for LF REJ; signal center shifts to ground for AC). |
| A Trig line stuck high or low (U421C-10) | "SEARCH FAILED AT $5 \mu \mathrm{~s}$ for SELF CAL" on time measurements. Gross value problems with A TL. |
|  | Test: Set the A Trigger mode to NORM. Check that the sweep can be triggered on the Channel 1 signal. |
| B Trig Source Multiplexer (U431A) | Minor to gross value problems (depending on which bit is stuck) with types 2, 3, and 4 measurements. <br> Minor to gross value problems with B TL. "SEARCH RETURNED BAD VALUE AT . . . " on time measurements (unless stuck in LINE SOURCE). |
|  | Test: Set B Trigger Source to VERT. For each channel, display only that channel and check that the signal applied to the displayed channel appears at $U 431$ pin 25. |
| B Trig Cplg Multiplexer (U431B) | Minor to gross value problems with Types 3 and 4 measurements (depending on which coupling bit is stuck). <br> Gross value problems with Type 2 measurements (unless stuck in measurement mode input). <br> Minor to gross value problems with B TL. |
| B Trig BW Limit circuitry | Minor value problems with Type 3 and 4 measurements if stuck in limited BW position. <br> Minor value problems with Type 2 measurements if stuck in full BW position. |
| BW FULL B line (U1103-14) | See notes on "B Trig BW limiter." |
|  | Test: Should be CMOS low when SCOPE BW button is lit. <br> Should be CMOS high when SCOPE BW button is not lit; use ALT Horizontal Mode with B CPLG set to DC. |

Table 6-9, (cont)
Measurement Error Troubleshooting Hints

| Circuit Problem | Symptoms |
| :---: | :---: |
| B Trig <br> (U431C) | Gross value problems with Types 2, 3, and 4 measurements. Gross value problems with B TL. <br> "SEARCH RETURNED BAD VALUE AT . . ." on time measurements. |
| B TV TRIG EN line stuck high (U1103-4) | Gross value problems with Type 2 measurements. |
|  | Test: Run $\mathrm{CH} 1 / \mathrm{CH} 2$ VOLTMETER DC measurement and check that the B TV TRIG EN signal is at TTL low. |
| VERT COMP EN line stuck high (U1103-7) | See notes on "VERT COMP" (schematic Diagram 2). |
|  | Test: $\overline{V E R T}$ COMP EN should be at CMOS high in normal use. Run "SELF CAL" from the CH 1/CH 2 VOLTMETER menu and check that VERT COMP EN goes to a CMOS low. |
| LINE/TIME BASE CAL signal Mux stuck in LINE position (U1106A) | "RETURNED BAD SEARCH VALUE AT . . ." from "SELF CAL" for time measurements. |
|  | Test: Run "EXERCISE TIME REF" diagnostic and check the output of U1106A (pin 1) for changing signal. |
| DC Average circuit (U1101B | Gross value problems for Type 2 measurements (minor value problems with low frequencies if RC values in the filter are incorrect). |
|  | Test: Display only CH 1 , run DC measurement. Apply to 50 Hz sinewave signal with a DC offset to the CH 1 input. Check that only the dc value appears at output of the DC Average circuit. |
| DISPLAY AND TRIGGER LOGIC AND PROCESSOR INTERFACE (schematic Diagram 4) |  |
| MB RETURN line (U502-12) | See notes on VERT COMP (schematic Diagram 2). |
| MP DLY SEL line (U503-13 or MP DLY SEL Interface (U602) | "SEARCH RETURNED BAD VALUE AT $5 \mu \mathrm{~s}$ " on time measurements. |
|  | Test: Turn all measurements off. Use the A Horizontal mode. The MP DLY SEL signal should be at a TTL high. |
| SR DATA line (U606C-6) | Effects are the same as those caused by malfunctions in SR 0 and SR 1. |
|  | Test: The A Sweep rate changes when SEC/DIV knob is rotated in the A Horizontal Display Mode. |
| SR 0 CLK line (U606F-12) | Effects are the same as those caused by malfunctions in SR 0. |
|  | Test: Channel 1 sensitivity changes when CH 1 VOLTS/DIV knob is rotated. |

Table 6-9, (cont)
Measurement Error Troubleshooting Hints

| Circuit Problem | Symptoms |
| :---: | :---: |
| SR 1 CLK line (U606B-4) | Effects same as those caused by malfunctions in SR 1. |
|  | Test: Check that the A Sweep rate changes when SEC/DIV knob is rotated in the A Horizontal Display Mode. |
| SR 1 CLK TTL line (U501-13) | Effects same as those caused by malfunctions in auxiliary section of SR 1 (U1103, Diagram 3). |
|  | Test: HF noise in trace reduces when SCOPE BW button is lit and increases when not lit. |
| Processor-to-DisplaySequencer Interface (U600) | Gross effects on all voltage and time measurements. |
|  | Test: Run DIAGNOSE and note results of SLIC CONTROL REG test; Set the A Trigger Mode to NORM; check that the ATS 0-2 signal lines (pins 31-33) change when the A Trigger SOURCE is changed. Check that no "LOW REP RATE" warning occurs with Type 4 measurements. |
| TDO Level Shifter (U603, Q603, Q602) | Same as Processor-to-Display-Sequencer Interface problem. |
|  | Test: Using NORM mode for both triggers, VERT source for both triggers, and CH 1 only displayed; apply a four-division, squarewave signal to the CH 1 input. <br> In the A Horizontal mode, check that the Trig'd LED light goes off and the sweep stops running with the Trigger LEVEL control at full CW rotation. <br> Change to AUTO mode for A trigger; check that sweep free runs with the Trigger LEVEL control at full CW rotation. <br> Check that the Trig'd LED can be lit and the sweep can be triggered when the Trigger Level is set to within the signal limits. Keep the A Sweep triggered for the next check. <br> In B Horizontal mode, check that the Trig'd LED goes off, and the sweep stops running with the Trigger LEVEL control set at full CW rotation. <br> Check that the Trig'd LED can be lit and the sweep made to trigger when Trigger Level is set to within the signal limits. <br> Change to RUNS AFTER Mode for the B Trigger. Check that the B Sweep free runs. |

Table 6-9, (cont)
Measurement Error Troubleshooting Hints

| Circuit Problem | Symptoms |
| :---: | :---: |
| DLY SEL line stuck low (U602-32) | "SEARCH RETURNED BAD VALUE AT $5 \mu \mathrm{~s}$ " for "SELF CAL" on time measurements. <br> Test: Use settings given in previous test. Set the first delay zone to the start of the sweep with $\leftarrow$ control. Check that the second delay zone can be positioned over the entire sweep length with the $\rightarrow$ control. |
| DLY SEL line stuck high (U602-32) | "SEARCH RETURNED BAD VALUE AT 0.1 ms " for "SELF CAL" on time measurements. |
|  | Test: Use settings given in previous test. Set the first delay zone to the start of the sweep with $\leftarrow$ control. Check that the second delay zone can be positioned over the entire sweep length with the $\rightarrow 1$ control. |
| ATS 0-2 (U600, pins 31-33) A Trig Source Multiplexer | See notes on "A Trig Source Multiplexer" (schematic Diagram 4). |
| BTS 0-2 (U600, pins 27-29) B Trig Source Multiplexer | See notes on "B Trig Source Multiplexer" (schematic Diagram 3). |
| B SLOPE line stuck high (U600-26) | Gross problems with +PEAK value, Types 2, 3, and 4 measurements. Gross value problems with the B TL measurement. |
| TRIG CLK line (U600-19) | See notes on "A Trig Cplg Multiplexer" and "B Trig Cplg Multiplexer" (U421 and U431, Diagram 3). |
| A AND B SWEEP AND DELAY COMPARATORS (schematic Diagram 5) |  |
| DLY END 0 line stuck low or high (U315-15) | "SEARCH RETURNED BAD VALUE AT $5 \mu \mathrm{~s}$ " for "SELF CAL" on time measurements. |
|  | Test: Run the $k-$ TIME $\rightarrow$ measurement in ALT Horizontal Mode with the A SEC/DIV at $1 \mathrm{~ms} / \mathrm{div}$ and the B SEC/DIV at $0.1 \mathrm{~ms} / \mathrm{div}$. Check that the first delay zone can be positioned over the length of sweep using the $k$ control. |
| Ref/Delta Delay Muxes stuck (U301A \& C) | See notes on DLY SEL (schematic Diagram 4). |
| A Sweep Control circuit (U302 \& U303) | "SEARCH RETURNED BAD VALUE AT (affected SEC/DIV setting)" for "SELF CAL" on time measurements. |
|  | Test: Use "EXERCISE VOLT REF" diagnostic. |

Table 6-9, (cont)
Measurement Error Troubleshooting Hints

| Circuit Problem | Symptoms |
| :---: | :---: |
| VOLT CAL 0-2 (U302 \& U303) | Gross value problems with Types 1, 2, 3, and 4 measurements. Gross value and cursor problems with A TL and B TL. |
|  | Test: Use "EXERCISE VOLT REF" diagnostic. |
| Z-AXIS, CRT, PROBE ADJUST, AND CONTROL MUX (schematic Diagram 7) |  |
| VOLT CAL line (U931-3) | Gross value problems with types $1,2,3$, and 4 measurements. Gross value and cursor problems with $A$ TL and B TL. |
|  | Test: Use "EXERCISE VOLT REF" diagnostic. |
| MEASUREMENT PROCESSOR (schematic Diagram 8) |  |
| TB CAL line (U2501-22) | "SEARCH FAILED AT SWEEP SPEED . . ." in "SELF CAL" on time measurements. |
|  | Test: Use "EXERCISE TIME REF" diagnostic. <br> Check that TB CAL signal is correct and signal path is intact to U421A pin 4 (Diagram 3) through U1106A. |
| $\begin{aligned} & \text { SLIC WR } \\ & (\text { U2518-12) } \end{aligned}$ | See notes on "Processor-to-Display-Sequencer Interface" (schematic Diagram 4). |
| $\begin{aligned} & \overline{\text { SLIC RD }} \\ & (\text { U2503C-8) } \end{aligned}$ | See notes on "Processor-to-Display-Sequencer Interface" (schematic Diagram 4). |
| $\begin{aligned} & \text { FLIC WR } \\ & (\mathrm{U} 2518-11) \end{aligned}$ | See notes and tests on "TDO Level Shifter" (schematic Diagram 4). |
| MB DATA (U2515-11) | See notes on "Processor-to-SLIC Interface" (schematic Diagram 4). |
| Field \& Mixer Control Latch or Readout Position Mixer stuck (U2411, U2414, U2415) | Gross cursor problems with Types 1, 2, 3, and 4 measurements. Gross cursor problems with A TL and B TL. |
|  | Test: Run $k$ VOLTS $\rightarrow$ CURSOR Measurement mode with only CH 1 displayed. <br> Check that cursors move the CH 1 position control. <br> Check that $k$ cursor moves with $k$ control and $\rightarrow 1$ cursor moves with $\rightarrow$ control. <br> Check that top and bottom line of readout do not move with any position control. |

Table 6-9, (cont)
Measurement Error Troubleshooting Hints

| Circuit Problem | Symptoms |
| :---: | :---: |
| ADC, DAC SYSTEM (schematic Diagram 11) |  |
| $\begin{aligned} & \text { A TRIG LVL } \\ & \text { (U2304C-8) } \end{aligned}$ | See notes for "A TRIG" (schematic Diagram 3). |
|  | Test: Select A trigger, set A Trig mode to NORM. <br> Check that A TRIG LVL can be set to any value from -2.5 to +2.5 volts using the Trigger LEVEL control. |
| $\begin{aligned} & \text { B REF TRIG LVL } \\ & (\mathrm{U} 2304 \mathrm{~B}-7) \end{aligned}$ | See notes for "B TRIG" (hints for schematic Diagram 3). |
|  | Test: Select $B$ trigger, set $B$ Trig mode to NORM, and select $B$ Horizontal mode. <br> Check that B REF TRIG LVL can be set to any value from -2.5 to +2.5 volts with Trigger LEVEL pot. |
| REF DLY <br> (U2305C-8) | "RETURNED BAD SEARCH VALUE AT . . ." for "SELF CAL" on time measurements. |
|  | Test: Run $\vDash$ TIME $\rightarrow \mid$ measurement in ALT Horizontal mode; A at $1 \mathrm{~ms} /$ div, B at $0.1 \mathrm{~ms} / \mathrm{div}$. <br> Check that first delay zone can be positioned over length of sweep with the $k$ control. |
| DELTA DELAY <br> (U2305B-7) | "RETURNED BAD SEARCH VALUE AT . . ." for "SELF CAL" on on time measurements. |
|  | Test: Use the preceding REF DELAY settings, and set first delay zone to start of sweep with $K$ control. <br> Check that second delay zone can be positioned over length of sweep with $\rightarrow$ control. |
| REF CURSOR (U2304D-14) | Gross value problems with Type 1 measurements. <br> Gross cursor problems with Types 2, 3, and 4 measurements. <br> Gross cursor problems with A TL and B TL. |
|  | Test: Run $k-$ VOLTS $\rightarrow \forall$ CURSOR Measurement Mode. <br> Check that $1 \leftarrow$ cursor can be positioned $\pm 15$ divisions around the trace ground. |
| DELTA CURSOR(U2304A-1) | Gross value problems with Type 1 measurements. <br> Gross cursor problems with Types 2, 3, and 4 measurements. <br> Gross cursor problems with A TL and B TL. |
|  | Test: Run $k$ VOLTS $\rightarrow$ CURSOR Measurement Mode. <br> Check that $\rightarrow$ cursor can be positioned $\pm 15$ divisions around the trace ground level. |

## CORRECTIVE MAINTENANCE

## INTRODUCTION

Corrective maintenance consists of component replacement and instrument repair. This part of the manual describes special techniques and procedures that are needed to replace components in this instrument. If it is necessary to ship your instrument to a Tektronix Service Center for repair or service, refer to the Repackaging for Shipment information in this section.

## MAINTENANCE PRECAUTIONS

To reduce the possibility of personal injury or instrument damage, observe the following precautions.

1. Disconnect the instrument from the ac-power source before removing or installing components.
2. Verify that the line-rectifier filter capacitors are discharged prior to performing any servicing.
3. Use care not to interconnect instrument grounds which may be at different potentials (cross grounding).
4. When soldering on circuit boards or small insulated wires, use only a 15-watt, pencil-type soldering iron.

## $\{$ CAUTION $\}$

Do not exceed 9 in-lb of torque when tightening the 6-32 screws.
5. Use care not to overtighten screws into chassis. Threads that have been formed directly into aluminum components can be stripped out. If this occurs, use a 6-32 nut to secure the screw.

## WARNING

Portions of the power supply are floating at the ac line voltage level and pose a shock hazard if not isolated from ground.
6. Use an isolation transformer to supply power to the 22461 Y or 2246 Mod A if you troubleshoot in power supply with power applied to the instrument.

## OBTAINING REPLACEMENT PARTS

Electrical and mechanical replacement parts can be obtained through your local Tektronix Field Office or representative. However, many of the standard electronic components may usually be obtained from a local commercial source. Before purchasing or ordering a part from a source other than Tektronix, Inc., please check the Replaceable Electrical Parts list for the proper value, rating, tolerance, and description.

## NOTE

The physical size and shape of a component may affect instrument performance, particularly at high frequencies. Always use direct-replacement components, unless it is known that a substitute will not degrade instrument performance. Parts in the crt high-voltage and Z-Axis circuits are safety-controlled-USE EXACT REPLACEMENTS in these circuits.

## Special Parts

In addition to the standard electronic components, some special parts are used in the $22461 Y$ and $2246 \operatorname{Mod} A$. These components are manufactured or selected by Tektronix, Inc. to meet specific performance requirements, or are manufactured for Tektronix, Inc. in accordance with our specifications. The various manufacturers can be identified by referring to the Cross Index-Manufacturer's Code number to Manufacturer at the beginning of the Replaceable Electrical Parts list (Section 8). Most of the mechanical parts for in this instrument are
manufactured by Tektronix, Inc. Order all special parts directly from your local Tektronix Field Office or representative.

## Ordering Parts

When ordering replacement parts from Tektronix, Inc., be sure to include all of the following information:

1. Instrument type (include modification or option numbers).
2. Instrument serial number.
3. A description of the part (if electrical, include its full circuit component number).
4. Tektronix part number.

## REPACKAGING FOR SHIPMENT

It is recommended that the original carton and packing material be saved in the event it is necessary for the instrument to be reshipped using a commercial transport carrier. If the original materials are unfit or not available, then repackage the instrument using the following procedure.

1. Use a corrugated cardboard shipping carton having a test strength of at least 275 pounds and with an inside dimension at least six inches greater than the instrument dimensions.
2. If instrument is being shipped to a Tektronix Service Center, enclose the following information: show the owner's address, name and phone number of a contact person, type and serial number of the instrument, reason for returning, and a complete description of the service required.
3. Completely wrap the instrument with polyethylene sheeting or equivalent to protect the outside finish and prevent entry of foreign materials into the instrument.
4. Cushion the instrument on all sides using three inches of padding material or urethane foam, tightly packed between the carton and the instrument.
5. Seal the shipping carton with an industrial stapler or strapping tape.
6. If the instrument was NOT purchased under Air Force Contract No. F41608-88-D-008, address the shipping carton to the nearest Tektronix Service Center. Please include your own return address on the shipping carton.

NOTE
Special instructions for instruments purchased under Air Force Contract No. F41608-88-D-008:

If the instrument IS under warranty, contact your local Tektronix Service Center for shipping instructions.

If the instrument IS NOT under warranty, address the shipping carton to the nearest Tektronix Service Center. Please include your own return address on the shipping carton.

## MAINTENANCE AIDS

The maintenance aids listed in Table 6-10 include items required for performing most of the maintenance procedures in this instrument. Equivalent products may be substituted for the examples given, provided their characteristics are similar.

## INTERCONNECTIONS

Several types of mating connectors are used for the interconnecting cable pins. The following information provides the replacement procedures for the various type connectors.

## End-Lead Pin Connectors

Pin connectors used to connect the wires to the interconnect pins are factory assembled. They consist of machine-inserted pin connectors mounted in plastic holders. If the connectors are faulty, the entire wire assembly should be replaced.

## Ribbon-Cable Connectors

The etch-ribbon cables have the connector pins crimped onto the wire runs. If the connectors are defective, the entire ribbon cable assembly must be replaced.

Table 6-10
Maintenance Aids

| Description | Specification | Usage | Example |
| :---: | :---: | :---: | :---: |
| Soldering Iron | 15 to 25 W . | General soldering and unsoldering. | Antex Precision Model C. |
| Torx Screwdriver Tips and Handle | Torx tips: \#T7, \#T9, \#T10, \#T15, and \#T20. <br> Handle: 1/4 inch hex drive. | Assembly and disassembly. | Tektronix Part Numbers: <br> Handles: <br> $81 / 2$ in. 003-0293-00 <br> $31 / 2$ in. 003-0445-00 |
| Nutdrivers | 1/4 inch, 5/16 inch, 1/2 inch, and 9/16 inch. | Assembly and disassembly. | Xcelite \#8, \#10, \#16 and \#18. |
| Open-end Wrench | 9/16 inch and 1/2 inch. | Channel Input and Ext Trig BNC Connectors | Tektronix Part Numbers: <br> 9/16) 003-0502-00. <br> 1/2) 003-0822-00 |
| Hex Wrenches | 0.050 inch, 1/16 inch. | Assembly and disassembly. | Allen wrenches. |
| Long-nose Pliers |  | Component removal and replacement. | Diamalloy Model LN55-3. |
| Diagonal Cutters |  | Component removal and replacement. | Diamalloy Model M554-3. |
| Vacuum Solder Extractor. | No Static Charge Retention. | Unsoldering static sensitive devices and components on multilayer boards. | Pace Model PC-10. |
| Contact Cleaner | No-Noise. ${ }^{\text {® }}$ | Switch and pot cleaning. | Tektronix Part Number 006-0442-02. |
| Pin-Replacement Kit |  | Replace circuit board connector pins. | Tektronix Part Number 040-0542-01. |
| IC-removal Tool |  | Removing DIP IC packages. | Augat T114-1. |
| Isopropyl Alcohol | Reagent grade. | Cleaning attenuator and front-panel assemblies. | 2-Isopropanol. |
| Isolation Transformer |  | Isolate the instrument from the ac power source for safety. | Tektronix Part Number 006-5953-00. |
| 1X Probe |  | Power supply ripple check. | TEKTRONIX P6101A |

## BATTERY

The 22461 Y instruments serial numbered B714999 and below use a pair of silver-oxide cells with an life expectancy of 1 year. Above this serial number, the 2246 1Y uses a lithium battery. The 2246 Mod A uses a lithium battery.

The battery is used to supply backup power to the System RAM and should last for at least 5 years. When it becomes necessary to replace the battery, be sure to observe the following general warning about disposal of lithium batteries.

## WARNING

To avoid personal injury, observe proper procedures for handling and disposal of lithium batteries. Improper handling may cause fire, explosion, or severe burns. Do not recharge, crush, disassemble, heat the battery above $212^{\circ} \mathrm{F}\left(100^{\circ} \mathrm{C}\right)$, incinerate, or expose contents of the battery to water. Dispose of battery in accordance with local, state, and national regulations.

Typically, small quantities (less than 20) can be safely disposed of with ordinary garbage in a sanitary landfill. Larger quantities must be sent by surface transport to a hazardous waste disposal facility. The batteries should be individually packaged to prevent shorting and packed in a sturdy container that is clearly labeled "Lithium Batteries-DO NOT OPEN."

## TRANSISTORS AND INTEGRATED CIRCUITS

Transistors and integrated circuits should not be replaced unless they are actually defective. If one is removed from its socket or unsoldered from the circuit board during routine maintenance, return it to its original board location. Unnecessary replacement or transposing of semiconductor devices may affect the adjustment of the instrument. When a semiconductor is replaced, check the performance of any circuit that may be affected.

Any replacement component should be of the original type or a direct replacement. Bend component leads to fit their circuit board holes, and cut the leads to the same length as the original component. See Figure 9-2 in the Diagrams section for the semiconductor lead-configurations.


#### Abstract

After replacing a power transistor, check that the collector is not shorted to the chassis before applying power to the instrument.


To remove socketed dual-in-line packaged (DIP) integrated circuits, pull slowly and evenly on both ends of the device. Avoid disengaging one end of the integrated circuit from the socket before the other, since this may damage the pins.

To remove a soldered DIP IC when it is going to be replaced, clip all the leads of the device and remove the leads from the circuit board one at a time. If the device must be removed intact for possible reinstallation, do not heat adjacent conductors consecutively. Apply heat to pins at alternate sides and ends of the IC as solder is removed. Allow a moment for the circuit board to cool before proceeding to the next pin.

## SOLDERING TECHNIQUES

The reliability and accuracy of this instrument can be maintained only if proper soldering techniques are used to remove or replace parts. General soldering techniques that apply to maintenance of any precision electronic equipment should be used when working on this instrument.

## WARNING

To avoid an electric-shock hazard, observe the following precautions before attempting any soldering: turn the instrument off, disconnect it from the ac power source, and wait at least three minutes for the line-rectifier filter capacitors to discharge.

Use rosin-core wire solder containing $63 \%$ tin and $37 \%$ lead. Contact your local Tektronix Field Office or representative to obtain the names of approved solder types.

When soldering on circuits boards or small insulated wires, use only a 15-watt, pencil-type soldering iron. A higher wattage soldering iron may cause etched circuit conductors to separate from the board base material and melt the insulation on small wires. Always keep the soldering-iron tip properly tinned to ensure the best heat transfer from the tip to the solder joint. Apply only enough solder to make a firm joint. After soldering, clean the area around
the solder connection with an approved fluxremoving solvent (such as isopropyl alcohol) and allow it to air dry.

Only an experienced maintenance person, proficient in the use of vacuum-type desoldering equipment should attempt repair of any circuit board in this instrument. Many integrated circuits are static sensitive and may be damaged by solder extractors that generate static charges. Perform work involving static-sensitive devices only at a static-free work station while wearing a grounded antistatic wrist strap. Use only an antistatic vacuum-type solder extractor approved by a Tektronix Service Center.

## \{CAUTION\}

Attempts to unsolder, remove, and resolder leads from the component side of a circuit board may cause damage to the reverse side of the circuit board. The following techniques should be used to replace a component on a circuit board:

1. Touch the vacuum desoldering tool tip to the lead at the solder connection. Never place the tip directly on the board; doing so may damage the board.

## NOTE

Some components are difficult to remove from the circuit board due to a bend placed in the component leads during machine insertion. To make removal of machineinserted components easier, straighten the component leads on the reverse side of the circuit board.
2. When removing a multipin component, especially an IC, do not heat adjacent pins consecutively. Apply heat to the pins at alternate sides and ends of the IC as solder is removed. Allow a moment for the circuit board to cool before proceeding to the next pin.

Excessive heat can cause the etched circuit conductors to separate from the circuit board. Never allow the solder extractor tip to remain at one place on the board for more than three seconds. Solder wick, springactuated or squeeze-bulb solder suckers, and heat blocks (for desoldering multipin components) must not be used. Damage caused by poor soldering techniques can void the instrument warranty.
3. Bend the leads of the replacement component to fit the holes in the circuit board. If the component is replaced while the board is installed in the instrument, cut the leads so they protrude only a small amount through the reverse side of the circuit board. Excess lead length may cause shorting to other conductive parts.
4. Insert the leads into the holes of the board so that the replacement component is positioned the same as the original component. Most components should be firmly seated against the circuit board.
5. Touch the soldering iron tip to the connection and apply enough solder to make a firm solder joint. Do not move the component while the solder hardens.
6. Cut off any excess lead protruding through the circuit board (if not clipped to the correct length in step 3).
7. Clean the area around the solder connection with an approved flux-removing solvent. Be careful not to remove any of the printed information from the circuit board.

## REMOVAL AND REPLACEMENT INSTRUCTIONS

## WARNING

To avoid electric shock, disconnect the instrument from the power input source before removing or replacing any component or assembly.

The exploded view drawings in Replaceable Mechanical Parts list may be helpful during removal
and reinstallation of individual components or subassemblies. Circuit board and component locations are shown in Diagrams section.

Read these instructions before attempting to remove or install any components.

## Cabinet

To remove the cabinet:

1. Unplug the power cord from its rear-panel connector.
2. Place the instrument face down on a clean, flat surface.
3. Remove the Torx-head screw from the right side near the rear of the cabinet.
4. Remove the plastic rear cover, held with four Torx-head screws.
5. Slide the cabinet housing up and off the instrument.

## WARNING

Potentially dangerous voltages exist at several points throughout this instrument. If it is operated with the cabinet removed, do not touch exposed connections or components. Before replacing parts or cleaning, disconnect the ac-power source from the instrument and check that the line-rectifier filter capacitors have discharged. Also, check the low voltages at the Power-Supply/Main-Board interface connector (J1204). If any of the supply-voltage or line-voltage filter capacitors remain charged for more that 20 seconds, discharge them to ground through a $1 \mathrm{k} \Omega, 5$ - or 6 -watt resistor.

To install the cabinet:
6. Carefully slide the cabinet housing over the rear of the instrument. Be careful not to snag any of the folded ribbon cables. Make sure the cabinet housing slides between the plastic front-panel housing and the instrument chassis.
7. Install the rear-panel. Secure it with four \#15 Torx-head screws.
8. Install a Torx-head screw in the right side of the cabinet.

## Crt Removal and Replacement

## WARNING

Use care when handling a crt. Breaking the crt can cause high-velocity scattering of glass fragments. Protective clothing and safety glasses or safety face shield should be worn. Avoid striking the crt on any object which might cause it to crack or implode. When storing a crt, either place it in a protective carton or set it face down on a smooth surface in a protected location with a soft mat under faceplate.

To remove the crt:

## WARNING

To avoid electrical shock, carefully discharge the crt anode lead directly to the metal chassis. To avoid static-discharge damage to electronic components, do not allow the anode lead to discharge into the adjacent circuitry.

1. Disconnect the high-voltage anode lead. Pull the anode-lead coupler apart slowly and carefully. DO NOT touch the exposed connector pin as it is withdrawn from coupler socket. Discharge the exposed anode pin to the metal chassis only. A hole is provided in the left side of the power supply chassis for the purpose of holding the end of the lead to prevent a recharge while it is disconnected.
2. Unplug the trace rotation cable (P27) from the Main board.
3. Unplug the two vertical and the two horizontal deflection leads from the crt neck. Grasp each lead connector with long-nosed pliers and pull it straight away from the crt neck pins. Be careful not to bend the neck pins.
4. Remove the crt implosion shield and bezel frame (held with two screws at the lower side).
5. Remove rear crt shield (held in place by 3 selftapping screws).
6. Place your left hand on crt neck shield and your right hand over crt face. Move the crt and neck shield assembly forward to unplug the crt from its socket and carefully withdraw it from the instrument, feeding the trace rotation cable and anode lead through their respective access holes in the crt front shield. Do not hold the crt assembly by the neck shield only.
7. If it is necessary remove the metal neck shield from crt. Carefully slide the shield to rear of the crt. Be careful not to damage the neck pins.

To install the crt:
8. Install the metal shield over the neck of the crt. Make sure that the plastic grommet is in place over the front of the shield. Align the neck pins with the shield holes.
9. Check that the graticule scale-illumination light pipe is in place at bottom front of crt opening. Also make sure that the four crt corner cushions are in place in the crt opening of the subpanel.
10. Carefully guide the crt, anode lead, and trace rotation cable into the instrument and through the access holes in the crt shield. Line up the crt base pins with base socket. Make sure that the ground clip above the rear of the crt shield goes outside of the shield. Hold in on the rear of the base socket with one hand and push on the face of the crt with the other hand to completely seat the crt base pins. If the crt will not go in all the way, check for bent pins. DO NOT FORCE this connection!
11. Connect the trace rotation cable (P27) to the Main board.
12. Connect the vertical and horizontal deflection leads to the crt neck pins. The horizontal deflection leads (going to bottom pins) should be crossed.
13. Connect the high-voltage anode lead.
14. Install the crt implosion shield and frame using two $7 / 8$ in. Torx-head screws.
15. Check that the graticule illumination light bulbs are in place in the light pipe at the bottom of the crt.
16. Reinstall rear crt shield.

## BNC Connectors (Vertical Inputs)

To replace BNC Connectors:

1. Remove the Main board (see Main board removal procedure).

## NOTE

Do not disconnect the ends of the delay line from board as indicated in the Main board removal procedure. It is not necessary for replacing the input BNC connectors.
2. Unsolder the input lead of the defective BNC connector(s).
3. Using a 9/16 open-end wrench, remove and replace the defective BNC connector(s).
4. Resolder the input lead(s).
5. Replace the Main board (see Main board installation procedure).

## A16 Processor Board

To remove the Processor board:

1. Unplug ribbon cables P2501, P2502, P2302, and P2304 from the the processor board. To aid the release of the ribbon-cable pins from connector, slide a thin-shafted, flat-bladed screwdriver between the ribbon cable (near the connector) and the etched-circuit board and pry gently upward.
2. Remove the six Torx-head attaching screws (one at each corner and two in the middle).
3. Unplug P2105 (17-pin) from the Potentiometer board and lift the Processor board out of the instrument.

To install the Processor board:


Do not exceed 9 in-1b of torque when tightening the 6-32 screws that hold the circuit board to the chassis. Damage to the circuit board andlor screw threads may result if the screws are overtightened.
4. Position the board to align the screw holes and install the six Torx-head attaching screws (two, $5 / 8 \mathrm{in}$. screws in the center and one, 7/16 in. screw at each corner).
5. Plug P2105 into the Potentiometer board.
6. Plug in ribbon cables P2501, P2502, P2302, P2304, and P2105. Press the ribbon cable pins firmly into the connector holes.

## A18 Power Supply Board

To remove the Power Supply board:

1. Remove the Processor board (see Processor board removal procedure).

## WARNING

To avoid electrical shock, carefully discharge the crt anode lead directly to the metal chassis. To avoid static-discharge damage to electronic components, do not allow the anode lead to discharge into the adjacent circuitry.
2. Disconnect the high-voltage anode lead. Pull the anode-lead coupler apart slowly and carefully. DO NOT touch the exposed connector pin as it is withdrawn from coupler socket. Discharge the exposed anode pin to the metal chassis only. A hole is provided in the left side of the power supply chassis for the purpose of holding the end of the lead to prevent a recharge while it is disconnected.
3. Remove the eight screws holding the power supply housing shield and remove the shield.
4. Disconnect the connectors from J 2208 and J2225 and the two wires from ac-line filter. (Note the color stripes on the wires to the line filter for reinstallation.)
5. Pull the HV connector through the grommet in the power supply housing and remove the HV lead and P2208 cable from the retaining clip.
6. Set the POWER switch in the OFF (out) position.

## \{CAUTION\}

The POWER switch must be in the OFF position to safely remove the shaft from the shaft of the switch in the following step. Pulling the shaft off with the POWER switch on may damage the switch shaft and spring assembly.
7. Remove the power-switch-extension shaft. Snap the extension shaft off the transitional pivot assembly, then remove the shaft by prying from the switch actuator.
8. Remove the six screws that hold down the Power Supply board.
9. Unplug the Power Supply board from the Main board interface connector. Grasp the two heat sinks near the center of the board, one with each hand, and pull up to disconnect the interface connection.
10. Lift the front of Power Supply board and withdraw the board from the power-supply housing.

To install the Power Supply board:
11. Place the Power Supply board into power-supply housing. First, guide the fuse holder into the rear panel, then lower the front end of the board until the board interface pins touch the interface connector.
12. Plug the interface pins into the interface connector. With the Power Supply board against the rear panel, pull up on the large electrolytic capacitor (near the center of the board) with one hand and push down on HV multiplier module (at front of board) with the other hand. This action tends to align the pins with the connector. At the same time you will have to move the board around slightly so that the pins will easily slide into the connector holes. DO NOT FORCE this connection, otherwise you may bend the pins.

## CAUTION

Do not exceed 9 in-lb of torque when tightening the 6-32 screws that hold the circuit board to the chassis. Damage to the circuit board or screw threads may result if the screws are overtightened.
13. Secure the circuit board with six screws.
14. Install the power-switch-extension shaft. Snap the shaft onto the switch, then onto the transitional pivot assembly.
15. Insert the high-voltage lead through the power-supply-housing grommet and snap the connector shell into the clamp at the front of the power-supply housing.
16. Connect the leads to J2208, J2225, and the ac-line filter (observe the color coding noted when the filter leads were disconnected).
17. Route the HV lead and P2208 cable through the retaining clip.
18. Install the power-supply-housing shield with eight screws.
19. Connect the crt anode lead to the HV connector.
20. Install the Processor board (see Processor board installation instructions).

## Potentiometer/Switch board Assembly

To remove the Potentiometer/Switch board assembly:

1. Unplug ribbon-cable connector P2501 from the Processor board and unplug ribbon-cable connector P2105 from the Potentiometer board.
2. Remove the CH 1 and CH 2 VOLTS/DIV VAR knobs and the SEC/DIV VAR knob. (A 1/16 in. hexagonal wrench is needed for the set screws).
3. Pull out all the remaining front-panel knobs to the right of the crt. Grasp the knobs firmly and pull straight out from the front panel.
4. Pull out on the four captive plastic snap fasteners on the back of the switch board that hold the Switch board assembly to the front panel (not those that hold the Potentiometer board to the Switch board). Use long-nose pliers as necessary to reach the fasteners.
5. Unclip the high-voltage connector from the front of the power-supply housing. Remove the plastic retaining clip from the housing (it is pressed in). Move the high-voltage connector to the top of the power-supply housing to make room for removing the Potentiometer/Switch board.
6. Move the Potentiometer/Switch board assembly back away from the front panel and lift it out of the instrument.

To separate the A12 Potentiometer board from the Switch board:
7. Pull out on the three snap fasteners that hold the Potentiometer board to the Switch board.
8. Separate the Potentiometer board from the Switch board.
9. If necessary, unplug the VAR control shafts from their potentiometers.

To install the Potentiometer board:
10. Set the three snap fasteners on the board in the released (out) position.
11. Plug the three VAR control shafts onto the VAR potentiometers.
12. Set the Potentiometer board in place over the Switch board and press in on the snap fasteners.

To install the Potentiometer/Switch board assembly:
13. Set the four snap fasteners (on the Switch board) in the released (out) position.
14. Guide the Potentiometer/Switch board assembly into place behind the front panel and press in on the snap fasteners.
15. Install the control knobs. Push knobs in while rotating slightly until they align with the shaft and snap in place. The two knobs without a positionindicator rib go on the $k$ and $\rightarrow$ control shafts.
16. Install the three VAR control knobs, using 1/16 in. allen wrench. Make sure that VAR controls are in the detent (fully CW) position, then rotate the knobs so that the VAR label is horizontal before tightening the set screws.
17. Install the high-voltage connector clip to the front side of the power-supply housing and snap the connector shell into it.
18. Connect ribbon cable J 2105 to the Potentiometer board and P2501 to the Processor board. Position the connector pins in the socket holes and push them fully into place.

## A10 Main Board

## NOTE

This procedure is intended for the complete replacement of the Main board. All repairs and component replacements (except BNC connectors) can be done without completely removing the Main board. When replacing BNC connectors, use the BNC Connector replacement procedure previously given in this section.

To remove the Main board:

1. Remove the crt (see crt removal procedure).
2. Pull out and remove the five crt-display control knobs.
3. Remove the Processor board (see Processor board removal procedure).
4. Remove the shield from the power-supply housing (held with eight screws).
5. Unplug the three-wire cable from J 2208 on the Power Supply board. Pull the cable and connector through the plastic grommet.
6. Release the crt socket from its holder on the rear panel. Push the socket out the rear enough to turn it sideways and push it through to the inside of the instrument.
7. Remove the Potentiometer/Switch board assembly (see Potentiometer/Switch board assembly removal procedure).
8. Remove the top and bottom attenuator shields. The bottom shield is held with 5 screws and the top shield is held with one remaining screw. See Figure 6-4.

## NOTE

If the Main board is being removed to replace or repair a component (such as a BNC connector), it is not necessary to disconnect the delay line from the board as indicated in the following step.
9. Unsolder the main delay-line wires from both sides of board (see Figures 6-4 and 6-5).
10. Unclip the delay line from both sides of the board and from the two clips at the lower side of the rear panel. Remove the two clips from the rear panel.
11. Remove the ten screws that hold the Main board to the chassis. Back out the three screws going through the rear panel enough to allow removal of Main board. See Figure 6-4.
12. Pull the three ribbon cables through to the bottom of the instrument.
13. Lift the back of main board enough to disconnect interface connection between Main board and Power Supply board.
14. Slide the Main board back away from front panel to completely remove the board from the instrument.

To install the Main board:
15. Guide the BNC connectors at front of the Main board into the holes in the front panel. Make sure that you guide the PROBE ADJUST jack into the front panel as well as the BNC connectors.
16. Lower the rear of Main board while guiding the interface connector onto the power supply interface pins. DO NOT FORCE this connection; the pins may bend. Make sure that the grommet holding the crt and power supply wires is in place between the board and the rear panel.

## \{CAUTION

Do not exceed 9 in-lb of torque when tightening the 6-32 screws that hold the circuit board to the chassis. Damage to the circuit board or screw threads may result if the screws are overtightened.
17. Secure the Main board with ten screws. See Figure 6-4.
18. Solder both ends of delay line to Main board. Be sure to observe the polarity of the leads. See Figures 6-4 and 6-5. Press the ends of delay line into the clips on board.
19. Snap the two plastic clips into the lower edge of the rear panel and snap the delay line into them.
20. Connect the three-wire cable from the crtsocket cable assembly to J2208 on the Power Supply board, making sure it is routed through the clip in the power-supply housing.
21. Install the shield on the power-supply housing (eight screws).
22. Install the inside attenuator shield (secure with one screw). Then install the outside attenuator shield (secure with five screws).
23. Install the Potentiometer/Switch board assembly (see Potentiometer/Switch board assembly installation procedure).


Figure 6-4. Main board removal.


Figure 6-5. Delay-line connections to top of Main board.
24. Install the Processor board.
25. Dress the two ribbon cables to the top of the instrument. Connect them to the Processor and Potentiometer boards.
26. Install the crt socket. Turn the socket sideways and push it through the crt-socket holder in the rear panel.
27. Install the crt (see crt installation procedure).

## OPTIONS

## INTRODUCTION

This section contains a general description of the instrument options and accessories available at the time of publication of this manual. Additional information about instrument options and other accessories can be obtained either by consulting the current Tektronix Product Catalog or by contacting your local Tektronix Field Office or representative.

## INTERNATIONAL POWER CORDS

Instruments are shipped with the detachable powercord option ordered by the customer. Descriptive information about the international power-cord options is provided in Section 2 "Preparation for Use." The following list describes the power cords available for the 22461 Y and 2246 Mod A.

| Standard | U. S. |
| :--- | :--- |
|  | $120 \mathrm{~V}, 60 \mathrm{~Hz}, 74 \mathrm{in}$. |
| Option A1 | Universal Euro 220 V, |
|  | $50 \mathrm{~Hz}, 2.5 \mathrm{~m}$ |
| Option A2 | UK $240 \mathrm{~V}, 50 \mathrm{~Hz}$, |
|  | 2.5 m |
| Option A3 | Australian 240 V, |
|  | $50 \mathrm{~Hz}, 2.5 \mathrm{~m}$ |
| Option A4 |  |
|  |  |
|  |  |
| Option A5 American |  |
|  | $240 \mathrm{~V}, 60 \mathrm{~Hz}, 2.5 \mathrm{~m}$ |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |

U. S.

120 V, $60 \mathrm{~Hz}, 74 \mathrm{in}$.
Universal Euro 220 V , $50 \mathrm{~Hz}, 2.5 \mathrm{~m}$

UK $240 \mathrm{~V}, 50 \mathrm{~Hz}$, 2.5 m

Australian 240 V , $50 \mathrm{~Hz}, 2.5 \mathrm{~m}$

North American

Switzerland 220 V , $50 \mathrm{~Hz}, 2.5 \mathrm{~m}$

## OPTION 1R RACKMOUNTED INSTRUMENT

When the 2246 1Y or 2246 Mod A Portable Oscilloscope is ordered with Option 1R, it is shipped in a configuration that permits easy installation into a 19 -inch-wide equipment rack. Also, an optional rackmounting kit may be ordered to convert the standard 22461 Y or 2246 Mod A to a rackmounted instrument. Installation instructions for rackmounting are provided in the documentation supplied with the rackmounting kit and the 1R Option.

# REPLACEABLE ELECTRICAL PARTS 

## PARTS ORDERING INFORMATION

Replacement parts are available from or through your local Tektronix, Inc. Field Office or representative.

Changes to Tektronix instruments are sometimes made to accommodate improved components as they become available, and to give you the benefit of the latest circuit improvements developed in our engineering department. It is therefore important. when ordering parts, to include the following information in your order: Part number, instrument type or number, serial number, and modification number if applicable.

If a part you have ordered has been replaced with a new or improved part, your local Tektronix, Inc. Field Office or representative will contact you concerning any change in part number.

Change information, if any, is located at the rear of this manual.

## LIST OF ASSEMBLIES

A list of assemblies can be found at the beginning of the Electrical Parts List. The assemblies are listed in numerical order, When the complete component number of a part is known, this list will identify the assembly in which the part is located.

## CROSS INDEX-MFR. CODE NUMBER TO MANUFACTURER

The Mfr. Code Number to Manufacturer index for the Electrical Parts List is located immediately after this page. The Cross Index provides codes. names and addresses of manufacturers of components listed in the Electrical Parts List.

ABBREVIATIONS
Abbreviations conform to American National Standard Y1.1.

## COMPONENT NUMBER (column one of the Electrical Parts List)

A numbering method has been used to identify assemblies, subassemblies and parts. Examples of this numbering method and typical expansions are illustrated by the following:

Example a.
component number


Read: Resistor 1234 of Assembly 23


Read: Resistor 1234 of Subassembly 2 of Assembly 23

Only the circuit number will appear on the diagrams and circuit board illustrations. Each diagram and circuit board illustration is clearly marked with the assembly number. Assembly numbers are also marked on the mechanical exploded views located in the Mechanical Parts List. The component number is obtained by adding the assembly number prefix to the circuit number

The Electrical Parts List is divided and arranged by assemblies in numerical sequence (e.g., assembly A1 with its subassemblies and parts, precedes assembly A2 with its subassemblies and parts)

Chassis-mounted parts have no assembly number prefix and are located at the end of the Electrical Parts List.

## TEKTRONIX PART NO. (column two of the Electrical Parts List)

Indicates part number to be used when ordering replacement part from Tektronix.

## SERIAL/MODEL NO. (columns three and four of the Electrical Parts List)

Column three (3) indicates the serial number at which the part was first used. Column four (4) indicates the serial number at which the part was removed. No serial number entered indicates part is good for all serial numbers.

## NAME \& DESCRIPTION (column five of the Electrical Parts List)

In the Parts List, an Item Name is separated from the description by a colon (:). Because of space limitations, an Item Name may sometimes appear as incomplete. For further liem Name identification, the U.S. Federal Cataloging Handbook H6-1 can be utilized where possible.

## MFR. CODE (column six of the Electrical Parts List)

Indicates the code number of the actual manufacturer of the part. (Code to name and address cross reference can be found immediately after this page.)

## MFR. PART NUMBER (column seven of the Electrical Parts List)

Indicates actual manufacturers part number.

# CROSS INDEX - MFR. CODE NUMBER TO MANUFACTURER 

| Mtr. Code | Manufacturer | Address | City, State, Zip Code |
| :---: | :---: | :---: | :---: |
| D5243 | ROEDERSTEIN E SPEZIALFABRIK FUER KONDENSATOREN GMBN | LUDMILLASTRASSE 23-25 | 8300 LANDSHUT GERMANY |
| S0482 | SONY CONSUMER ELECTRONICS |  | TOKYO JAPAN |
| 53774 | OSHINO ELECTRIC LAMP WORKS LTD | 52 MINAMI SHINAGAWA 2 CHORE SHINAGAWA KU | TOKYO JAPAN |
| S4307 | SCHAFFNER ELECTRONIK AG |  | LUTERBACH SWITZERLAND |
| TKOIH | SUNONWEALTH ELECTRIC <br> MACHINE IND CO LTD <br> \#149, YI YUNG RD, LING TA DISTRICT, | KAOHSIUNG, TAIWAN, R.O.C. POBOX 1436 | KAOHSIUNG, TAIWAN, R.O.C. |
| TK0032 | POWELL ELECTRONICS | 411 FAIRCHILD DR | MT VIEW CA 94040 |
| TK0161 | WYLE LABORATORIES ELECTRONICS MARKETING GROUP LOS ANGELES DIV | 124 MARYLAND ST | EL SEGUNDO CA 90245-4115 |
| TK0213 | TOPTRON CORP |  | TOKYO JAPAN |
| TK0515 | EVOX-RIFA INC | 100 TRI-STATE INTERNATIONAL SUITE 290 | LINCOLNSHIRE IL 60015 |
| TK1441 | GFS MANUFACTURING INC | 6 PROGRESS DR PO BOX 1409 | DOVER NH 03820 |
| TK1689 | AROMAT CORP | 10400 N TANTAU AVE | CUPERTINO CA 95014-0708 |
| TK1727 | PHILIPS NEDERLAND BV AFD ELONCO | POSTBUS 90050 | 5600 PB EINDHOVEN THE NETHERLANDS |
| TK1743 | UNITRODE (UK) LTD | 6 CRESSWELL PARK BLACKHEATH | LONDON SE 3 9RD ENGLAND |
| TK1913 | WIMA <br> THE INTER-TECHNICAL GROUP IND | ONE BRIDGE ST PO BOX 23 | IRVINGTON NY 10533 |
| TK1935 | ACCRA FAB INC | 11007 NE 37TH CIRCLE | VANCOUVER WA 98682 |
| TK2058 | TDK CORPORATION OF AMERICA | 2254 N FIRST ST | SAN JOSE CA 95131 |
| TK2073 | TOCOS AMERICA INC | 565 W GULF ROAD | ARLINGTON HEIGHTS IL 60005 |
| TK2172 | WYLE LABORATORIES (DIST) | 5250 NE ELAM YOUNG PARKWAY SUITE 600 | HILLSBORO OR 97124 |
| TK2190 | VARTA BATTERIES INC | 300 EXECUTIVE BLVD | ELMSFORD NY 10523-1202 |
| 0DWW6 | MICRO POWER ELECTRONICS | 7973 SW CIRRUS DRIVE BLDG. \#22 | BEAVERTON OR 97005 |
| 0JR03 | ZMAN AND ASSOCIATES | 7633 S 180th | KENT WA 98032 |
| OJR04 | TOSHIBA AMERICA INC ELECTRONICS COMPONENTS DIV BUSINESS SECTOR | 2692 DOW AVE | TUSTIN CA 92680 |
| 0J7N9 | MCX INC | 30608 SAN ANTONIO ST | HAYWARD CA 94544 |
| 00779 | AMP INC | 2800 FULLING MILL PO BOX 3608 | HARRISBURG PA 17105 |
| 01121 | ALLEN-BRADLEY CO | 1201 S 2ND ST | MILWAUKEE WI 53204-2410 |
| 01295 | TEXAS INSTRUMENTS INC SEMICONDUCTOR GROUP | 13500 N CENTRAL EXPY PO BOX 655012 | DALLAS TX 75265 |
| 01884 | SPRAGUE ELECTRIC CO DEARBORN ELECTRONICS DIV | PO BOX 1076 | LONGWOOD FL 32750-1076 |
| 04222 | AVX CERAMICS DIV OF AVX CORP | 19TH AVE SOUTH POBOX 867 | MYRTLE BEACH SC 29577 |

## CROSS INDEX - MFR. CODE NUMBER TO MANUFACTURER

| Mfr. Code | Manufacturer | Address | City, State, Zip Code |
| :---: | :---: | :---: | :---: |
| 04713 | MOTOROLA INC SEMICONDUCTOR PRODUCTS SECTOR | 5005 E MCDOWELL RD | PHOENIX AZ 85008-4229 |
| 05828 | GENERAL INSTRUMENT CORP GOVERNMENT SYSTEMS DIV | 600 W JOHN ST | HICKSVILLE NY 11802 |
| 09922 | BURNDY CORP | RICHARDS AVE | NORWALK CT 06852 |
| 1W344 | UNITED CHEMI-CON INC | 9801 W HIGGINS <br> SUITE 430 | ROSEMONT IL 60018-4704 |
| 11236 | CTS CORP <br> BERNE DIV <br> THICK FILM PRODUCTS GROUP | 406 PARR ROAD | BERNE IN 46711-9506 |
| 12697 | CLAROSTAT MFG CO INC | LOWER WASHINGTON ST | DOVER NH 03820 |
| 12969 | UNITRODE CORP | 5 FORBES RD | LEXINGTON MA 02173-7305 |
| 14193 | CAL-RINC | 1601 OLYMPIC BLVD PO BOX 1397 | SANTA MONICA CA 90406 |
| 14552 | MICROSEMI CORP | 2830 S FAIRVIEW ST | SANTA ANA CA 92704-5948 |
| 14604 | ELMWOOD SENSORS INC SUB OF FASCO INDUSTRIES INC | 500 NARRAGANSETT PARK DR PO BOX 2325 | PAWTUCKET RI 02861-4325 |
| 14936 | GENERAL INSTRUMENT CORP DISCRETE SEMI CONDUCTOR DIV | 600 W JOHN ST | HICKSVILLE NY 11802 |
| 15454 | KETEMA RODAN DIVISION | 2900 BLUE STAR STREET | ANAHEIM CA 92806-2591 |
| 16299 | AVX CORPORATION | 3900 ELECTRONICS DR | RALEIGH NC 27604-1620 |
| 18324 | SIGNETICS CORP MILITARY PRODUCTS DIV | 4130 S MARKET COURT | SACRAMENTO CA 95834-1222 |
| 19701 | PHILIPS COMPONENTS DISCRETE PRODUCTS <br> DIV RESISTIVE PRODUCTS FACILITY AIRPORT ROAD | PO BOX 760 | MINERAL WELLS TX 76067-0760 |
| 2 D 532 | ALLEGRO MICROSYSTEMS INC SEMICONDUCTOR DIVISION | 70 PEMBROKE ROAD | CONCORD NH 03301 |
| 24165 | SPRAGUE ELECTRIC CO | 267 LOWELL ROAD | HUDSON NH 03051 |
| 24355 | ANALOG DEVICES INC | RT 1 INDUSTRIAL PK PO BOX 9106 | NORWOOD MA 02062 |
| 24546 | CORNING GLASS WORKS | 550 HIGH ST | BRADFORD PA 16701-3737 |
| 24931 | SPECIALTY CONNECTOR CO INC | 2100 EARLYWOOD DR PO BOX 547 | FRANKLIN IN 46131 |
| 27014 | NATIONAL SEMICONDUCTOR CORP | 2900 SEMICONDUCTOR DR | SANTA CLARA CA 95051-0606 |
| 27264 | MOLEX INC | 2222 WELLINGTON COURT | LISLE IL 60532-1613 |
| 31433 | KEMET ELECTRONICS CORP NATIONAL SALES HEADQUARTERS | PO BOX 5928 | GREENVILLE SC 29606 |
| 31918 | ITT SCHADOW INC | 8081 WALLACE RD | EDEN PRAIRIE MN 55344-2224 |
| 32997 | BOURNS INC TRIMPOT DIV | 1200 COLUMBIA AVE | RIVERSIDE CA 92507-2114 |
| 34335 | ADVANCED MICRO DEVICES | 901 THOMPSON PL | SUNNYVALE CA 94086-4518 |
| 34371 | HARRIS CORP HARRIS SEMICONDUCTOR PRODUCTS GROUP | 200 PALM BAY BLVD PO BOX 883 | MELBOURNE FL 32919 |
| 50088 | SGS-THOMSON MICROELECTRONICS INC | 1310 ELECTRONICS DR | CARROLLTON TX 75006-6905 |

## CROSS INDEX - MFR. CODE NUMBER TO MANUFACTURER

| Mir. Code | Manufacturer | Address | City, State, Zip Code |
| :---: | :---: | :---: | :---: |
| 50101 | FREQUENCY SOURCES INC SEMICONDUCTOR DIV SUB OF LORAL CORP | 16 MAPLE RD | CHELMSFORD MA 01824-3737 |
| 50434 | HEWLETT-PACKARD CO OPTOELECTRONICS DIV | 370 W TRIMBLE RD | SAN JOSE CA 95131 |
| 51406 | MURATA ERIE NORTH AMERICA INC HEADQUARTERS AND GEORGIA OPERATIONS | 2200 LAKE PARK DR | SMYRNA GA 30080 |
| 51642 | CENTRE ENGINEERING INC | 2820 E COLLEGE AVE | STATE COLLEGE PA 16801-7515 |
| 52763 | STETCO INC | 3344 SCHIERHORN | FRANKLIN PARK IL 60131 |
| 52769 | SPRAGUE-GOODMAN ELECTRONICS INC | 134 FULTON AVE | GARDEN CITY PARK NY 11040-5352 |
| 53387 | MINNESOTA MINING MFG CO | PO BOX 2963 | AUSTIN TX 78769-2963 |
| 54583 | TDK ELECTRONICS CORP | 12 HARBOR PARK DR | PORT WASHINGTON NY 11550 |
| 54937 | DEYOUNG MANUFACTURING INC | 12920 NE 125TH WAY | KIRKLAND WA 98034-7716 |
| 55680 | NICHICON /AMERICA/ CORP | 927 E STATE PKY | SCHAUMBURG IL 60195-4526 |
| 56845 | DALE ELECTRONICS INC | $\begin{aligned} & 2300 \text { RIVERSIDE BLVD } \\ & \text { PO BOX } 74 \end{aligned}$ | NORFOLK NE 68701-2242 |
| 57668 | ROHM CORP | 8 WHATNEY <br> PO BOX 19515 | IRVINE CA 92713 |
| 59640 | SUPERTEX INC | 1225 BORDER BY DESCOX 19515 | SUNNYVALE CA 94086 |
| 61058 | MATSUSHITA ELECTRIC CORP OF AMERICA PANASONIC INDUSTRIAL CO DIV | ONE PANASONIC WAY PO BOX 1502 | SECAUCUS NJ 07094-2917 |
| 61529 | AROMAT CORP | 250 SHEFFIELD ST | MOUNTAINSIDE NJ 07092-2303 |
| 65786 | CYPRESS SEMICONDUCTOR CORP | 3901 N 1ST ST | SAN JOSE CA 95134-1506 |
| 66302 | VLSI TECHNOLOGY INC | 1109 MCKAY DR | SAN JOSE CA 95131-1706 |
| 71400 | BUSSMANN <br> DIV OF COOPER INDUSTRIES INC | 114 OLD STATE RD PO BOX 14460 | ST LOUIS MO 63178 |
| 71590 | CRL COMPONENTS INC | HWY 20 W PO BOX 858 | FORT DODGE IA 50501 |
| 75042 | IRC ELECTRONIC COMPONENTS PHILADELPHIA DIV TRW FIXED RESISTORS | 401 N BROAD ST | PHILADELPHIA PA 19108-1001 |
| 75498 | MULTICOMP INC | 3005 SW 154TH TERRACE \#3 | BEAVERTON OR 97006 |
| 80009 | TEKTRONIX INC | 14150 SW KARL BRAUN DR PO BOX 500 | BEAVERTON OR 97077-0001 |
| 84411 | AMERICAN SHIZUKI CORP OGALLALA OPERATIONS | 301 WEST O ST | OGALLALA NE 69153-1844 |
| 91637 | DALE ELECTRONICS INC | $\begin{aligned} & 2064 \text { 12TH AVE } \\ & \text { PO BOX } 609 \end{aligned}$ | COLUMBUS NE 68601-3632 |


| Component Number | Tektronix Part No. | Serlal Effectlve | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A8 | 670-9783-01 |  |  | CIRCUIT BD ASSY:CRT CONTROL | 80009 | 670978301 |
| A9 | 671-0978-00 | B700100 | B710099 | CIRCUIT BD ASSY:2 MILLIVOLT BANDWIDTH (2246 1Y) | 80009 | 671097800 |
| A9 | 671-0978-00 | B100100 | B110099 | CIRCUIT BD ASSY:2 MILLIVOLT BANDWIDTH (2246 MOD A,2Y) | 80009 | 671097800 |
| A10 | 671-0875-00 | B700100 | B710099 | CIRCUIT BD ASSY:MAIN (2246 1Y) | 80009 | 671087500 |
| A10 | 671-0875-01 | B710100 | B710629 | CIRCUIT BD ASSY:MAIN (2246 1Y) | 80009 | 671087501 |
| A10 | 671-0875-02 | B710630 | B714999 | CIRCUIT BD ASSY:A10 MAIN (2246 1Y) | 80009 | 671087502 |
| A10 | 671-0875-03 | B715000 |  | CIRCUIT BD ASSY:AYO MAIN (2246 1Y) | 80009 | 671087503 |
| A10 | 671-0875-00 | B100100 | B110099 | CIRCUIT BD ASSY:MAIN (2246 MOD A,2Y) | 80009 | 671087500 |
| A10 | 671-0875-01 | B110100 | B111185 | CIRCUIT BD ASSY:MAIN ( 2246 MOD A,2Y) | 80009 | 671087501 |
| A10 | 671-0875-02 | B111186 | 8119999 | CIRCUIT BD ASSY:A10 MAIN ( 2246 MOD A,2Y) | 80009 | 671087502 |
| A10 | 671-0875-03 | B120000 |  | CIRCUIT BD ASSY:A10 MAIN (2246 MOD A,2Y) | 80009 | 671087503 |
| A12 | 670-9402-01 |  |  | CIRCUIT BD ASSY:POTENTIOMETER | 80009 | 670940201 |
| A14 | 670-9399-01 |  |  | CIRCUIT BD ASSY:SWITCH | 80009 | 670939901 |
| A16 | 672-1306-00 | B700100 | B711092 | CIRCUIT BD ASSY:PROCESSOR (2246 1Y) <br> (WITH U2519 FIRMWARE) | 80009 | 672130600 |
| A16 | 672-1306-01 | B711093 |  | CIRCUIT BD ASSY:PROCESSOR,671-1086-XX WIRED <br> (2246 1Y) <br> (WITH U2519 FIRMWARE) | 80009 | 672130601 |
| A16 | 672-0254-00 | B100100 | B113797 | CIRCUIT BD ASSY: PROCESSOR (2246 MOD A,2Y) <br> (WITH U2519 FIRMWARE) | 80009 | 672025400 |
| A16 | 672-0254-01 | B113798 |  | CIRCUIT BD ASSY:PROCESSOR,671-1239-XX WIRED <br> ( 2246 MOD A,2Y) <br> (WITH U2519 FIRMWARE) | 80009 | 672025401 |
| A16A1 | 671-1086-00 | 8700100 | B711092 | CIRCUIT BD ASSY:PROCESSOR (2246 1Y) <br> (WITHOUT U2519 FIRMWARE) | 80009 | 671108600 |
| A16A1 | 671-1086-01 | B711093 |  | CIRCUIT BD ASSY:A16 PROCESSOR (2246 1Y) <br> (WITHOUT U2519 FIRMWARE) | 80009 | 671108601 |
| A16A1 | 671-0947-00 | B100100 | B101981 | CIRCUIT BD ASSY:PROCESSOR (2246 MOD A,2Y) <br> (WITHOUT U2519 FIRMWARE) | 80009 | 671094700 |
| A16A1 | 671-1239-00 | B101982 | B113797 | CIRCUIT BD ASSY:PROCESSOR ( 2246 MOD A, 2 ) <br> (WITHOUT U2519 FIRMWARE) | 80009 | 671123900 |
| A16A1 | 671-1239-01 | B113798 |  | CIRCUIT BD ASSY PROCESSOR ( 2246 MOD A, 2 Y) <br> (WITHOUT U2519 FIRMWARE) | 80009 | 671123901 |
| A18 | 670-9398-04 |  |  | CIRCUIT BD ASSY:LV POWER SUPPLY | 80009 | 670939804 |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A8 | 670-9783-01 |  | CIRCUIT BD ASSY:CRT CONTROL | 80009 | 670978301 |
| A8R901 | 311-2344-00 |  | RES,VAR,NONWW:CKT BD,4.7K OHM,20\%,1.25WLINE AR | 71590 | BA17140001 |
| A8R902 | 311-2344-00 |  | RES,VAR,NONWW:CKT BD,4.7K OHM,20\%,1.25WLINE AR | 71590 | BA17140001 |
| A8R903 | 311-2344-00 |  | RES,VAR,NONWW:CKT BD,4.7K OHM,20\%,1.25WLINE AR | 71590 | BA17140001 |
| A8R905 | 311-2344-00 |  | RES,VAR,NONWW:CKT BD,4.7K OHM,20\%,1.25WLINE AR | 71590 | BA17140001 |
| A8W900 | 131-4038-00 |  | CONN,HDR::PCB,;MALE,RTANG, $1 \times 8,0.1$ CTR,0.2 95 MLG X 0.140 TALL.TIN | 00779 | 640453-8 |


| Component Number | Tektronlx Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A9 | 671-0978-00 | B700100 | B710099 | CIRCUTT BD ASSY:2 MILLIVOLT BANDWIDTH (2246 IY) | 80009 | 671097800 |
| A9 | 671-0978-00 | B100100 | B110099 | CIRCUIT BD ASSY:2 MILLIVOLT BANDWIDTH (2246 MOD A,2Y) | 80009 | 671097800 |
| A9C185 | 281-0810-00 | B100100 | B110099 | CAP,FXD,CER DI:5.6PF, +/-0.5PF,100VTUBULAR,MI (2246 MOD A,2Y) | 04222 | SA102A5R6DAA |
| A9C185 | 281-0810-00 | B700100 | B710099 | CAP,FXD,CER DI:5.6PF $+/-0.5 \mathrm{PF}, 100 \mathrm{VTUBULAR,MI}$ (2246 1Y) | 04222 | SA102A5R6DAA |
| A9C186 | 281-0810-00 | B100100 | B110099 | CAP,FXD,CER DI:5.6PF, $+/-0.5$ PF, $100 \mathrm{VTUBULAR,MI}$ (2246 MOD A, 2 Y) | 04222 | SA102A5R6DAA |
| A9C186 | 281-0810-00 | B700100 | B710099 | CAP,FXD,CER DI:5.6PF $+/-0.5 \mathrm{PF}, 100 \mathrm{VTUBULAR,MI}$ (2246 1Y) | 04222 | SA102A5R6DAA |
| A9C188 | 281-0909-00 | B100100 | B110099 | CAP,FXD,CER DI:0.022UF, $20 \%, 50 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A9C188 | 281-0909-00 | B700100 | B710099 | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A9Q181 | 151-0712-00 | B100100 | B110099 | TRANSISTOR,SIG:BIPOLAR,PNP;20V,50MA,600MHZ AMPLIFIER <br> (2246 MOD A,2Y) | 04713 | MPSH81 |
| A9Q181 | 151-0712-00 | B700100 | B710099 | TRANSISTOR,SIG:BIPOLAR,PNP:2OV,50MA,600MHZ AMPLIFIER $(2246 \mathrm{iV})$ | 04713 | MPSH81 |
| A9Q182 | 151-0712-00 | B100100 | B110099 | TRANSISTOR,SIG:BIPOLAR,PNP;20V,50MA,600MHZ AMPLIFIER <br> (2246 MOD A.2Y) | 04713 | MPSH81 |
| A9Q182 | 151-0712-00 | B700100 | B710099 | TRANSISTOR,SIG:BIPOLAR,PNP;20V,50MA,600MHZ AMPLIFIER (2246 iY) | 04713 | MPSH81 |
| A9R185 | 313-1512-00 | B100100 | B110099 | RES, $F$ XD,FILM:5.1K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-51000J |
| A9R185 | 313-1512-00 | B700100 | B710099 | RES,FXD,FILM:5.1K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-51000J |
| A9R188 | 313-1512-00 | B100100 | B110099 | RES,FXD,FILM:5.1K OHM,5\%,0.2W (2246 MOD A.2Y) | 91637 | CCF50-2-51000J |
| A9R188 | 313-1512-00 | 8700100 | B710099 | RES,FXD,FILM:5.1K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-51000.J |
| A9R189 | 313-1271-00 | B100100 | B110099 | RES,FXD,FILM:270 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-270ROJ |
| A9R189 | 313-1271-00 | B700100 | B710099 | RES,FXD,FILM:270 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-270ROJ |
| A9VR101 | 152-0278-00 | B100100 | B110099 | DIODE,ZENER:,;3V,5\%,0.4W (2246 MOD A, 2 Y ) | 04713 | 1N4372ARL |
| A9VR101 | 152-0278-00 | B700100 | B710099 | $\begin{aligned} & \text { DIODE,ZENER:; } 3 V, 5 \%, 0.4 \mathrm{~W} \\ & (2246 \text { iY) } \end{aligned}$ | 04713 | 1N4372ARL |
| A9W105 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A9W105 | 131-0566-00 | B700100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A9W106 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L (2246 MOD A,2Y) | 24546 | OMA 07 |
| A9W106 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A9W107 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225 L (2246 MOD A,2Y) | 24546 | OMAA 07 |


| Component Number | Tektronix <br> Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A9W107 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225 L (2246 1Y) | 24546 | OMA 07 |
| A9W108 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L (2246 MOD A,2Y) | 24546 | OMA 07 |
| A9W108 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L (2246 1Y) | 24546 | OMA 07 |
| A9W212 | 195-0970-00 | B100100 | B110099 | LEAD,ELECTRICAL:26 AWG,3.0 L,0-N (2246 MOD A,2Y) | 80009 | 195097000 |
| A9W212 | 195-0970-00 | B700100 | B710099 | LEAD,ELECTRICAL:26 AWG,3.0 L,0-N (2246 1Y) | 80009 | 195097000 |
| A9W222 | 195-0970-00 | B100100 | B110099 | LEAD,ELECTRICAL:26 AWG,3.0 L,0-N (2246 MOD A,2Y) | 80009 | 195097000 |
| A9W222 | 195-0970-00 | B700100 | B710099 | LEAD,ELECTRICAL:26 AWG,3.0 L,0-N (2246 1Y) | 80009 | 195097000 |

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| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10 | 671-0875-00 | B700100 | B710099 | CIRCUIT BD ASSY:MAIN (2246 1Y) | 80009 | 671087500 |
| A10 | 671-0875-01 | B710100 | B710629 | CIRCUIT BD ASSY:MAIN (2246 1Y) | 80009 | 671087501 |
| A10 | 671-0875-02 | B710630 | B714999 | CIRCUIT BD ASSY:A10 MAIN (2245 1Y) | 80009 | 671087502 |
| A10 | 671-0875-03 | B715000 |  | CIRCUIT BD ASSY:A10 MAIN (2246 1Y) | 80009 | 671087503 |
| A10 | 671-0875-00 | 8100100 | B110099 | CIRCUIT BD ASSY:MAIN (2246 MOD A,2Y) | 80009 | 671087500 |
| A10 | 671-0875-01 | B110100 | B111185 | CIRCUIT BD ASSY:MAIN (2246 MOD A.2Y) | 80009 | 671087501 |
| A10 | 671-0875-02 | B111186 | 8119999 | CIRCUIT BD ASSY:A10 MAIN (2246 MOD A.2Y) | 80009 | 671087502 |
| A10 | 671-0875-03 | B120000 |  | CIRCUIT BD ASSY:A10 MAIN (2246 MOD A.2Y) | 80009 | 671087503 |
| A10AT117 | 307-2135-00 | B100100 | 8101582 | RES NTWK,FXD,FI:ATTENUATOR DIP PKG (2246 MOD A,2Y) | 80009 | 307213500 |
| A10AT117 | 307-2135-01 | B101583 | B113045 | ATTENUATOR:1M OHM ATTENUATOR NETWORK (2246 MOD A, 2Y) | 80009 | 307213501 |
| A10AT117 | 307-2135-02 | B113046 | B114266 | ATTENUATOR:1M OHM ATTENUATOR NETWORK (2246 MOD A,2Y) | 80009 | 307213502 |
| A10AT117 | 307-2135-03 | B114267 |  | ATTENUATOR:1M OHM ATTENUATOR NETWORK (2246 MOD A,2Y) | 80009 | 307213503 |
| A10AT117 | 307-2135-00 | B700100 | B700493 | RES NTWK,FXD,FI:ATTENUATOR DIP PKG (2246 1Y) | 80009 | 307213500 |
| A10AT117 | 307-2135-01 | B700494 | B711016 | ATTENUATOR:1M OHM ATTENUATOR NETWORK (2246 1Y) | 80009 | 307213501 |
| A10AT117 | 307-2135-02 | B711017 | B711094 | ATTENUATOR:1M OHM ATTENUATOR NETWORK (2246 1Y) | 80009 | 307213502 |
| A10AT117 | 307-2135-03 | B711095 |  | ATTENUATOR:1M OHM ATTENUATOR NETWORK (2246 1Y) | 80009 | 307213503 |
| A10AT127 | 307-2135-00 | B100100 | B101582 | RES NTWK,FXD,FI:ATTENUATOR DIP PKG (2246 MOD A,2Y) | 80009 | 307213500 |
| A10AT127 | 307-2135-01 | B101583 | B113045 | ATTENUATOR:1M OHM ATTENUATOR NETWORK (2246 MOD A,2Y) | 80009 | 307213501 |
| A10AT127 | 307-2135-02 | B113046 | B114266 | ATTENUATOR:1M OHM ATTENUATOR NETWORK ( 2246 MOD A,2Y) | 80009 | 307213502 |
| A10AT127 | 307-2135-03 | B114267 |  | ATTENUATOR:1M OHM ATTENUATOR NETWORK (2246 MOD A,2Y) | 80009 | 307213503 |
| A10AT127 | 307-2135-00 | 8700100 | B700493 | RES NTWK,FXD,FI:ATTENUATOR DIP PKG (2246 1Y) | 80009 | 307213500 |
| A10AT127 | 307-2135-01 | B700494 | B711016 | ATTENUATOR:1M OHM ATTENUATOR NETWORK (2246 1Y) | 80009 | 307213501 |
| A10AT127 | 307-2135-02 | 8711017 | B711094 | ATTENUATOR:1M OHM ATTENUATOR NETWORK $(2246 \text { 1Y) }$ | 80009 | 307213502 |
| A10AT127 | 307-2135-03 | B711095 |  | ATTENUATOR:1M OHM ATTENUATOR NETWORK $(2246 \mathrm{iY})$ | 80009 | 307213503 |
| A10C101 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C102 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C103 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C104 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C105 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C106 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronlx Part No. | Serial No. Effective Dscont | Name \& Description | Mir. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10C107 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C108 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C111 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C112 | 283-0414-01 |  | CAP,FXD,CER DI:0.022UF,20\%,500V | 04222 | SR217C223MAATR |
| A10C113 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C114 | 281-0214-00 |  | CAP,VAR,CER DI:0.6-3PF,400V | 52763 | 313613-140 |
| A10C121 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C122 | 283-0414-01 |  | CAP,FXD,CER DI:0.022UF,20\%,500V | 04222 | SR217C223MAATR |
| A10C123 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C124 | 281-0214-00 |  | CAP,VAR,CER DI:0.6-3PF,400V | 52763 | 313613-140 |
| A10C125 | 281-0770-00 |  | CAP,FXD,CER DI: $1000 \mathrm{PF}, 20 \%, 100 \mathrm{~V}$ | 04222 | SA101C102MAA |
| A10C126 | 281-0770-00 |  | CAP,FXD,CER DI:1000PF,20\%,100V | 04222 | SA101C102MAA |
| A10C131 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C132 | 281-0938-00 |  | CAP,FXD,CER DI:20PF,2\%,500V | 04222 | MA107A200GAA |
| A10C133 | 281-0799-00 |  | CAP,FXD,CER DI:62PF,2\%,100V | 04222 | SA101A620GAA |
| A10C134 | 281-0306-00 |  | CAP,VAR,CER DI:3.3-20PF | 52769 | GKU 18000 |
| A10C135 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A100136 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C137 | 281-0797-00 |  | CAP,FXD,CER DI:15PF, $10 \%, 100 \mathrm{~V}$, TUBULAR,MI | 04222 | SA106A150KAA |
| A10C138 | 281-0315-00 |  | CAPVAR,CER DI:2.8-10PF | 52769 | GKU 10000 |
| A10C139 | 281-0797-00 |  | CAP,FXD,CER DI:15PF, $10 \%, 100 \mathrm{~V}$, ,TUBULAR,M1 | 04222 | SA106A150KAA |
| A10C140 | 290-0974-01 |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C151 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C152 | 281-0938-00 |  | CAP,FXD,CER DI:20PF,2\%,500V | 04222 | MA107A200GAA |
| A10C153 | 281-0799-00 |  | CAP,FXD,CER DI:62PF,2\%,100V | 04222 | SA101A620GAA |
| A10C154 | 281-0306-00 |  | CAP,VAR,CER DI:3.3-20PF | 52769 | GKU 18000 |
| A10C155 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C156 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C157 | 281-0797-00 |  | CAP,FXD,CER DI:15PF, $10 \%, 100 \mathrm{~V}$, TUBULAR,MI | 04222 | SA106A150KAA |
| A10C158 | 281-0315-00 |  | CAP,VAR,CER DI:2.8-10PF | 52769 | GKU 10000 |
| A10C159 | 281-0797-00 |  | CAP,FXD,CER DI:15PF, $10 \%, 100 \mathrm{~V}$, ,TUBULAR,MI | 04222 | SA106A150KAA |
| A10C171 | 290-0974-01 |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C172 | 281-0909-00 |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C173 | 281-0772-00 |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A10C180 | 290-0944-01 |  | CAP,FXD,ELCTLT:220UF,20\%,10V | 1W344 | UMC10VB221M8X11 |
| A10C181 | 290-0944-01 |  | CAP,FXD,ELCTLT:220UF,20\%,10V | 1W344 | UMC10VB221M8×11 |
| A10C185 | 281-0810-00 | B110100 | CAP,FXD,CER DI:5.6PF, $+1-0.5$ PF, 100 VTUBULAR,MI ( 2246 MOD A,2Y) | 04222 | SA102A5R6DAA |
| A10C185 | 281-0810-00 | B710100 | CAP,FXD,CER DI:5.6PF, $+1-0.5$ PF, 100 VTUBULAR,MI (2246 1Y) | 04222 | SA102A5R6DAA |
| A10C186 | 281-0810-00 | B110100 | CAP,FXD,CER DI:5.6PF, $+/-0.5 \mathrm{PF}, 100 \mathrm{VTUBULAR}, \mathrm{MI}$ (2246 MOD A,2Y) | 04222 | SA102A5R6DAA |
| A10C186 | 281-0810-00 | B710100 | CAP,FXD,CER DI:5.6PF, $+/-0.5$ PF, $100 \mathrm{VTUBULAR,M!}$ (2246 1Y) | 04222 | SA102A5R6DAA |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C188 | 281-0909-00 | B110100 |  | CAPPFXD,CER DI:O.022UF,20\%,50V (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C188 | 281-0909-00 | B710100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C190 | 281-0797-00 |  |  | CAP,FXD,CER DI:15PF, $10 \%, 100 \mathrm{~V}$, ,TUBULAR,MI | 04222 | SA106A150KAA |
| A10C191 | 281-0797-00 |  |  | CAP,FXD,CER DI:15PF, 10\%,100V,TUBULAR,MI | 04222 | SA106A150KAA |
| A10C201 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5×11 |
| A10C202 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C203 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5×11 |
| A10C204 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C205 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C206 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C210 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C211 | 281-0759-00 |  |  | CAP,FXD,CER DI:22PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101A220KAA |
| A10C212 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C213 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C214 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C215 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC50VB10RM5X11 |
| A10C216 | 281-0909-00 | B100100 | B113468 | CAP,FXD,CER DI:0.022UF, $20 \%, 50 \mathrm{~V}$ (2246 MOD A, 2 Y) | 04222 | SA105C223MAA |
| A10C216 | 281-0775-01 | B113469 |  | CAP.FXD,CER DI:0.1UF,20\%,50V ( 2246 MOD A,2Y) | 04222 | SA105E104MAA |
| A10C216 | 281-0909-00 | B700100 | B711092 | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C216 | 281-0775-01 | B711093 |  | CAP,FXD,CER DI:0.1UF,20\%,50V (2246 1Y) | 04222 | SA105E104MAA |
| A10C217 | 281-0909-00 | B100100 | B113468 | CAP.FXD,CER DI:0.022UF,20\%,50V (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C217 | 281-0775-01 | B113469 |  | CAP,FXD,CER DI:0.1UF,20\%,50V ( 2246 MOD A,2Y) | 04222 | SA105E104MAA |
| A10C217 | 281-0909-00 | B700100 | B711092 | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C217 | 281-0775-01 | B711093 |  | CAP,FXD,CER DI:0.1UF,20\%,50V (2246 1Y) | 04222 | SA105E104MAA |
| A10C218 | 281-0775-01 |  |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A10C219 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C220 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C221 | 281-0759-00 |  |  | CAP,FXD,CER DI:22PF, $10 \%, 100 \mathrm{~V}$ | 04222 | Salotaz20kaA |
| A10C222 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C223 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C224 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C225 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C228 | 281-0775-01 |  |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A10C229 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C232 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |


| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C233 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C234 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C235 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C238 | 281-0775-01 |  |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A10C239 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C242 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C243 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C244 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C245 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C248 | 281-0775-01 |  |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A10C249 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C258 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5×11 |
| A10C265 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C268 | 281-0770-00 |  |  | CAP,FXD,CER DI:1000PF,20\%,100V | 04222 | SA101C102MAA |
| A 10 C 271 | 281-0798-00 |  |  | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ | TK1743 | CGB510GEN |
| A10C272 | 281-0819-00 |  |  | CAP,FXD,CER DI:33 PF,5\%,50V | 04222 | SA102A330JAA |
| A10C273 | 281-0307-00 |  |  | CAP,VAR,CER DI:3-8-25PF | 52769 | GKU 25000 |
| A10C274 | 281-0305-00 | B100100 | B101874 | CAPVAR,CER DI:1.5-4.0PF (2246 MOD A,2Y) | 52769 | GKU 4R000 |
| A10C274 | 281-0305-00 | B700100 | B700723 | CAPVAR,CER DI:1.5-4.0PF (2246 1Y) | 52769 | GKU 4R000 |
| A100275 | 281-0872-00 | B100100 | B101874 | CAP,FXD,CER DI:91PF,5\%,100V (2246 MOD A,2Y) | 04222 | MC101A910J |
| A10C275 | 281-0776-00 | B101875 |  | CAP,FXD,CER DI:120PF,5\%,100V (2246 MOD A, 2 Y) | 04222 | SA102A121JAA |
| A10C275 | 281-0872-00 | B700100 | B700723 | CAP,FXD,CER DI:91PF,5\%,100V (2246 1Y) | 04222 | MC101A910J |
| A10C275 | 281-0776-00 | B700724 |  | CAP,FXD,CER DI:120PF,5\%,100V (2246 1Y) | 04222 | SA102A121JAA |
| A10C282 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C283 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C297 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C298 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C301 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A 10 C 302 | 281-0770-00 |  |  | CAP,FXD,CER DI:1000PF,20\%,100V | 04222 | SA101C102MAA |
| A10C303 | 290-0183-00 |  |  | CAP,FXD,ELCTLT:1UF,10\%,35V | 31433 | T322B105K035AS |
| A10C304 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C305 | 290-0183-00 |  |  | CAP,FXD,ELCTLT:1UF,10\%,35V | 31433 | T322B105K035AS |
| A100306 | 290-0183-00 |  |  | CAP,FXD,ELCTLT:1UF,10\%,35V | 31433 | T322B105K035AS |
| A100.307 | 295-0198-00 |  |  | CAP SET,MATCHED:(1),10.0UF, $1.5 \%, 25 \mathrm{~V} /(1) 0.1 \mathrm{U}$ F, $1.5 \%, 35 \mathrm{~V} /(1) 0.0099 \mathrm{UF}, 1.5 \%, 50 \mathrm{~V}$ (LOCATIONS A,B,C) | 75498 | ORDER BY DESC |
| A10C308 | 281-0909-00 |  |  | CAP,FXD.CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C309 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C310 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C311 | 281-0798-00 |  |  | CAP,FXD,CER DI: 51 PF, $1 \%, 100 \mathrm{~V}$ | TK1743 | CGB510GEN |
| A10C312 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronlx <br> Part No. | Serial Effective | No. Dscont | Name \& Description | Mir. Code | Mir. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C313 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C314 | 281-0307-00 |  |  | CAP,VAR,CER DI:3-8-25PF | 52769 | GKU 25000 |
| A10C315 | 281-0798-00 |  |  | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ | TK1743 | CGB510GEN |
| A10C316 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C317 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C318 | 281-0909-00 |  |  | CAPPKXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C319 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C320 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C321 | 281-0798-00 |  |  | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ | TK1743 | CGB510GEN |
| A10C322 | 281-0909-00 | B110100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C322 | 281-0909-00 | B710100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C326 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C329 | 281-0307-00 |  |  | CAP,VAR,CER DI:3-8-25PF | 52769 | GKU 25000 |
| A10С330 | 281-0799-00 |  |  | CAP,FXD,CER DI:62PF,2\%,100V | 04222 | SA101A620GAA |
| A10C337 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10С338 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10С339 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C351 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C421 | 281-0775-01 |  |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A10C422 | 281-0861-00 |  |  | CAP,FXD,CER DI:270PF,5\%,50V | 04222 | SA101A271JAA |
| A10C423 | 281-0864-00 |  |  | CAP,FXD, CER DI:430PF,5\%,100V | 04222 | SA101A431JAA |
| A10C424 | 290-0183-00 |  |  | CAP,FXD,ELCTLT:1UF,10\%,35V | 31433 | T322B105K035AS |
| A10C425 | 281-0820-00 | B700100 | B710290 | CAP,FXD,CER DI:680 PF, $10 \%, 50 \mathrm{~V}$ (2246 1Y) | 04222 | SA101C681KAA |
| A10C425 | 281-0861-00 | B710291 | B714999 | CAPPKXD,CER DI:270PF,5\%,50V (2246 1Y) | 04222 | SA101A271JAA |
| A10C425 | 283-0196-00 | B715000 |  | CAP,FXD,CER DI:270PF, $10 \%, 50 \mathrm{~V}$ (2246 1Y) | 16299 | SR155C271KAA |
| A10C425 | 281-0820-00 | B100100 | B110637 | CAP,FXD,CER DI:680 PF, $10 \%, 50 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA101C681KAA |
| A10C425 | 281-0861-00 | B110638 | B119999 | CAP,FXD,CER DI:270PF,5\%,50V (2246 MOD A,2Y) | 04222 | SA101A271JAA |
| A10C425 | 283-0196-00 | B120000 |  | CAP,FXD,CER DI:270PF, $10 \%, 50 \mathrm{~V}$ (2246 MOD A, 2 Y) | 16299 | SR155C271KAA |
| A10C426 | 281-0864-00 |  |  | CAP,FXD,CER DI:430PF,5\%,100V | 04222 | SA101A431JAA |
| A10C432 | 281-0767-00 |  |  | CAP,FXD,CER DI:330PF,20\%,100V | 04222 | SA102C331MAA |
| A10C442 | 281-0909-00 | B110100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C442 | 281-0909-00 | B710100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C444 | 281-0765-00 |  |  | CAP,FXD,CER DI:100PF,5\%,100V | 04222 | SA102A101JAA |
| A10C445 | 290-0183-00 |  |  | CAP,FXD,ELCTLT:1UF,10\%,35V | 31433 | T322B105K035AS |
| A10C447 | 281-0765-00 |  |  | CAP,FXD,CER DI:100PF,5\%,100V | 04222 | SA102A101JAA |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C451 | 281-0775-01 |  |  | CAP,FXD,CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA105E104MAA |
| A10C452 | 281-0861-00 |  |  | CAP,FXD,CER DI:270PF,5\%,50V | 04222 | SA101A271JAA |
| A10C453 | 281-0864-00 |  |  | CAP,FXD,CER DI:430PF,5\%,100V | 04222 | SA101A431JAA |
| A10C454 | 290-0183-00 |  |  | CAP,FXD,ELCTLT:1UF,10\%,35V | 31433 | T322B105K035AS |
| A10C455 | 281-0820-00 | B700100 | B710290 | CAP,FXD,CER DI: 680 PF,10\%,50V (2246 1Y) | 04222 | SA101C681KAA |
| A10C455 | 281-0861-00 | B710291 |  | CAP,FXD,CER DI:270PF,5\%,50V (2246 1Y) | 04222 | SA101A271JAA |
| A10C455 | 281-0820-00 | B100100 | B110637 | CAP,FXD,CER DI:680 PF, $10 \%, 50 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA101C681KAA |
| A10C455 | 281-0861-00 | B110638 |  | CAP,FXD,CER DI:270PF,5\%,50V ( 2246 MOD A,2Y) | 04222 | SA101A271JAA |
| A10C462 | 281-0864-00 |  |  | CAP,FXD,CER DI:430PF,5\%,100V | 04222 | SA101A431JAA |
| A10C463 | 281-0813-00 |  |  | CAP,FXD,CER DI: $0.047 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA105E473MAA |
| A10C474 | 281-0776-00 |  |  | CAP,FXD,CER DI:120PF,5\%,100V | 04222 | SA102A121JAA |
| A10C475 | 290-0183-00 |  |  | CAP,FXD,ELCTLT:1UF,10\%,35V | 31433 | T322B105K035AS |
| A10C476 | 281-0819-00 | B110100 |  | CAP,FXD,CER DI:33 PF,5\%,50V ( 2246 MOD A, 2 Y) | 04222 | SA102A330JAA |
| A10C476 | 281-0819-00 | B710100 |  | CAP,FXD,CER DI:33 PF,5\%,50V (2246 1Y) | 04222 | SA102A330JAA |
| A10C477 | 281-0765-00 | B100100 | B110099 | CAP,FXD,CER DI:100PF,5\%,100V ( 2246 MOD A,2Y) | 04222 | SA102A101JAA |
| A10C477 | 281-0872-00 | B110100 |  | CAPFXX, CER DI:91PF, $5 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | MC101A910J |
| A10C477 | 281-0765-00 | B700100 | B710099 | CAP,FXD,CER DI:100PF,5\%,100V (2246 1Y) | 04222 | SA102A101JAA |
| A10C477 | 281-0872-00 | B710100 |  | CAP,FXD,CER DI:91PF,5\%,100V (2246 1Y) | 04222 | MC101A910J |
| A10C478 | 281-0864-00 | B110100 |  | CAP,FXD,CER DI:430PF,5\%,100V (2246 MOD A, $2 Y$ ) | 04222 | SA101A431JAA |
| A10C478 | 281-0864-00 | B710100 |  | CAP,FXD,CER DI:430PF,5\%,100V (2246 1Y) | 04222 | SA101A431JAA |
| A10C480 | 281-0909-00 | B110100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 MOD A.2Y) | 04222 | SA105C223MAA |
| A10C480 | 281-0909-00 | B710100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C481 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C482 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C483 | 281-0820-00 |  |  | CAP,FXD,CER DI:680 PF,10\%,50V | 04222 | SA101C681KAA |
| A10C484 | 281-0765-00 | B100100 | B110099 | CAP,FXD,CER DI: $100 \mathrm{PF}, 5 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA102A101JAA |
| A10C484 | 281-0861-00 | B110100 |  | CAP,FXD,CER DI:270PF,5\%,50V (2246 MOD A,2Y) | 04222 | SA101A271JAA |
| A10C484 | 281-0765-00 | B700100 | B710099 | CAP,FXD,CER DI:100PF,5\%,100V (2246 1Y) | 04222 | SA102A101JAA |
| A10C484 | 281-0861-00 | B710100 |  | CAP,FXD,CER DI:270PF,5\%,50V (2246 1Y) | 04222 | SA101A271JAA |
| A10C485 | 281-0765-00 | B100100 | B110099 | CAP,FXD,CER DI: $100 \mathrm{PF}, 5 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA102A101JAA |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mir. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C485 | 281-0861-00 | 8110100 | B119999 | $\begin{aligned} & \text { CAP,FXD,CER DI:270PF,5\%,50V } \\ & \text { (2246 MOD A,2Y) } \end{aligned}$ | 04222 | SA101A271JAA |
| A10C485 | 283-0196-00 | B120000 |  | CAP,FXD,CER DI:270PF, 10\%,50V (2246 MOD A,2Y) | 16299 | SR155C271KAA |
| A10C485 | 281-0765-00 | B700100 | B710099 | CAP,FXD,CER DI:100PF,5\%,100V (2246 1Y) | 04222 | SA102A101JAA |
| A10C485 | 281-0861-00 | B710100 | B714999 | CAP,FXD,CER DI:270PF,5\%,50V (2246 1Y) | 04222 | SA101A271JAA |
| A10C485 | 283-0196-00 | B715000 |  | CAP,FXD,CER DI:270PF, $10 \%, 50 \mathrm{~V}$ (2246 1Y) | 16299 | SR155C271KAA |
| A10C486 | 281-0765-00 | B100100 | 8110099 | CAP,FXD,CER DI:100PF, $5 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA102A101JAA |
| A10C486 | 281-0765-00 | B700100 | 8710099 | CAP,FXD,CER DI:100PF,5\%,100V (2246 1Y) | 04222 | SA102A101JAA |
| A10C487 | 281-0765-00 |  |  | CAP,FXD,CER DI:100PF,5\%,100V | 04222 | SA102A101JAA |
| A10C488 | 281-0765-00 | B100100 | B110099 | CAP,FXD,CER DI:100PF,5\%,100V (2246 MOD A,2Y) | 04222 | SA102A101JAA |
| A10C488 | 283-0196-00 | B120000 |  | CAP,FXD,CER DI:270PF, $10 \%, 50 \mathrm{~V}$ (2246 MOD A,2Y) | 16299 | SR155C271KAA |
| A10C488 | 281-0765-00 | B700100 | B710099 | CAP,FXD,CER DI:100PF, $5 \%, 100 \mathrm{~V}$ (2246 1Y) | 04222 | SA102A101JAA |
| A10C488 | 283-0196-00 | B715000 |  | CAP,FXD,CER DI:270PF,10\%,50V (2246 1Y) | 16299 | SR155C271KAA |
| A10C489 | 281-0765-00 |  |  | CAP,FXD,CER DI:100PF,5\%,100V | 04222 | SA102A101JAA |
| A10C491 | 281-0819-00 |  |  | CAP,FXD,CER DI:33 PF,5\%,50V | 04222 | SA102A330JAA |
| A10C492 | 281-0819-00 |  |  | CAP,FXD,CER DI:33 PF,5\%,50V | 04222 | SA102A330JAA |
| A10C493 | 281-0819-00 |  |  | CAPFXD,CER DI:33 PF,5\%,50V | 04222 | SA102A330JAA |
| A10C494 | 281-0819-00 |  |  | CAP,FXD,CER DI:33 PF,5\%,50V | 04222 | SA102A330JAA |
| A10C496 | 281-0864-00 | B110254 | B119999 | CAP,FXD,CER DI:430PF, $5 \%, 100 \mathrm{~V}$ (2246 MOD A, 2 Y) | 04222 | SA101A431JAA |
| A10C496 | 283-0196-00 | B120000 |  | CAP,FXD,CER DI:270PF, $10 \%, 50 \mathrm{~V}$ (2246 MOD A,2Y) | 16299 | SR155C271KAA |
| A10C496 | 281-0864-00 | B710152 | B714999 | CAP,FXD,CER DI:430PF,5\%,100V (2246 1Y) | 04222 | SA101A431JAA |
| A10C496 | 283-0196-00 | B715000 |  | CAP,FXD,CER DI:270PF, 10\%,50V (2246 1Y) | 16299 | SR155C271KAA |
| A10C501 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C502 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C503 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C505 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C600 | 281-0861-00 |  |  | CAP,FXD,CER DI:270PF,5\%,50V | 04222 | SA101A271JAA |
| A10C601 | 281-0861-00 |  |  | CAP,FXD,CER DI:270PF,5\%,50V | 04222 | SA101A271JAA |
| A10C602 | 281-0819-00 |  |  | CAP,FXD,CER DI:33 PF,5\%,50V | 04222 | SA102A330JAA |
| A10C603 | 281-0819-00 |  |  | CAP,FXD,CER DI:33 PF,5\%,50V | 04222 | SA102A330JAA |
| A10C604 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C605 | 281-0909-00 |  |  | CAP,FXD, CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C606 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C607 | 281-0765-00 |  |  | CAP,FXD,CER DI:100PF,5\%,100V | 04222 | SA102A101JAA |
| A10C608 | 281-0765-00 |  |  | CAP,FXD,CER DI:100PF, $5 \%, 100 \mathrm{~V}$ | 04222 | SA102A101JAA |
| A10C609 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |

Replaceable Electrical Parts 22461 Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mir. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C610 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C611 | 281-0810-00 |  |  | CAP,FXD,CER DI:5.6PF + /-0.5PF,100VTUBULAR,MI | 04222 | SA102A5R6DAA |
| A10C612 | 281-0810-00 |  |  | CAP,FXD,CER DI:5.6PF $+1-0.5 \mathrm{PF}, 100 \mathrm{VTUBULAR,MI}$ | 04222 | SA102A5R6DAA |
| A10C613 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C614 | 281-0776-00 | B110100 |  | CAP,FXD,CER DI:120PF,5\%,100V (2246 MOD A) | 04222 | SA102A121JAA |
| A10C614 | 281-0776-00 | B710100 |  | CAP,FXD,CER DI:120PF,5\%,100V (2246 1Y) | 04222 | SA102A121JAA |
| A10C636 | 281-0909-00 | B120000 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C636 | 281-0909-00 | B715000 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C666 | 281-0819-00 | B110994 |  | CAP,FXD,CER DI:33 PF,5\%,50V (2246 MOD A,2Y) | 04222 | SA102A330JAA |
| A10C666 | 281-0819-00 | B710442 |  | CAP,FXD,CER DI:33 PF,5\%,50V (2246 1Y) | 04222 | SA102A330JAA |
| A10C701 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C702 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C703 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C704 | 281-0909-00 |  |  | CAP,FXD, CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C705 | 283-0057-00 | B100100 | B119999 | CAP,FXD,CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SR302E105ZAA |
| A10C705 | 285-1460-00 | B120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C705 | 283-0057-00 | B700100 | B714999 | CAP,FXD,CER DI:O.1UF, $+80-20 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | SR302E105ZAA |
| A10C705 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 1Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C706 | 281-0893-00 |  |  | CAP,FXD,CER DI:4.7PF, + /-0.5PF,100VTUBULAR,MI | 04222 | SA102A4R7DAA |
| A10C707 | 281-0798-00 |  |  | CAP,FXD,CER DI:51PF,1\%,100V | TK1743 | CGB510GEN |
| A10C708 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A100711 | 283-0201-01 |  |  | CAP,FXD,CER DI:27PF,10\%,50V | 04222 | SR155A270KAATR |
| A100712 | 283-0201-01 |  |  | CAP,FXD,CER DI:27PF,10\%,50V | 04222 | SR155A270KAATR |
| A10C801 | 283-0057-00 | 8100100 | B119999 | CAP,FXD,CER DI:0.1UF $+80-20 \%, 200 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SR302E105ZAA |
| A10C801 | 285-1460-00 | B120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C801 | 283-0057-00 | B700100 | B714999 | CAP,FXD,CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | SR302E105ZAA |
| A10C801 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 1Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C802 | 283-0057-00 | 8100100 | B119999 | CAP,FXD,CER DI:0.1UF $+80-20 \%, 200 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SR302E105ZAA |
| A10C802 | 285-1460-00 | 8120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C802 | 283-0057-00 | B700100 | B714999 | CAP,FXD,CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | SR302E105ZAA |


| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \% Description | Mfr. Code | Mir. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C802 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 1Y) | TK1913 | MKS 20.1/250/2 |
| A10C803 | 281-0707-00 | 8100100 | B110099 | CAP,FXD,CER DI:15000PF,10\%,200V (2246 MOD A,2Y) | 04222 | MA302C153KAA |
| A10C803 | 281-0909-00 | B110100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C803 | 281-0707-00 | B700100 | B710099 | CAP,FXD,CER DI:15000PF, $10 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | MA302C153KAA |
| A10C803 | 281-0909-00 | B710100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C804 | 281-0909-00 | B100100 | B110099 | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C804 | 283-0057-00 | B110100 | B119999 | CAP,FXD,CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SR302E105ZAA |
| A10C804 | 285-1460-00 | B120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C804 | 281-0909-00 | B700100 | B710099 | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C804 | 283-0057~00 | B710100 | B714999 | CAP,FXD,CER DI:0.1UF $+\mathbf{8 0}-20 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | SR302E105ZAA |
| A10C804 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING $(22461 Y)$ | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C805 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C806 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C807 | 281-0064-00 | B100100 | B110099 | CAP,VAR,PLASTIC:0.25-1.5PF,600V (2246 MOD A,2Y) | 52769 | ER-530-013 |
| A10C807 | 281-0214-00 | B110100 |  | CAP,VAR,CER DI:0.6-3PF,400V (2246 MOD A,2Y) | 52763 | 313613-140 |
| A10C807 | 281-0064-00 | B700100 | B710099 | CAP,VAR,PLASTIC:0.25-1.5PF,600V (2246 1Y) | 52769 | ER-530-013 |
| A10C807 | 281-0214-00 | B710100 |  | CAP,VAR,CER DI:0.6-3PF,400V (2246 1Y) | 52763 | 313613-140 |
| A10C808 | 281-0765-00 |  |  | CAP,FXD,CER DI:100PF, $5 \%, 100 \mathrm{~V}$ | 04222 | SA102A101JAA |
| A10C809 | 283-0057-00 | B100100 | B119999 | CAP,FXD,CER DI: $0.1 \mathrm{UF},+80-20 \%, 200 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SR302E105ZAA |
| A10C809 | 285-1460-00 | B120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C809 | 283-0057-00 | B700100 | B714999 | $\begin{aligned} & \text { CAP,FXD,CER DI:0.1UF, }+80-20 \%, 200 \mathrm{~V} \\ & (2246 \mathrm{1Y}) \end{aligned}$ | 04222 | SR302E105ZAA |
| A10C809 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING $(22461 Y)$ | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C810 | 281-0707-00 | B100100 | B110099 | CAP,FXD,CER DI:15000PF,10\%,200V (2246 MOD A,2Y) | 04222 | MA302C153KAA |
| A10C810 | 281-0707-00 | B700100 | B710099 | CAP,FXD,CER DI:15000PF, $10 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | MA302C153KAA |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serlal Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C811 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C814 | 281-0064-00 | B100100 | B110099 | CAP,VAR,PLASTIC:0.25-1.5PF,600V (2246 MOD A,2Y) | 52769 | ER-530-013 |
| A100814 | 281-0214-00 | B110100 |  | CAP,VAR,CER DI:0.6-3PF,400V (2246 MOD A,2Y) | 52763 | 313613-140 |
| A10C814 | 281-0064-00 | B700100 | B710099 | CAP,VAR,PLASTIC:0.25-1.5PF,600V (2246 1V) | 52769 | ER-530-013 |
| A10C814 | 281-0214-00 | B710100 |  | CAP,VAR,CER DI:0.6-3PF,400V (2246 1Y) | 52763 | 313613-140 |
| A10C815 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C816 | 281-0909-00 |  |  | CAPPYXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C817 | 290-1198-00 |  |  | CAP,FXD,ELCTLT:100UF,20\%,10VAC | 80009 | 290119800 |
| A10C818 | 283-0057-00 | B100100 | B119999 | CAP,FXD,CER DI:0.1UF $+80-20 \%, 200 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SR302E105ZAA |
| A10C818 | 285-1460-00 | 8120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C818 | 283-0057-00 | B700100 | B714999 | CAP,FXD,CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | SR302E105ZAA |
| A10C818 | 285-1460-00 | B715000 |  | CAP.FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 1Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C819 | 281-0765-00 |  |  | CAP,FXD,CER DI:100PF,5\%,100V | 04222 | SA102A101JAA |
| A10C820 | 281-0909-00 | B110100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 MOD A, 2 Y) | 04222 | SA105C223MAA |
| A10C820 | 281-0909-00 | B710100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C821 | 281-0909-00 | B110100 |  | CAPPTXD,CER DI:0.022UF,20\%,50V ( 2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C821 | 281-0909-00 | B710100 |  | CAP.FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C822 | 281-0909-00 | B110100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C822 | 281-0909-00 | B710100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C860 | 281-0765-00 | B110100 |  | CAP,FXD,CER DI:100PF,5\%,100V (2246 MOD A,2Y) | 04222 | SA102A101JAA |
| A10C860 | 281-0765-00 | B710100 |  | CAP,FXD,CER DI:100PF,5\%,100V (2246 1Y) | 04222 | SA102A101JAA |
| A10C870 | 281-0816-00 | B110100 |  | CAP,FXD,CER DI: 82 PF,5\%,100V (2246 MOD A,2Y) | 04222 | SA106A820JAA |
| A10C870 | 281-0816-00 | B710100 |  | CAP,FXD,CER DI:82 PF,5\%,100V (2246 1Y) | 04222 | SA106A820JAA |
| A10C880 | 281-0909-00 | B110100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V ( 2246 MOD A, 2 Y) | 04222 | SA105C223MAA |
| A10C880 | 281-0909-00 | B710100 |  | CAP.FXD,CER DI:0.022UF. $20 \%, 50 \mathrm{~V}$ (2246 1Y) | 04222 | SA105C223MAA |
| A10C901 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C902 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C903 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C904 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |


| Component Number | Tektronix <br> Part No. | Serial | No. Dscont | Name \& Description | Mir. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C910 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C935 | 285-1339-00 |  |  | CAP,FXD.MTLZD:0.022UF,10\%,63V | TK1913 | MKS $20.022 / 63 /$ |
| A10C1001 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C1002 | 290-0974-01 | B700100 | B710172 | CAP,FXD,ELCTLT:10UF,20\%,50V (2246 1Y) | 1W344 | KMC5OVB1ORM5X11 |
| A10C1002 | 290-0974-00 | B710173 |  | CAP,FXD,ELCTLT:10UF,20\%,50VDC (2246 1Y) | 55680 | UVX1H100MAA |
| A10C1002 | 290-0974-01 | B100100 | B110288 | CAP,FXD,ELCTLT:10UF,20\%,50V (2246 MOD A,2Y) | 1W344 | KMC5OVB1ORM5X11 |
| A10C1002 | 290-0974-00 | B110289 |  | CAP,FXD,ELCTLT:10UF,20\%,50VDC ( 2246 MOD A,2Y) | 55680 | UVX1H100MAA |
| A10C1003 | 290-0974-01 | B700100 | B710172 | CAP,FXD,ELCTLT:10UF,20\%,50V (2246 1Y) | 1W344 | KMC5OVB1ORM5X11 |
| A10C1003 | 290-0974-00 | B710173 |  | CAP,FXD,ELCTLT:10UF,20\%,50VDC (2246 1Y) | 55680 | UVX1H100MAA |
| A10C1003 | 290-0974-01 | B100100 | B110288 | CAP,FXD,ELCTLT:YOUF,20\%,50V ( 2246 MOD A,2Y) | 1W344 | KMC5OVB1ORM5X11 |
| A10C1003 | 290-0974-00 | B110289 |  | CAP,FXD,ELCTLT:10UF,20\%,50VDC (2246 MOD A,2Y) | 55680 | UVX1H100MAA |
| A10C1004 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C1005 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C1006 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C1101 | 281-0909-00 | 8700100 | B710290 | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C1101 | 290-0183-00 | B710291 |  | CAP,FXD,ELCTLT:1UF,10\%,35V (2246 1Y) | 31433 | T322B105K035AS |
| A10C1101 | 281-0909-00 | B100100 | B110637 | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C1101 | 290-0183-00 | B110638 |  | CAP,FXD,ELCTLT:1UF,10\%,35V (2246 MOD A,2Y) | 31433 | T322B105K035AS |
| A10C1102 | 290-0183-00 | B100100 | B110099 | CAP,FXD,ELCTLT:1UF,10\%,35V (2246 MOD A, 2 Y) | 31433 | T322B105K035AS |
| A10C1102 | 281-0909-00 | B110100 | B110637 | CAPPXD,CER DI:0.022UF,20\%,50V (2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C1102 | 290-0183-00 | B110638 |  | CAP,FXD,ELCTLT:1UF, $10 \%, 35 \mathrm{~V}$ (2246 MOD A,2Y) ( 2246 MOD A,2Y) | 31433 | T322B105K035AS |
| A10C1102 | 290-0183-00 | 8700100 | B710099 | CAP,FXD,ELCTLT:1UF,10\%,35V (2246 1Y) | 31433 | T322B105K035AS |
| A10C1102 | 281-0909-00 | B710100 | 8710290 | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C1102 | 290-0183-00 | B710291 |  | CAP,FXD,ELCTLT:1UF, $10 \%$,35V (2246 1Y) | 31433 | T322B105K035AS |
| A10C1103 | 290-0183-00 |  |  | CAP,FXD,ELCTLT:1UF,10\%,35V | 31433 | T322B105K035AS |
| A10C1104 | 281-0909-00 | B110100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V ( 2246 MOD A,2Y) | 04222 | SA105C223MAA |
| A10C1104 | 281-0909-00 | B710100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C1105 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C1106 | 281-0820-00 |  |  | CAP,FXD,CER DI:680 PF, $10 \%, 50 \mathrm{~V}$ | 04222 | SA101C681KAA |
| A10C1107 | 281-0765-00 |  |  | CAP,FXD,CER DI:100PF,5\%,100V | 04222 | SA102A101JAA |


| Component Number | Tektronix <br> Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C1108 | 290-0183-00 | B710630 |  | ```CAP,FXD,ELCTLT:1UF,10%,35V (2246 1Y)``` | 31433 | T322B105K035AS |
| A10C1108 | 290-0183-00 | B111186 |  | CAP,FXD,ELCTLT:1UF,10\%,35V (2246 MOD A,2Y) | 31433 | T322B105K035AS |
| A10C1110 | 281-0799-00 |  |  | CAP,FXD,CER DI:62PF,2\%,100V | 04222 | SA101A620GAA |
| A10C1111 | 281-0799-00 |  |  | CAP,FXD,CER DI:62PF,2\%,100V | 04222 | SA101A620GAA |
| A10C1114 | 290-0974-01 |  |  | CAP,FXD,ELCTLT:10UF,20\%,50V | 1W344 | KMC5OVB1ORM5X11 |
| A10C1130 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C1143 | 281-0770-00 | B710630 |  | CAP,FXD,CER DI:1000PF,20\%,100V (2246 1Y) | 04222 | SA101C102MAA |
| A10C1143 | 281-0770-00 | B111186 |  | CAP,FXD,CER DI:1000PF,20\%,100V (2246 MOD A,2Y) | 04222 | SA101C102MAA |
| A10C1154 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C1155 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C1158 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C1159 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C1160 | 281-0909-00 | B110100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 MOD A) | 04222 | SA105C223MAA |
| A10C1160 | 281-0909-00 | B710100 |  | CAP,FXD,CER DI:0.022UF,20\%,50V (2246 1Y) | 04222 | SA105C223MAA |
| A10C1250 | 283-0853-00 | B120000 |  | CAP,FXD,CER DI:2.2PF,200V (2246 MOD A,2Y) | 04222 | SR212A2R2DAATR |
| A10C1250 | 283-0853-00 | B715000 |  | CAP,FXD,CER DI:2.2PF,200V (2246 1Y) | 04222 | SR212A2R2DAATR |
| A10C1251 | 283-0853-00 | B120000 |  | CAP,FXD,CER DI:2.2PF,200V (2246 MOD A,2Y) | 04222 | SR212A2R2DAATR |
| A10C1251 | 283-0853-00 | B715000 |  | CAP,FXD,CER DI:2.2PF,200V (2246 1Y) | 04222 | SR212A2R2DAATR |
| A10C2701 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C2702 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C2703 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C2704 | 281-0064-00 | B100100 | B110099 | CAP,VAR,PLASTIC:0.25-1.5PF,600V (2246 MOD A,2Y) | 52769 | ER-530-013 |
| A10C2704 | 283-0348-00 | B110100 |  | CAP,FXD,CER DI:0.5PF, +/-0.1PF,100V (2246 MOD A,2Y) | 51642 | W150-100-NP0-50 |
| A10C2704 | 281-0064-00 | 8700100 | 8710099 | CAP,VAR,PLASTIC:0.25-1.5PF,600V (2246 1Y) | 52769 | ER-530-013 |
| A10C2704 | 283-0348-00 | B710100 |  | $\begin{aligned} & \text { CAP,FXD,CER DI:0.5PF, +/-0.1PF,100V } \\ & (22461 Y) \end{aligned}$ | 51642 | W150-100-NPO-50 |
| A10C2705 | 281-0771-00 |  |  | CAP,FXD,CER DI:2200PF,20\%,200V | 04222 | SA106E222MAA |
| A10C2706 | 281-0893-00 |  |  | CAP,FXD,CER DI:4.7PF, +/-0.5PF,100VTUBULAR,MI | 04222 | SA102A4R7DAA |
| A10C2707 | 281-0893-00 |  |  | CAP,FXD,CER DI:4.7PF, +/-0.5PF, 100VTUBULAR,MI | 04222 | SA102A4R7DAA |
| A10C2708 | 283-0057-00 | B100100 | B119999 | CAP,FXD,CER DI:0.1UF, +80-20\%,200V (2246 MOD A,2Y) | 04222 | SR302E105ZAA |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effectlve | No. Dscont | Name \& Description | Mfr. Code | Mir. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C2708 | 285-1460-00 | B120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C2708 | 283-0057-00 | 8700100 | B714999 | CAP,FXD,CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | SR302E105ZAA |
| A10C2708 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING $(22461 Y)$ | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C2709 | 283-0057-00 | B100100 | B119999 | CAP,FXD,CER DI:0.1UF, +80-20\%,200V (2246 MOD A,2Y) | 04222 | SR302E105ZAA |
| A10C2709 | 285-1460-00 | B120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C2709 | 283-0057-00 | B700100 | B714999 | $\begin{aligned} & \text { CAP,FXD,CER DI:0.1UF, }+80-20 \%, 200 \mathrm{~V} \\ & (2246 \text { YY }) \end{aligned}$ | 04222 | SR302E105ZAA |
| A10C2709 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING (2246 1Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C2710 | 283-0057-00 | B100100 | B119999 | $\begin{aligned} & \text { CAP,FXD,CER DI:0.1UF, }+80-20 \%, 200 \mathrm{~V} \\ & \text { (2246 MOD A,2Y) } \end{aligned}$ | 04222 | SR302E105ZAA |
| A10C2710 | 285-1460-00 | B120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A1002710 | 283-0057-00 | B700100 | B714999 | CAP,FXD,CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | SR302E105ZAA |
| A10C2710 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 1Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C2711 | 285-1184-01 |  |  | CAP,FXD,MTLZD:0.01UF,20\%,4KV | 84411 | TEK183.01 204 |
| A10C2712 | 285-1040-00 |  |  | CAP,FXD,PLASTIC:1200PF,10\%,4000V | 84411 | TEK-134-122940 |
| A10C2713 | 281-0771-00 |  |  | CAP,FXD,CER Dl:2200PF,20\%,200V | 04222 | SA106E222MAA |
| A10C2715 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C2716 | 281-0771-00 |  |  | CAP,FXD,CER DI:2200PF,20\%,200V | 04222 | SA106E222MAA |
| A10C2717 | 283-0057-00 | B100100 | B119999 | CAP,FXD,CER DI:0.1UF $+80-20 \%, 200 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SR302E105ZAA |
| A10C2717 | 285-1460-00 | B120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V.5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C2718 | 283-0057-00 | B700100 | B714999 | CAP,FXD,CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | SR302E105ZAA |
| A10C2718 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING (2246 1Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C2719 | 285-1184-01 |  |  | CAP,FXD,MTLZD:0.01UF,20\%,4KV | 84411 | TEK183 . 01204 |
| A10C2720 | 285-1040-00 |  |  | CAP,FXD,PLASTIC:1200PF,10\%,4000V | 84411 | TEK-134-122940 |
| A10C2721 | 281-0771-00 |  |  | CAP,FXD,CER DI:2200PF,20\%,200V | 04222 | SA106E222MAA |
| A10C2723 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C2724 | 285-1184-01 |  |  | CAP,FXD,MTLZD:0.01UF,20\%,4KV | 84411 | TEK183.01 204 |
| A10C2758 | 285-1184-01 | B100100 | B110099 | CAP,FXD,MTLZD:0.01UF,20\%,4KV (2246 MOD A,2Y) | 84411 | TEK183.01 204 |
| A10C2758 | 285-1184-01 | B700100 | B710099 | CAP,FXD,MTLZD:0.01UF,20\%,4KV (2246 1Y) | 84411 | TEK183.01 204 |
| A10C2759 | 281-0759-00 |  |  | CAP,FXD,CER DI:22PF,10\%,100V | 04222 | SA101A220KAA |
| A10C2783 | 283-0057-00 | B100100 | B119999 | CAP,FXD,CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ | 04222 | SR302E105ZAA |

## Replaceable Electrical Parts

22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name 8 Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C2783 | 285-1460-00 | B120000 |  | CAP,FXD, MTLZZ:0.1UF, $20 \%, 250 \mathrm{~V}, 5 \mathrm{MM}$ LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C2783 | 283-0057-00 | B700100 | B714999 | CAP,FXD,CER DI:0.1UF $+80-20 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | SR302E105ZAA |
| A10C2783 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING (2246 7Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C2784 | 281-0909-00 |  |  | CAPFFXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A10C2785 | 283-0057-00 | B100100 | B119999 | CAP,FXD,CER DI:0.1UF $+80-20 \%, 200 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SR302E105ZAA |
| A10C2785 | 285-1460-00 | B120000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING <br> (2246 MOD A,2Y) | TK1913 | MKS $20.1 / 250 / 2$ |
| A10C2785 | 283-0057-00 | B700100 | B714999 | CAP,FXD,CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ (2246 1Y) | 04222 | SR302E105ZAA |
| A10C2785 | 285-1460-00 | B715000 |  | CAP,FXD,MTLZD:0.1UF,20\%,250V,5MM LEAD SPACING $(2246 \mathrm{IV})$ | TK1913 | MKS $20.1 / 250 / 2$ |
| A10CR131 | 152-0246-00 |  |  | SEMICOND DVC,DI:SW,SI,40V,200MA,DO-7 | 27014 | FDH5227.03 |
| A10CR151 | 152-0246-00 |  |  | SEMICOND DVC,DI:SW,SI,40V,200MA,DO-7 | 27014 | FDH5227.03 |
| A10CR171 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR201 | 152-0141-02 | B100100 | B119999 | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF (2246 MOD A,2Y) | 27014 | FDH9427 |
| A10CR201 | 152-1107-00 | B120000 |  | DIODE,SIG:SCHTKY;;40V,350MA,12PF (2246 MOD A,2Y) | 50088 | BAT48 |
| A10CR201 | 152-0141-02 | B700100 | B714999 | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF (2246 1Y) | 27014 | FDH9427 |
| A10CR201 | 152-1107-00 | B715000 |  | $\begin{aligned} & \text { DIODE,SIG:SCHTKY;40V,350MA,12PF } \\ & \text { (2246 iY) } \end{aligned}$ | 50088 | BAT48 |
| A10CR202 | 152-0141-02 | B100100 | B119999 | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF (2246 MOD A,2Y) | 27014 | FDH9427 |
| A10CR202 | 152-1107-00 | B120000 |  | DIODE,SIG:SCHTKY;;40V,350MA, 12PF (2246 MOD A,2Y) | 50088 | BAT48 |
| A10CR202 | 152-0141-02 | B700100 | B714999 | DIODE,SIG;,UITRA FAST;4OV,150MA,4NS,2PF (2246 1Y) | 27014 | FDH9427 |
| A10CR202 | 152-1107-00 | B715000 |  | DIODE,SIG:SCHTKY;40V,350MA,12PF (2246 iY) | 50088 | BAT48 |
| A10CR260 | 152-0066-00 |  |  | DIODE,RECT:,400V,1A,30A IFSM | 05828 | GP10G-020 |
| A10CR261 | 152-0066-00 |  |  | DIODE, RECT:,;400V,1A,30A IFSM | 05828 | GP10G-020 |
| A10CR262 | 152-0650-00 |  |  | DIODE,SIG:,VVC;30V,2.3PF@C25,C3/25=5.25 | 50101 | U11-4101 |
| A10CR263 | 152-0650-00 |  |  | DIODE,SIG:,VVC;30V,2.3PF@C25,C3/25=5.25 | 50101 | U11-4101 |
| A10CR301 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR432 | 152-0246-00 |  |  | SEMICOND DVC,DI:SW,SI,40V,200MA,DO-7 | 27014 | FDH5227.03 |
| A10CR462 | 152-0246-00 |  |  | SEMICOND DVC,DI:SW,SI,40V,200MA,DO-7 | 27014 | FDH5227.03 |
| A10CR603 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR801 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR802 | 152-0061-00 | B100100 | B110099 | SEMICOND DVC,DI:SW,SI,175V,0.1A,DO-35CD8393 (2246 MOD A,2Y) | 12969 | PV122 |
| A10CR802 | 152-0141-02 | B110100 |  | DIODE,SIG:,ULTRA FAST;4OV,150MA,4NS,2PF (2246 MOD A,2Y) | 27014 | FDH9427 |


| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10CR802 | 152-0061-00 | B700100 | B710099 | SEMICOND DVC,DI:SW,SI, 175V,0.1A,DO-35CD8393 (2246 Y ) | 12969 | PV122 |
| A10CR802 | 152-0141-02 | B710100 |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF (2246 iY) | 27014 | FDH9427 |
| A10CR803 | 152-0141-02 | B113047 |  | DIODE,SIG:,ULTRA FAST;4OV,150MA,4NS,2PF ( 2246 MOD A, 2 Y ) | 27014 | FDH9427 |
| A10CR803 | 152-0141-02 | B711085 |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF (2246 iY) | 27014 | FDH9427 |
| A10CR807 | 152-0141-02 | B113047 |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF (2246 MOD A,2Y) | 27014 | FDH9427 |
| A10CR807 | 152-0141-02 | B711085 |  | DIODE,SIG:,ULTRA FAST;4OV, 150MA,4NS,2PF (2246 1Y) | 27014 | FDH9427 |
| A10CR819 | 152-0061-00 | B100100 | B110099 | SEMICOND DVC,DI:SW,SI,175V,0.1A,DO-35CD8393 ( 2246 MOD A,2Y) | 12969 | PV122 |
| A10CR819 | 152-0061-00 | B700100 | B710099 | SEMICOND DVC,DI:SW,SI,175V,0.1A,DO-35CD8393 (2246 1Y) | 12969 | PV122 |
| A10CR935 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST:40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR936 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR1001 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST:40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR1002 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V, $150 \mathrm{MA}, 4 \mathrm{NS}, 2 \mathrm{PF}$ | 27014 | FDH9427 |
| A10CR1003 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR1004 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR1005 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR2701 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A10CR2702 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V, 1A,200NS | 14552 | MB2501 |
| A10CR2703 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V, 1A,200NS | 14552 | MB2501 |
| A10CR2704 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A10CR2705 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR2707 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR2713 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A10CR2714 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A10CR2715 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A10CR2716 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY; $400 \mathrm{~V}, 1 \mathrm{~A}, 200 \mathrm{NS}$ | 14552 | MB2501 |
| A10CR2717 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A10CR2718 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A10DL22 | 119-2119-01 |  |  | DELAY LINE,ELEC: W/STRAINRELIEF | 80009 | 119211901 |
| A10DS901 | 150-0146-00 |  |  | LAMP,INCAND:14V,80MA,73E, WEDGE BASE | S3774 | OL-2182-01 |
| A10DS902 | 150-0146-00 |  |  | LAMP,INCAND:14V,80MA,73E,WEDGE BASE | S3774 | OL-2182-01 |
| A10DS903 | 150-0146-00 |  |  | LAMP,INCAND:14V,80MA,73E,WEDGE BASE | S3774 | OL-2182-01 |
| A10DS2701 | 150-0035-00 |  |  | LAMP,GLOW:90V MAX,0.3MA,AID-T,WIRE LD | TK0213 | JH005/3011JA |
| A10DS2702 | 150-0035-00 |  |  | LAMP,GLOW:90V MAX,0.3MA,AID-T,WIRE LD | TK0213 | JH005/3011JA |
| A10DS2703 | 150-0035-00 |  |  | LAMP,GLOW:90V MAX,0.3MA,AID-T,WIRE LD | TK0213 | JH005/3011JA |
| A10DS2704 | 150-0035-00 |  |  | LAMP,GLOW:90V MAX,0.3MA,AID-T,WIRE LD | TK0213 | JH005/3011JA |

Replaceable Electrical Parts
22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. <br> Code | Mfr, Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10.17 | 131-3731-00 |  |  | CONN,RF JACK:BNC:.50 OHM,FEMALE,STR,SLDR CUP/FRONT PNL,0.5-28 W/D FLAT,1.062 L 0.188 | 24931 | 28JR377-1 |
| A10.12 | 131-3731-00 |  |  | CONN,RF JACK:BNC,; 50 OHM,FEMALE,STR,SLDR CUP/FRONT PNL,0.5-28 W/D FLAT, $1.062 \mathrm{~L} \times 0.188$ | 24931 | 28JR377-1 |
| A10J13 | 131-3731-00 |  |  | CONN,RF JACK:BNC;;50 OHM,FEMALE,STR,SLDR CUP/FRONT PNL,0.5-28 W/D FLAT,1.062 L X 0.188 | 24931 | 28JR377-1 |
| A10.J14 | 131-3731-00 |  |  | CONN,RF JACK:BNC,;50 OHM,FEMALE,STR,SLDR CUP/FRONT PNL,0.5-28 W/D FLAT,1.062 LX 0.188 | 24931 | 28JR377-1 |
| A10」15 | 131-3464-00 |  |  | CONTACT,ELEC:BRASS | TK1935 | ORDER BY DESC |
| A10J927 | 131-2921-00 | B100100 | B110099 | CONN,HDR:PCB,;MALE,RTANG, $2 \times 1,0.1$ CTR, 0.318 MLG $\times 0.110$ TAIL, $0.238 \mathrm{H}, 30 \mathrm{GOLD}$ (2246 MOD A,2Y) | 00779 | 1-86479-3 |
| A10J927 | 131-4546-00 | B110100 |  | CONN,HDR:PCB;;MALE,STR, $1 \times 2,0.1$ CTR,0.230 MLG $\times 0.120$ TAIL, 30 GOLD <br> (2246 MOD A,2Y) | 00779 | 103239-2 |
| A10,927 | 131-2921-00 | B700100 | B710099 | CONN,HDR:PCB;;MALE,RTANG, $2 \times 1,0.1$ CTR, 0.318 MLG $\times 0.110$ TAIL, $0.238 \mathrm{H}, 30 \mathrm{GOLD}$ (2246 1Y) | 00779 | 1-86479-3 |
| A10J927 | 134-4546-00 | B710100 |  | CONN,HDR:PCB;MALE,STR, $1 \times 2,0.1$ CTR,0.230 MLG $\times 0.120$ TAIL, 30 GOLD (2246 1Y) | 00779 | 103239-2 |
| A10.J1204 | 131-3638-00 |  |  | CONN,BOX PWR:PCB;;FEMALE,STR, $1 \times 13,0.156$ CTR, $0.450 \mathrm{H} \times 0.160$ TAIL, BOTTOM ENTRY, 20 GOLD, $0.156 \times 0.125$ CTR PCB | 27264 | 09-62-3132 |
| A10K100 | 148-0174-00 |  |  | RELAY,ARMATURE: 1 FORM C,12VDC | TK1689 | DS1EMDC12V-H166 |
| A10K101 | 148-0174-00 |  |  | RELAY,ARMATURE:1 FORM C,12VDC | TK1689 | DS1EMDC12V-H166 |
| A10K102 | 148-0173-01 |  |  | RELAY,ARMATURE: 1 FORM C,INSERT LOSS 0.3 DB@900MHZ,VSWR 1.5@900MHZ,COIL 12VDC 720 OHM,CONTACT 30VDC 0.5A 10W,PKG . $795 \times .441$ | TK1689 | RK1EDC12V |
| A10K103 | 148-0173-01 |  |  | RELAY,ARMATURE: 1 FORM C,INSERT LOSS 0.3 DB@900MHZ,VSWR 1.5@900MHZ,COIL 12VDC 720 OHM,CONTACT 3OVDC 0.5A 10W,PKG . $795 \times .441$ | TK1689 | RK1EDC12V |
| A10K104 | 148-0174-00 |  |  | RELAY,ARMATURE: 1 FORM C,12VDC | TK1689 | DS1EMDC12V-H166 |
| A10K105 | 148-0174-00 |  |  | RELAY,ARMATURE: 1 FORM C,12VDC | TK1689 | DS1EMDC12V-H166 |
| A10K107 | 148-0174-00 |  |  | RELAY,ARMATURE:1 FORM C.12VDC | TK1689 | DS1EMDC12V-H166 |
| A10K108 | 148-0174-00 |  |  | RELAY,ARMATURE:1 FORM C,12VDC | TK1689 | DS1EMDC12V-H166 |
| A10K109 | 148-0173-01 |  |  | RELAY,ARMATURE: 1 FORM C,INSERT LOSS 0.3 DB@900MHZ,VSWR 1.5@900MHZ,COIL 12VDC 720 OHM,CONTACT 3OVDC 0.5A 10W,PKG . $795 \times .441$ | TK1689 | RK1EDC12V |
| A10K110 | 148-0173-01 |  |  | RELAY,ARMATURE: 1 FORM C,INSERT LOSS 0.3 DB@900MHZ,VSWR 1.5@900MHZ,COIL 12VDC 720 OHM,CONTACT 3OVDC 0.5A 10W,PKG $.795 \times .441$ | TK1689 | RK1EDC12V |
| A10K111 | 148-0174-00 |  |  | RELAY,ARMATURE: 1 FORM C, 12 VDC | TK1689 | DS1EMDC12V-H166 |
| A10K112 | 148-0174-00 |  |  | RELAY,ARMATURE: 1 FORM C,12VDC | TK1689 | DS1EMDC12V-H166 |
| A10L. 101 | 108-1319-00 |  |  | INDUCTOR,FIXED:33UH, $10 \%, 1.8 \mathrm{~A}$ | 54583 | TSL1110-330K 1R |
| A10L102 | 108-1319-00 |  |  | INDUCTOR,FIXED:33UH,10\%,1.8A | 54583 | TSL1110-330K 1R |
| A10L 130 | 108-0682-00 |  |  | COIL,RF:FIXED,61NH | OJRO3 | 108-0682-00 |
| A10L140 | 108-0682-00 |  |  | COIL,RF:FIXED,61NH | OJRO3 | 108-0682-00 |
| A10L201 | 108-1319-00 |  |  | INDUCTOR,FIXED:33UH,10\%,1.8A | 54583 | TSL1110-330K 1R |
| A10L216 | 108-1339-00 |  |  | COIL,RF:FXD,330NH | 91637 | IR-2.330 MICRO |
| A10L217 | 108-1339-00 |  |  | COIL,RF:FXD,330NH | 91637 | IR-2.330 MICRO |
| A10L426 | 108-1281-00 |  |  | COIL,RF:FXD,2.2UH,10\% | 54583 | SPTO3O5-2R2K |


| Component Number | Tektronlx Part No. | Serial | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10L432 | 108-1341-00 |  |  | COIL,RF:FXD, 180NH,10\%,0.1 OHM,1100MA MI AXIAL LEADS | 91637 | IR-20.180 MICR |
| A10L445 | 108-1339-00 |  |  | COIL,RF:FXD,330NH | 91637 | IR-2.330 MICRO |
| A10L446 | 108-1339-00 | B100100 | B110099 | COIL,RF:FXD,330NH (2246 MOD A,2Y) | 91637 | IR-2.330 MICRO |
| A10L446 | 108-1339-00 | B700100 | B710099 | COIL,RF:FXD, 330 NH (2246 Y ) | 91637 | IR-2.330 MICRO |
| A10L462 | 108-1341-00 |  |  | COIL,RF:FXD, 180NH, $10 \%, 0.1$ OHM,1100MA MI AXIAL LEADS | 91637 | IR-2 0.180 MICR |
| A10L475 | 108-1339-00 |  |  | COIL,RF:FXD,330NH | 91637 | IR-2.330 MICRO |
| A10L476 | 108-1339-00 | B100100 | B110099 | COIL,RF:FXD,330NH (2246 MOD A,2Y) | 91637 | IR-2.330 MICRO |
| A10L476 | 108-1339-00 | B700100 | B710099 | $\begin{aligned} & \text { COIL,RF:FXD,330NH } \\ & (2246 \text { 1Y) } \end{aligned}$ | 91637 | IR-2.330 MICRO |
| A10L701 | 108-1339-00 |  |  | COIL,RF:FXD,330NH | 91637 | IR-2.330 MICRO |
| A10L702 | 108-1339-00 |  |  | COIL,RF:FXD,330NH | 91637 | IR-2.330 MICRO |
| A10L703 | 120-1688-00 |  |  | TRANSFORMER,RF:TAPPED INDUCTOR | OJR03 | 120-1688-00 |
| A10L704 | 120-1688-00 |  |  | TRANSFORMER,RF:TAPPED INDUCTOR | OJR03 | 120-1688-00 |
| A10Q131 | 151-1042-00 |  |  | TRANSISTOR,SIG:JFET,N-CH;6V,15MA,4.5MS,IDSS (1-2) <0.5MA <br> (LOCATIONS A AND B) | 04713 | SPF627M2 |
| A10Q151 | 151-1042-00 |  |  | TRANSISTOR,SIG:JFET,N-CH;6V,15MA,4.5MS,IDSS (1-2) $<0.5 \mathrm{MA}$ <br> (LOCATIONS A AND B) | 04713 | SPF627M2 |
| A10Q171 | 151-0164-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;60V,600MA200MHZ AMPLIFIER | 04713 | MPS2907A |
| A100181 | 151-0712-00 | B110100 |  | TRANSISTOR,SIG:BIPOLAR,PNP;20V,50MA,600MHZ AMPLIFIER <br> (2246 MOD A) | 04713 | MPSH81 |
| A10Q181 | 151-0712-00 | B710100 |  | TRANSISTOR,SIG:BIPOLAR,PNP;20V,50MA,600MHZ AMPLIFIER <br> (2246 1Y) | 04713 | MPSH81 |
| A10Q182 | 151-0712-00 | B110100 |  | TRANSISTOR,SIG:BIPOLAR,PNP;20V,50MA,600MHZ AMPLIFIER <br> ( 2246 MOD A) | 04713 | MPSH81 |
| A10Q182 | 151-0712-00 | B710100 |  | TRANSISTOR,SIG:BIPOLAR,PNP;20V,50MA,600MHZ AMPLIFIER $(22461 Y)$ | 04713 | MPSH81 |
| A10Q250 | 151-0712-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP:20V,50MA,600MHZ AMPLIFIER | 04713 | MPSH81 |
| A100251 | 151-0712-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;2OV,50MA,600MHZ AMPLIFIER | 04713 | MPSH81 |
| A100252 | 151-0271-05 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;15V,30MA,2.0GHZ, AMPLIFIER | 04713 | MPSH69RLRP |
| A10Q253 | 151-0271-05 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;15V,30MA,2.0GHZ, AMPLIFIER | 04713 | MPSH69RLRP |
| A10Q284 | 151-0192-05 |  |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | SPS8801RLRP |
| A10Q285 | 151-0192-05 |  |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | SPS8801RLRP |


| Component Number | Tektronix Part No. | Serlal No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10Q301 | 151-0254-03 |  | TRANSISTOR,SIG:BIPOLAR,NPN;30V,500MA, 125MHZ,AMPLIFIER,DARLINGTON | OJR04 | MPSA14, TPE2 |
| A100302 | 151-0188-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;4OV,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A10Q303 | 151-0188-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A10Q304 | 151-0830-00 |  | TRANSISTOR:NPN,SI,AMPLIFIER,625,TO-92 MPS918 | 80009 | 151083000 |
| A10Q305 | 151-0830-00 |  | TRANSISTOR:NPN,SI,AMPLIFIER,625,TO-92 MPS918 | 80009 | 151083000 |
| A10Q306 | 151-0830-00 |  | TRANSISTOR:NPN,SI,AMPLIFIER,625,TO-92 MPS918 | 80009 | 151083000 |
| A10Q307 | 151-0829-00 |  | TRANSISTOR:PNP,SI,TO-92 | 04713 | 2N5087RLRP |
| A100308 | 151-0830-00 |  | TRANSISTOR:NPN,SI,AMPLIFIER,625,TO-92 MPS918 | 80009 | 151083000 |
| A100309 | 151-0830-00 |  | TRANSISTOR:NPN,SI,AMPLIFIER,625,TO-92 MPS918 | 80009 | 151083000 |
| A10Q310 | 151-1042-00 |  | TRANSISTOR,SIG:JFET,N-CH;6V,15MA,4.5MS,IDSS (1-2) <0.5MA <br> (LOCATIONS A AND B) | 04713 | SPF627M2 |
| A100311 | 151-0188-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A10Q312 | 151-0188-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A10Q313 | 151-0736-00 |  | TRANSISTOR:NPN,SI,TO-92 | OJR04 | 2N4401 |
| A10Q315 | 151-0188-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP:4OV,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A10Q316 | 151-0188-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;4OV,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A100317 | 151-0830-00 |  | TRANSISTOR:NPN,SI,AMPLIFIER,625,TO-92 MPS918 | 80009 | 151083000 |
| A100318 | 151-0830-00 |  | TRANSISTOR:NPN,SI,AMPLIFIER,625,TO-92 MPS918 | 80009 | 151083000 |
| A10Q320 | 151-0829-00 |  | TRANSISTOR:PNP,SI,TO-92 | 04713 | 2N5087RLRP |
| A10Q321 | 151-0830-00 |  | TRANSISTOR:NPN,SI,AMPLIFIER,625,TO-92 MPS918 | 80009 | 151083000 |
| A10Q322 | 151-0830-00 |  | TRANSISTOR:NPN,SI,AMPLIFIER,625,TO-92 MPS918 | 80009 | 151083000 |
| A10Q323 | 151-1042-00 |  | TRANSISTOR,SIG:JFET,N-CH;6V,15MA,4.5MS,IDSS (1-2) <0.5MA <br> (LOCATION A AND B) | 04713 | SPF627M2 |
| A10Q325 | 151-0188-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A10Q326 | 151-0736-00 |  | TRANSISTOR:NPN,SI,TO-92 | OJR04 | 2N4401 |
| A10Q328 | 151-0829-00 |  | TRANSISTOR:PNP,SI,TO-92 | 04713 | 2N5087RLRP |
| A10Q329 | 151-0829-00 |  | TRANSISTOR:PNP,SI,TO-92 | 04713 | 2N5087RLRP |
| A10Q330 | 151-0829-00 |  | TRANSISTOR:PNP,SI,TO-92 | 04713 | 2N5087RLRP |
| A10Q331 | 151-0829-00 |  | TRANSISTOR:PNP,SI,TO-92 | 04713 | 2N5087RLRP |
| A10Q332 | 151-0736-00 |  | TRANSISTOR:NPN,SI,TO-92 | 0JR04 | 2N4401 |
| A10Q333 | 151-0736-00 |  | TRANSISTOR:NPN,SI,TO-92 | 0JR04 | 2N4401 |
| A10Q440 | 151-0188-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP:40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix <br> Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A100444 | 151-0190-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN:40V,200MA, 300MHZ,AMPLIFIER | 04713 | 2N3904 |
| A10Q470 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250 MHZ ,AMPLIFIER | 04713 | 2N3906 |
| A100474 | 151-0190-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN:4OV,200MA, 300 MHZ ,AMPLIFIER | 04713 | 2N3904 |
| A10Q480 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;4OV,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A100600 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;4OV,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A10Q601 | 151-0424-00 |  |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | MPS2369A |
| A100602 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A100603 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A100604 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A100605 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;4OV,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A10Q606 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A100607 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;4OV,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A100608 | 151-0190-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN;4OV,200MA, 300MHZ,AMPLIFIER | 04713 | 2N3904 |
| A100701 | 151-0846-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN;30V,400MA, 800MHZ,AMPLIFIER,COMMON BASE PKG | 04713 | SRF5286 |
| A100702 | 151-0846-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN;3OV,400MA, 800MHZ,AMPLIFIER,COMMON BASE PKG | 04713 | SRF5286 |
| A100703 | 151-0190-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN:40V,200MA, 300MHZ,AMPLIFIER | 04713 | 2N3904 |
| A10Q704 | 151-0190-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN;40V,200MA, 300MHZ,AMPLIFIER | 04713 | 2N3904 |
| A100801 | 151-1212-00 | B100100 | B110099 | TRANSISTOR,PWR:MOS,P-CH;160V,0.25A,25 OHM ( 2246 MOD A,2Y) | 59640 | VP0116N2 |
| A100801 | 151-0270-00 | B110100 |  | TRANSISTOR:PNP,SI,TO-39 (2246 MOD A,2Y) | 04713 | ST919 |
| A100801 | 151-1212-00 | B700100 | B710099 | TRANSISTOR,PWR:MOS,P-CH;160V,0.25A,25 OHM (2246 1Y) | 59640 | VPO116N2 |
| A100801 | 151-0270-00 | B710100 |  | TRANSISTOR:PNP,SI,TO-39 (2246 1Y) | 04713 | ST919 |
| A100802 | 151-1211-00 | B100100 | B110099 | TRANSISTOR,PWR:MOS,N-CH;160V,0.25A,40 OHM (2246 MOD A, 2 Y ) | 59640 | VN1316N2 |
| A10Q802 | 151-0274-00 | B110100 |  | TRANSISTOR:NPN,SI,TO-5 (2246 MOD A,2Y) | 04713 | 2N3501 |
| A100802 | 151-1211-00 | B700100 | B710099 | TRANSISTOR,PWR:MOS,N-CH; 160V,0.25A,40 OHM (2246 1Y) | 59640 | VN1316N2 |
| A10Q802 | 151-0274-00 | B710100 |  | TRANSISTOR:NPN,SI,TO-5 (2246 1Y) | 04713 | 2N3501 |


| Component Number | Tektronix Part No. | Serial Effectlve | No. Dscont | Name \& Description | Mrr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10Q803 | 151-0736-00 | B100100 | B110099 | $\begin{aligned} & \text { TRANSISTOR:NPN,SI,TO-92 } \\ & \text { (2246 MOD A,2Y) } \end{aligned}$ | $0 . J R 04$ | 2N4401 |
| A10Q803 | 151-0190-00 | B110100 |  | TRANSISTOR,SIG:BIPOLAR,NPN:40V,200MA, 300MHZ,AMPLIFIER (2246 MOD A,2Y) | 04713 | 2N3904 |
| A10Q803 | 151-0736-00 | B700100 | B710099 | TRANSISTOR:NPN,SI,TO-92 (2246 1Y) | OJR04 | 2N4401 |
| A10Q803 | 151-0190-00 | B710100 |  | TRANSISTOR,SIG:BIPOLAR,NPN:40V,200MA, 300MHZ,AMPLIFIER (2246 1Y) | 04713 | 2N3904 |
| A10Q804 | 151-0712-00 | B100100 | B110099 | TRANSISTOR,SIG:BIPOLAR,PNP;20V,50MA,600MHZ AMPLIFIER <br> (2246 MOD A,2Y) | 04713 | MPSH81 |
| A100804 | 151-0190-00 | 8110100 |  | TRANSISTOR,SIG:BIPOLAR,NPN;40V,200MA, 300MHZ,AMPLIFIER <br> (2246 MOD A, 2 Y) | 04713 | 2N3904 |
| A10Q804 | 151-0712-00 | B700100 | B710099 | TRANSISTOR,SIG:BIPOLAR,PNP;20V,50MA,600MHZ AMPLIFIER (2246 1Y) | 04713 | MPSH81 |
| A100804 | 151-0190-00 | B710100 |  | TRANSISTOR,SIG:BIPOLAR,NPN:40V,200MA, 300 MHZ ,AMPLIFIER <br> (2246 1Y) | 04713 | 2N3904 |
| A10Q805 | 151-1211-00 | B100100 | B110099 | TRANSISTOR,PWR:MOS,N-CH;160V,0.25A, 40 OHM (2246 MOD A,2Y) | 59640 | VN1316N2 |
| A10Q805 | 151-0270-00 | B110100 |  | TRANSISTOR:PNP,SI,TO-39 (2246 MOD A,2Y) | 04713 | ST919 |
| A10Q805 | 151-1211-00 | B700100 | B710099 | TRANSISTOR,PWR:MOS,N-CH;160V,0.25A, 40 OHM (2246 1Y) | 59640 | VN1316N2 |
| A10Q805 | 151-0270-00 | B710100 |  | TRANSISTOR:PNP,SI,TO-39 (2246 1Y) | 04713 | ST919 |
| A100806 | 151-1212-00 | B100100 | B110099 | TRANSISTOR,PWR:MOS,P-CH;160V,0.25A,25 OHM (2246 MOD A,2Y) | 59640 | VP0116N2 |
| A10Q806 | 151-0274-00 | B110100 |  | TRANSISTOR:NPN,SI,TO-5 (2246 MOD A,2Y) | 04713 | 2N3501 |
| A10Q806 | 151-1212-00 | B700100 | B710099 | ```TRANSISTOR,PWR:MOS,P-CH;160V,0.25A,25 OHM (2246 1Y)``` | 59640 | VP0116N2 |
| A10Q806 | 151-0274-00 | B710100 |  | TRANSISTOR:NPN,SI,TO-5 (2246 1Y) | 04713 | 2N3501 |
| A10Q807 | 151-0164-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP:60V,600MA, 200MHZ,AMPLIFIER | 04713 | MPS2907A |
| A10Q808 | 151-0711-00 | B100100 | B110099 | TRANSISTOR,SIG:BIPOLAR,NPN;25V,50MA,650MHZ. AMPLIFIER <br> (2246 MOD A,2Y) | 04713 | MPSH10 |
| A10Q808 | 151-0711-00 | B700100 | B710099 | TRANSISTOR,SIG:BIPOLAR,NPN:25V,50MA,650MHZ AMPLIFIER (2246 1Y) | 04713 | MPSH10 |
| A10Q809 | 151-0711-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN;25V,50MA,650MHZ AMPLIFIER | 04713 | MPSH10 |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A100810 | 151-0711-00 |  | TRANSISTOR,SIG:BIPOLAR,NPN:25V,50MA,650MHZ AMPLIFIER | 04713 | MPSH10 |
| A100811 | 151-0190-00 | B110100 | TRANSISTOR,SIG:BIPOLAR,NPN:40V,200MA, 300MHZ,AMPLIFIER (2246 MOD A,2Y) | 04713 | 2N3904 |
| A100811 | 151-0190-00 | B710100 | TRANSISTOR,SIG:BIPOLAR,NPN;40V,200MA, 300MHZ,AMPLIFIER $(2246 \text { 1Y) }$ | 04713 | 2N3904 |
| A100812 | 151-0220-00 | B110100 | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 400MHZ,AMPLIFIER <br> (2246 MOD A,2Y) | 2 C 532 | PTBTC105BK |
| A100812 | 151-0220-00 | B710100 | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 400MHZ,AMPLIFIER $(2246 \mathrm{TV})$ | 2 D 532 | PTBTC105BK |
| A100905 | 151-0622-00 |  | TRANSISTOR:PNP,SI,40V,1A,TO-226AE/237 2 N6727 | 04713 | MPS6727 |
| A100907 | 151-0622-00 |  | TRANSISTOR:PNP,SI,40V,1A,TO-226AE/237 2N6727 | 04713 | MPS6727 |
| A100908 | 151-0622-00 |  | TRANSISTOR:PNP,SI,40V,1A,TO-226AE/237 2N6727 | 04713 | MPS6727 |
| A1001001 | 151-0424-00 |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | MPS2369A |
| A10Q1002 | 151-0424-00 |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | MPS2369A |
| A10Q1003 | 151-0424-00 |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | MPS2369A |
| A10Q1004 | 151-0424-00 |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | MPS2369A |
| A10Q1005 | 151-0216-04 |  | TRANSISTOR,SIG:BIPOLAR,PNP;25V,100MA. 170MHZ,AMPLIFIER | 04713 | MPS6523RLRP |
| A10Q1101 | 151-0216-04 |  | TRANSISTOR,SIG:BIPOLAR,PNP;25V,100MA, 170MHZ,AMPLIFIER | 04713 | MPS6523RLRP |
| A1001102 | 151-0192-05 |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | SPS8801RLRP |
| A10Q1103 | 151-0216-04 |  | TRANSISTOR,SIG:BIPOLAR,PNP;25V,100MA, 170MHZ,AMPLIFIER | 04713 | MPS6523RLRP |
| A10Q1104 | 151-0192-05 |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | SPS8801RLRP |
| A10Q1105 | 151-0216-04 |  | TRANSISTOR,SIG:BIPOLAR,PNP:25V,100MA, 170MHZ,AMPLIFIER | 04713 | MPS6523RLRP |
| A10Q1106 | 151-0192-05 |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | SPS8801RLRP |
| A10Q2701 | 151-0164-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;60V,600MA, 200MHZ,AMPLIFIER | 04713 | MPS2907A |
| A10Q2702 | 151-0164-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;60V,600MA, 200MHZ,AMPLIFIER | 04713 | MPS2907A |
| A10Q2703 | 151-0736-00 |  | TRANSISTOR:NPN,SI,TO-92 | OJR04 | 2N4401 |
| A10Q2704 | 151-0736-00 |  | TRANSISTOR:NPN,SI,TO-92 | OJR04 | 2N4401 |
| A10Q2705 | 151-0192-05 |  | TRANSISTOR:NPN,SI,TO-92 | 04713 | SPS8801RLRP |
| A10Q2706 | 151-0190-00 |  | TRANSISTOR,SIG:BIPOLAR,NPN;40V,200MA, 300MHZ,AMPLIFIER | 04713 | 2N3904 |
| A10Q2707 | 151-0190-00 |  | TRANSISTOR,SIG:BIPOLAR,NPN;40V,200MA, $300 \mathrm{MHZ}, \mathrm{AMPLIFIER}$ | 04713 | 2N3904 |
| A10Q2708 | 151-0188-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A10Q2709 | 151-0188-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A10Q2711 | 151-0199-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;12V,8OMA, SWITCHING | 04713 | MPS3640 |
| A10Q2712 | 151-0347-02 |  | TRANSISTOR,SIG:BIPOLAR,NPN:160V,600MA, 100MHZ,AMPLIFIER | 04713 | 2N5551 RLRP (AM |
| A10Q2713 | 151-0350-00 |  | TRANSISTOR,SIG:BIPOLAR,PNP;150V,600MA, 100MHZ,AMPLIFIER | OJR04 | TO BE ASSIGNED |


| Component Number | Tektronlx <br> Part No. | Serlal Effectlve | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10Q2715 | 151-0190-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN;40V,200MA, 300MHZ,AMPLIFIER | 04713 | 2N3904 |
| A10R101 | 313-1822-00 |  |  | RES,FXD,FILM:8.2K,OHM,5\%,0.2W | 91637 | CCF50-2-82000J |
| A10R102 | 313-1822-00 |  |  | RES,FXD,FILM:8.2K,OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-82000J |
| A10R103 | 313-1822-00 |  |  | RES,FXD,FILM:8.2K,OHM,5\%,0.2W | 91637 | CCF50-2-82000J |
| A10R104 | 313-1822-00 |  |  | RES,FXD,FILM:8.2K,OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-82000J |
| A10R105 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A10R106 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R107 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A10R108 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A10R111 | 315-0620-00 |  |  | RES,FXD,FILM:62 OHM,5\%,0.25W | TK1727 | SFR25 2322-181- |
| A10R113 | 313-1200-00 |  |  | RES,FXD,FILM:20 OHM,5\%,0.2W | 91637 | CCF50-2-20R00J |
| A10R114 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM, 5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10R115 | 322-3085-00 |  |  | RES,FXD,FILM: 75 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G75R00F |
| A10R121 | 315-0620-00 |  |  | RES,FXD,FILM:62 OHM,5\%,0.25W | TK1727 | SFR25 2322-181- |
| A10R123 | 313-1200-00 |  |  | RES,FXD,FILM:20 OHM,5\%,0.2W | 91637 | CCF50-2-20R00J |
| A10R124 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10R125 | 322-3085-00 |  |  | RES,FXD,FILM: 75 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G75R00F |
| A10R131 | 315-0390-00 | B100100 | B119999 | RES,FXD,FILM:39 OHM,5\%,0.25W (2246 MOD A,2Y) | TK1727 | SFR25 2322-181- |
| A10R131 | 315-0620-00 | B120000 |  | RES,FXD,FILM: 62 OHM, $5 \%, 0.25 \mathrm{~W}$ (2246 MOD A, 2 Y ) | TK1727 | SFR25 2322-181- |
| A10R131 | 315-0390-00 | B700100 | B714999 | $\begin{aligned} & \text { RES,FXD,FILM:39 OHM,5\%,0.25W } \\ & (2246 ~ 1 Y) \end{aligned}$ | TK1727 | SFR25 2322-181- |
| A10R131 | 315-0620-00 | B715000 |  | $\begin{aligned} & \text { RES,FXD,FILM: } 62 \text { OHM, } 5 \%, 0.25 \mathrm{~W} \\ & (22461 Y) \end{aligned}$ | TK1727 | SFR25 2322-181- |
| A10R132 | 322-3443-00 |  |  | RES,FXD,FILM:402K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G40202F |
| A10R133 | 322-3443-00 |  |  | RES,FXD,FILM:402K OHM,1\%,0.2W,TC = TOMI | 91637 | CCF501G40202F |
| A10R134 | 322-3414-00 |  |  | RES,FXD,FILM:200K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G20002F |
| A10R135 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10R136 | 322-3284-00 |  |  | RES,FXD,FILM:8.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G88700F |
| A10R137 | 322-3217-00 |  |  | RES,FXD,FILM:1.78K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G17800F |
| A10R138 | 322-3210-00 |  |  | RES,FXD,FILM:1.5K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G15000F |
| A10R139 | 322-3085-00 |  |  | RES,FXD,FILM: 75 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G75R00F |
| A10R140 | 322-3085-00 |  |  | RES,FXD,FILM: 75 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G75R00F |
| A10R141 | 311-2224-00 |  |  | RES,VAR,NONWW:TRMR,20 OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 200 ML |
| A10R142 | 322-3056-00 |  |  | RES,FXD,FILM:37.4 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF50-2F37R40F |
| A10R151 | 315-0390-00 | B100100 | B119999 | RES,FXD,FILM:39 OHM,5\%,0.25W (2246 MOD A,2Y) | TK1727 | SFR25 2322-181- |
| A10R151 | 315-0620-00 | B120000 |  | RES,FXD,FILM: 62 OHM, $5 \%, 0.25 \mathrm{~W}$ ( 2246 MOD A,2Y) | TK1727 | SFR25 2322-181- |
| A10R151 | 315-0390-00 | B700100 | B714999 | RES,FXD,FILM:39 ОHM,5\%,0.25W (2246 1Y) | TK1727 | SFR25 2322-181- |


| Component Number | Tektronix <br> Part No. | Serial Effective | No. Dscont | Name \& Descriptlon | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R151 | 315-0620-00 | B715000 |  | RES,FXD,FILM:62 OHM,5\%,0.25W (2246 1Y) | TK1727 | SFR25 2322-181- |
| A10R152 | 322-3443-00 |  |  | RES,FXD,FILM:402K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G40202F |
| A10R153 | 322-3443-00 |  |  | RES,FXD,FILM:402K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G40202F |
| A10R154 | 322-3414-00 |  |  | RES,FXD,FILM:200K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G20002F |
| A10R155 | 313-1100-00 |  |  | RES,FXD,FILM: 10 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10R00J |
| A10R156 | 322-3284-00 |  |  | RES, FXD, FILM: 8.87 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G88700F |
| A10R157 | 322-3217-00 |  |  | RES, FXD,FILM:1.78K ОHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G17800F |
| A10R158 | 322-3210-00 |  |  | RES,FXD,FILM:1.5K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G15000F |
| A10R159 | 322-3085-00 |  |  | RES,FXD,FILM: 75 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G75R00F |
| A10R160 | 322-3085-00 |  |  | RES,FXD,FILM: 75 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G75R00F |
| A10R161 | 311-2224-00 |  |  | RES,VAR,NONWW:TRMR, 20 OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 200 ML |
| A10R162 | 322-3056-00 |  |  | RES,FXD,FILM:37.4 OHM, $1 \%, 0.2 \mathrm{~W}$, TC = TOMI | 91637 | CCF50-2F37R40F |
| A10R171 | 313-1471-00 |  |  | RES,FXD,FILM:470 OHM,5\%,0.2W | 91637 | CCF50-2-470ROJ |
| A10R175 | 313-1204-00 |  |  | RES,FXD,FILM: $200 \mathrm{~K}, 5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-20002 J |
| A10R176 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A10R177 | 313-1472-00 |  |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000」 |
| A10R178 | 313-1472-00 |  |  | RES,FXD,FILM: 4.7 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000 J |
| A10R179 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R180 | 313-1101-00 |  |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R181 | 313-1101-00 |  |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A10R182 | 308-0058-00 |  |  | RES,FXD,WW:1.5 OHM, 10\%,1W | 75042 | SP-20-1R500K |
| A10R185 | 313-1512-00 | B110100 |  | RES,FXD,FILM:5.1K OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-51000J |
| A10R185 | 313-1512-00 | B710100 |  | RES,FXD,FILM:5.1K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-51000J |
| A10R188 | 313-1512-00 | B110100 |  | RES,FXD,FILM:5.1K OHM,5\%,0.2W ( 2246 MOD A,2Y) | 91637 | CCF50-2-51000J |
| A10R188 | 313-1512-00 | B710100 |  | RES,FXD,FILM:5.1K OHM,5\%,0.2W $(2246 \text { 1Y) }$ | 91637 | CCF50-2-51000 J |
| A10R201 | 322-3193-00 |  |  | RES,FXD,FILM:1K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G10000F |
| A10R202 | 322-3193-00 |  |  | RES,FXD,FILM:1K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G10000F |
| A10R203 | 322-3193-00 |  |  | RES,FXD,FILM: 1 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G10000F |
| A10R204 | 322-3193-00 |  |  | RES,FXD,FILM: 1 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G10000F |
| A10R205 | 322-3150-00 |  |  | RES,FXD,FILM:357 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G357R0F |
| A10R206 | 322-3236-00 |  |  | RES,FXD,FILM:2.8K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G28000F |
| A10R207 | 322-3150-00 |  |  | RES,FXD,FILM:357 OHM, 1\%,0.2W,TC = TOMI | 91637 | CCF501G357ROF |
| A10R208 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10R00J |
| A10R209 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM,5\%,0.2W | 91637 | CCF50-2-510ROJ |
| A10R210 | 313-1331-00 |  |  | RES,FXD,FILM:330 OHM,5\%,0.2W | 91637 | CCF50-2-330ROJ |
| A10R211 | 311-2329-00 | B100100 | B110099 | RES,VAR,NONWW:TRIMMER,5K OHM,10\% (2246 MOD A,2Y) | 32997 | 3386R-EA5-502 |
| A10R211 | 311-2454-00 | B110100 | B119999 | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W,LOG ( 2246 MOD A,2Y) | 32997 | 3362U-EP6-502R |
| A10R211 | 311-2232-00 | B120000 |  | RES,VAR,NONWW:TRMR,2K OHM,20\%,0.5W LINEAR <br> (2246 MOD A, 2 Y) | TK2073 | GF06UT2 202 ML |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effectlve | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R211 | 311-2329-00 | B700100 | B710099 | RES,VAR,NONWW:TRIMMER,5K OHM,10\% (2246 1Y) | 32997 | 3386R-EA5-502 |
| A10R211 | 311-2454-00 | B710100 | B714999 | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W,LOG (2246 1Y) | 32997 | 3362U-EP6-502R |
| A10R211 | 311-2232-00 | B715000 |  | RES,VAR,NONWW:TRMR,2K OHM, $20 \%, 0.5 \mathrm{~W}$ LINEAR <br> (2246 1Y) | TK2073 | GF06UT2 202 M L |
| A10R212 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM,5\%,0.2W | 91637 | CCF50-2-510ROJ |
| A10R213 | 313-1243-00 |  |  | RES,FXD,FILM:24K OHM,5\%,0.2W | 57668 | CCF50-1-G2402J |
| A10R214 | 322-3285-00 |  |  | RES,FXD,FILM:9.09K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G90900F |
| A10R215 | 313-1821-00 |  |  | RES,FXD,FILM:820 OHM,5\%,0.2W | 91637 | CCF50-2-820ROJ |
| A10R216 | 313-1103-00 | B120000 |  | RES,FXD,FILM:10K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10001J |
| A10R216 | 313-1103-00 | 8715000 |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-10001J |
| A10R217 | 313-1472-00 | B120000 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-47000J |
| A10R217 | 313-1472-00 | B715000 |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-47000J |
| A10R218 | 322-3237-00 |  |  | RES,FXD,FILM: 2.87 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G28700F |
| A10R219 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R220 | 313-1331-00 |  |  | RES,FXD,FILM:330 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-330ROJ |
| A10R221 | 311-2329-00 | B100100 | B110099 | RES,VAR,NONWW:TRIMMER,5K OHM,10\% (2246 MOD A,2Y) | 32997 | 3386R-EA5-502 |
| A10R221 | 311-2454-00 | B110100 | B119999 | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W,LOG (2246 MOD A,2Y) | 32997 | 3362U-EP6-502R |
| A10R221 | 311-2232-00 | B120000 |  | RES,VAR,NONWW:TRMR,2K OHM,20\%,0.5W LINEAR <br> (2246 MOD A,2Y) | TK2073 | GF06UT2 202 ML |
| A10R221 | 311-2329-00 | 8700100 | B710099 | RES,VAR,NONWW:TRIMMER.5K OHM,10\% (2246 1Y) | 32997 | 3386R-EA5-502 |
| A10R221 | 311-2454-00 | B710100 | B714999 | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W,LOG (2246 1Y) | 32997 | 3362U-EP6-502R |
| A10R221 | 311-2232-00 | B715000 |  | RES,VAR,NONWW:TRMR,2K OHM, $20 \%, 0.5 \mathrm{~W}$ LINEAR <br> (2246 1Y) | TK2073 | GF06UT2 202 ML |
| A10R222 | 313-1511-00 |  |  | RES,FXD,FILM: 510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A10R223 | 313-1243-00 |  |  | RES,FXD,FILM: 24 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 57668 | CCF50-1-G2402J |
| A10R224 | 322-3285-00 |  |  | RES,FXD,FILM: 9.09 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G90900F |
| A10R225 | 313-1472-00 |  |  | RES, FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000J |
| A10R226 | 313-1027-00 |  |  | RES,FXD,FILM:2.7 OHM,5\%,0.2W | 57668 | TR20JE 02E7 |
| A10R227 | 313-1472-00 |  |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000J |
| A10R228 | 322-3237-00 |  |  | RES, FXD,FILM: 2.87 K OHM, $1 \%, 0.2 \mathrm{~W}$, TC $=$ TOMI | 91637 | CCF501G28700F |
| A10R229 | 313-1104-00 |  |  | RES,FXD,FILM:100K OHM,5\%,0.2W | 91637 | CCF50-2-10002J |
| A10R230 | 313-1331-00 |  |  | RES,FXD,FILM:330 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-330ROJ |
| A10R231 | 311-2329-00 | B100100 | 8110099 | RES,VAR,NONWW:TRIMMER,5K OHM,10\% (2246 MOD A, 2 Y) | 32997 | 3386R-EA5-502 |


| Component Number | Tektronix Part No. | Serial Effectlve | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R231 | 311-2454-00 | B110100 | B119999 | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W,LOG (2246 MOD A,2Y) | 32997 | 3362U-EP6-502R |
| A10R231 | 311-2232-00 | B120000 |  | RES,VAR,NONWW:TRMR, 2 K OHM, $20 \%, 0.5 \mathrm{~W}$ LINEAR <br> (2246 MOD A,2Y) | TK2073 | GF06UT2 202 ML |
| A10R231 | 311-2329-00 | B700100 | B710099 | RES,VAR,NONWW:TRIMMER,5K OHM,10\% (2246 1Y) | 32997 | 3386R-EA5-502 |
| A10R231 | 311-2454-00 | B710100 | B714999 | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W,LOG (2246 1Y) | 32997 | 3362U-EP6-502R |
| A10R231 | 311-2232-00 | B715000 |  | RES,VAR,NONWW:TRMR,2K OHM,20\%,0.5W LINEAR <br> (2246 1Y) | TK2073 | GF06UT2 202 M L |
| A10R232 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A10R233 | 313-1243-00 |  |  | RES,FXD,FILM:24K OHM, $5 \%, 0.2 \mathrm{~W}$ | 57668 | CCF50-1-G2402J |
| A10R234 | 322-3285-00 |  |  | RES,FXD,FILM: 9.09 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G90900F |
| A10R235 | 322-3237-00 |  |  | RES,FXD,FILM:2.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOM | 91637 | CCF501G28700F |
| A10R238 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10RO0J |
| A10R240 | 313-1331-00 |  |  | RES,FXD,FILM:330 OHM, 5\%,0.2W | 91637 | CCF50-2-330ROJ |
| A10R241 | 311-2329-00 | B100100 | B110099 | RES,VAR,NONWW:TRIMMER,5K OHM,10\% (2246 MOD A,2Y) | 32997 | 3386R-EA5-502 |
| A10R241 | 311-2454-00 | B110100 | B119999 | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W,LOG (2246 MOD A,2Y) | 32997 | 3362U-EP6-502R |
| A10R241 | 311-2232-00 | B120000 |  | RES,VAR,NONWW:TRMR,2K OHM,20\%,0.5W LINEAR <br> (2246 MOD A.2Y) | TK2073 | GF06UT2 202 ML |
| A10R241 | 311-2329-00 | B700100 | B710099 | RES,VAR,NONWW:TRIMMER,5K OHM,10\% (2246 1Y) | 32997 | 3386R-EA5-502 |
| A10R241 | 311-2454-00 | B710100 | B714999 | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W,LOG (2246 1Y) | 32997 | 3362U-EP6-502R |
| A10R241 | 311-2232-00 | B715000 |  | RES,VAR,NONWW:TRMR,2K OHM, $20 \%, 0.5 \mathrm{~W}$ LINEAR <br> (2246 1Y) | TK2073 | GF06UT2 202 M L |
| A10R242 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A10R243 | 313-1243-00 |  |  | RES,FXD,FILM:24K OHM, $5 \%, 0.2 \mathrm{~W}$ | 57668 | CCF50-1-G2402J |
| A10R244 | 322-3285-00 |  |  | RES,FXD,FILM:9.09K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOM 1 | 91637 | CCF501G90900F |
| A10R245 | 313-1027-00 |  |  | RES,FXD,FILM:2.7 OHM,5\%,0.2W | 57668 | TR20JE 02E7 |
| A10R248 | 322-3237-00 |  |  | RES,FXD,FILM:2.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOM | 91637 | CCF501G28700F |
| A10R249 | 313-1027-00 | B120000 |  | RES,FXD,FILM:2.7 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 57668 | TR20JE 02E7 |
| A10R249 | 313-1027-00 | B715000 |  | RES,FXD,FILM:2.7 OHM,5\%,0.2W (2246 1Y) | 57668 | TR20JE 02E7 |
| A10R250 | 307-0792-00 | B100100 | B119999 | RES NTWK,FXD,FI:7,82 OHM, $2 \%, 0.15 W$ EACH (2246 MOD A,2Y) | 91637 | MSP08A01820G OR |
| A10R250 | 307-0792-00 | B700100 | B714999 | RES NTWK,FXD,FI:7,82 OHM,2\%,0.15W EACH (2246 1Y) | 91637 | MSP08A01820G OR |
| A10R251 | 307-0792-00 | B100100 | B119999 | RES NTWK,FXD,FI:7,82 OHM,2\%,0.15W EACH (2246 MOD A,2Y) | 91637 | MSP08A01820G OR |
| A10R251 | 307-0792-00 | B700100 | B714999 | RES NTWK,FXD,FI:7,82 OHM,2\%,0.15W EACH (2246 1Y) | 91637 | MSP08A01820G OR |
| A10R254 | 322-3318-00 |  |  | RES,FXD,FILM:20K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G20001F |
| A10R255 | 322-3318-00 |  |  | RES,FXD,FILM:ZOK OHM $, 1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G20001F |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R256 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A10R260 | 311-2234-00 |  |  | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 502 M L |
| A10R261 | 313-1243-00 | B100100 | 8119999 | RES,FXD,FILM:24K OHM,5\%,0.2W (2246 MOD A,2Y) | 57668 | CCF50-1-G2402J |
| A10R261 | 313-1473-00 | B120000 |  | RES,FXD,FILM:47K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-47001J |
| A10R261 | 313-1243-00 | 8700100 | B714999 | RES,FXD,FILM:24K OHM,5\%,0.2W (2246 1V) | 57668 | CCF50-1-G2402J |
| A10R261 | 313-1473-00 | B715000 |  | RES,FXD,FILM:47K OHM,5\%,0.2W (2246 1V) | 91637 | CCF50-2-47001J |
| A10R262 | 322-3083-00 |  |  | RES, FXD, FILM:71.5 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G71R50F |
| A10R263 | 322-3083-00 |  |  | RES,FXD,FILM:71.5 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G71R50F |
| A10R264 | 322-3083-00 |  |  | RES,FXD, FILM:71.5 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G71R50F |
| A10R265 | 322-3083-00 |  |  | RES,FXD,FILM:71.5 OHM, $1 \%, 0.2 \mathrm{~W}$, TC = TOMI | 91637 | CCF501G71R50F |
| A10R266 | 313-1302-00 |  |  | RES,FXD,FILM:3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-30000J |
| A10R267 | 322-3164-00 |  |  | RES,FXD, FILR: 499 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G499ROF |
| A10R268 | 322-3158-00 |  |  | RES,FXD,FILM: $432 \mathrm{OHM}, 1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF 501 G432 Q0 |
| A10R269 | 322-3158-00 |  |  | RES,FXD,FILM:432 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF 501 G432 Q0 |
| A10R270 | 313-1751-00 |  |  | RES,FXD,FILM:750 OHM,5\%,0.2W | 91637 | CCF50-2-750ROJ |
| A10R271 | 313-1912-00 |  |  | RES,FXD,FILM:9.1K OHM, 5\%,0.2W | 91637 | CCF501G9100ROF |
| A10R272 | 311-2232-00 |  |  | RES,VAR,NONWW:TRMR,2K OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 202 ML |
| A10R273 | 311-2230-00 |  |  | RES,VAR,NONWW:TRMR, 500 OHM, $20 \%, 0.50$ LINEAR | TK2073 | GF06UT2 501 ML |
| A10R274 | 313-1100-00 |  |  | RES,FXD,FILM: 10 OHM,5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10R275 | 311-2227-00 |  |  | RES,VAR,NONWW:TRMR, 100 OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 101 M L |
| A10R276 | 322-3213-00 |  |  | RES,FXD,FILM:1.62K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G16200F |
| A10R277 | 322-3213-00 |  |  | RES,FXD,FILM:1.62K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G16200F |
| A10R278 | 322-3141-00 |  |  | RES,FXD,FILM: $287 \mathrm{OHM}, 1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G287ROF |
| A10R279 | 322-3141-00 |  |  | RES,FXD,FILM:287 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G287ROF |
| A10R280 | 322-3098-00 |  |  | RES,FXD,FILM:102 OHM, $1 \%, 0.2 \mathrm{~W}$, TC = TOMI | 91637 | CCF501G102ROF |
| A10R281 | 322-3098-00 |  |  | RES,FXD,FILM:102 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ = TOMI | 91637 | CCF501G102ROF |
| A10R282 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10R283 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10ROOJ |
| A10R284 | 313-1393-00 |  |  | RES,FXD,FILM:39K OHM,5\%,0.2W | 91637 | CCF50-2-39001J |
| A10R285 | 313-1393-00 |  |  | RES,FXD,FILM:39K OHM,5\%,0.2W | 91637 | CCF50-2-39001J |
| A10R286 | 322-3097-00 |  |  | RES,FXD,FILM: 100 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G100R0F |
| A10R287 | 322-3097-00 |  |  | RES,FXD,FILM:100 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G100R0F |
| A10R288 | 322-3193-00 |  |  | RES,FXD,FILM:1K OHM, $1 \%, 0.2 W, T C=$ T0 | 91637 | CCF501G10000F |
| A10R289 | 322-3193-00 |  |  | RES,FXD,FILM:1K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G10000F |
| A10R290 | 322-3123-00 |  |  | RES,FXD,FILM: $187 \mathrm{OHM}, 1 \%, 0.2 W, T C=$ TOMI | 94637 | CCF501G187R0F |
| A10R291 | 322-3123-00 |  |  | RES,FXD,FILM: $187 \mathrm{OHM}, 1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G187R0F |
| A10R292 | 313-1752-00 |  |  | RES,FXD,FILM:7.5K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-75000J |
| A10R293 | 313-1752-00 |  |  | RES,FXD,FILM:7.5K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-75000J |
| A10R294 | 313-1202-00 |  |  | RES,FXD,FILM:2K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-20000J |
| A10R295 | 313-1302-00 |  |  | RES,FXD,FILM:3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-30000J |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10R296 | 322-3117-00 |  | RES,FXD,FILM:162 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ = TOMI | 91637 | CCF501G162R0F |
| A10R297 | 313-1100-00 |  | RES, FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10R298 | 313-1027-00 |  | RES,FXD,FILM:2.7 OHM,5\%,0.2W | 57668 | TR20JE 02E7 |
| A10R299 | 313-4204-00 |  | RES,FXD,FILM: $200 \mathrm{~K}, 5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-20002J |
| A10R301 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A10R302 | 313-1027-00 |  | RES,FXD,FILM:2.7 OHM, $5 \%, 0.2 \mathrm{~W}$ | 57668 | TR20JE 02E7 |
| A10R303 | 313-1102-00 |  | RES,FXD,FILM: 1 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A10R304 | 313-1470-00 |  | RES,FXD,FILM:47 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47ROOJ |
| A10R305 | 313-1511-00 |  | RES,FXD,FILM: 510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A10R306 | 313-1051-00 |  | RES,FXD,FILM:5.1 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CT3-5R100J |
| A10R307 | 322-3328-02 |  | RES,FXD,FILM: 25.5 K OHM, $0.5 \%, 0.2 \mathrm{~W}, \mathrm{TC}=\mathrm{T} 2$ | 91637 | CCF501D25501D |
| A10R308 | 322-3319-02 |  | RES,FXD,FILM:20.5K OHM, $0.5 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T2 | 91637 | CCF501D20501D |
| A10R309 | 322-3289-00 |  | RES,FXD,FILM: 10 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF50G10001F |
| A10R310 | 313-1473-00 |  | RES,FXD,FILM:47K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47001J |
| A10R311 | 322-3269-02 |  | RES,FXD,FILM:6.19K OHM, $0.2 \mathrm{~W}, 5 \%$ | 91637 | CCF501D61900D |
| A10R312 | 313-1100-00 |  | RES,FXD,FILM: 10 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10R00.J |
| A10R313 | 307-2132-00 |  | RES NTWK,FXD,FI:REF VOLTAGE DIVIDER | 80009 | 307213200 |
| A10R314 | 322-3333-02 |  | RES,FXD,FILM:28.7K OHM,0.2W, $5 \%$ | 91637 | CCF501D28701D |
| A10R315 | 313-1470-00 |  | RES,FXD,FILM:47 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47R00」 |
| A10R316 | 313-1270-00 |  | RES,FXD,FILM:27 OHM 5\%,0.2W | 91637 | CCF50-2-27R00J |
| A10R317 | 313-1101-00 |  | RES, FXD,FILM: 100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A10R318 | 313-1681-00 |  | RES, FXD,FILM:680 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-680ROJ |
| A10R319 | 313-1562-00 |  | RES,FXD,FILM:5.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-56000 J |
| A10R320 | 313-1470-00 |  | RES,FXD,FILM:47 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47ROOJ |
| A10R321 | 307-2133-00 |  | RES NTWK, FXD, FI:RESISTOR NETWORK | 80009 | 307213300 |
| A10R322 | 313-1512-00 |  | RES,FXD,FILM:5.1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-51000 J |
| A10R323 | 313-1512-00 |  | RES,FXD,FILM: 5.1 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-51000 J |
| A10R325 | 313-1132-00 |  | RES,FXD,FILM:1.3K OHM,5\%,0.2W | 91637 | CCF50-2-13000 J |
| A10R326 | 313-1132-00 |  | RES, FXD, FILM:1.3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-13000J |
| A10R327 | 313-1470-00 |  | RES,FXD,FILM:47 OHM,5\%,0.2W | 91637 | CCF50-2-47ROOJ |
| A10R328 | 313-1101-00 |  | RES,FXD,FILM: $100 \mathrm{OHM}, 5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R329 | 313-1101-00 |  | RES,FXD,FILM:100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R330 | 313-1101-00 |  | RES,FXD,FILM:100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R331 | 313-1392-00 |  | RES,FXD,FILM:3.9K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-39000 J |
| A10R332 | 313-1820-00 |  | RES,FXD,FILM:82 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-82R00J |
| A10R333 | 313-1120-00 |  | RES,FXD,FILM:12 OHM,5\%,0.2W | 91637 | CCF50-2-12R00J |
| A10R334 | 313-1151-00 |  | RES,FXD,FILM:150 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-150ROJ |
| A10R335 | 313-1162-00 |  | RES,FXD,FILM:1.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-16000 J |
| A10R336 | 313-1162-00 |  | RES,FXD,FILM:1.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-16000 J |
| A10R337 | 313-1151-00 |  | RES,FXD,FILM:150 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-150ROJ |
| A10R338 | 313-1132-00 |  | RES,FXD,FILM:1.3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-13000. |
| A10R339 | 313-1100-00 |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10ROOJ |
| A10R340 | 313-1820-00 |  | RES,FXD,FILM:82 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-82ROOJ |
| A10R341 | 313-1162-00 |  | RES,FXD,FILM:1.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-16000 J |
| A10R342 | 313-1132-00 |  | RES,FXD,FILM:1.3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-13000J |


| Component Number | Tektronlx Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R343 | 313-1162-00 |  |  | RES,FXD,FILM:1.6K OHM,5\%,0.2W | 91637 | CCF50-2-16000J |
| A10R344 | 313-1332-00 |  |  | RES,FXD,FILM:3.3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-33000J |
| A10R345 | 313-1100-00 |  |  | RES,FXD,FILM: 10 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10R00J |
| A10R346 | 313-1101-00 |  |  | RES,FXD,FILM:100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R347 | 313-1202-00 |  |  | RES,FXD,FILM:2K OHM,5\%,0.2W | 91637 | CCF50-2-20000J |
| A10R348 | 322-3193-00 |  |  | RES,FXD,FILM: 1 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G10000F |
| A10R349 | 322-3193-00 |  |  | RES,FXD,FILM:1K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G10000F |
| A10R350 | 307-0540-00 |  |  | RES NTWK,FXD,Fl:(5) 1 K OHM, $2 \%, 0.7 \mathrm{~W}$ | 11236 | 770-61-R1K OR 7 |
| A10R351 | 315-0155-00 | B100100 | B100823 | RES,FXD,FILM:1.5M OHM, $5 \%, 0.25 \mathrm{~W}$ (2246 MOD A,2Y) | TK1727 | SFR25 2322-181- |
| A10R352 | 322-3193-00 |  |  | RES,FXD,FILM:1K OHM, 1\%,0.2W,TC = T0 | 91637 | CCF501G10000F |
| A10R353 | 313-1152-00 |  |  | RES,FXD,FILM:1.5K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-15000J |
| A10R354 | 313-1101-00 |  |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A10R355 | 313-1681-00 |  |  | RES,FXD,FILM:680 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-680ROJ |
| A10R356 | 313-1562-00 |  |  | RES,FXD,FILM:5.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-56000J |
| A10R357 | 307-0540-00 |  |  | RES NTWK,FXD,FI:(5)1K OHM, $2 \%, 0.7 \mathrm{~W}$ | 11236 | 770-61-R1K OR 7 |
| A10R358 | 313-1561-00 |  |  | RES,FXD,FILM:560 OHM,5\%,0.2W | 91637 | CCF50-2-560ROJ |
| A10R359 | 313-1162-00 |  |  | RES,FXD,FILM:1.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-16000J |
| A10R360 | 313-1162-00 |  |  | RES,FXD,FILM:1.6K OHM,5\%,0.2W | 91637 | CCF50-2-16000J |
| A10R361 | 313-1200-00 |  |  | RES,FXD,FILM:20 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-20R00J |
| A10R362 | 313-1392-00 |  |  | RES,FXD,FILM:3.9K ОНM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-39000 J |
| A10R363 | 313-1332-00 |  |  | RES,FXD,FILM:3.3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-33000 J |
| A10R364 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM,5\%,0.2W | 91637 | CCF50-2-510ROJ |
| A10R365 | 313-1132-00 |  |  | RES,FXD,FILM:1.3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-13000J |
| A10R366 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A10R367 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000 J |
| A10R369 | 313-1331-00 |  |  | RES,FXD,FILM:330 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-330ROJ |
| A10R370 | 313-1820-00 |  |  | RES,FXD,FILM:82 OHM,5\%,0.2W | 91637 | CCF50-2-82R00J |
| A10R371 | 313-1120-00 |  |  | RES,FXD,FILM:12 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-12R00J |
| A10R372 | 313-1200-00 |  |  | RES,FXD,FILM:20 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-20R00J |
| A10R373 | 313-1820-00 |  |  | RES,FXD,FILM:82 OHM,5\%,0.2W | 91637 | CCF50-2-82R00J |
| A10R374 | 313-1100-00 |  |  | RES,FXD,FILM: 10 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10R00J |
| A10R375 | 313-1101-00 |  |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R376 | 313-1332-00 |  |  | RES,FXD,FILM:3.3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-33000J |
| A10R377 | 322-3193-00 |  |  | RES,FXD,FILM: 1 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G10000F |
| A10R378 | 313-1820-00 |  |  | RES,FXD,FILM: 82 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-82R00J |
| A10R379 | 322-3193-00 |  |  | RES,FXD,FILM: 1 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G10000F |
| A10R380 | 313-1820-00 |  |  | RES,FXD,FILM:82 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-82R00J |
| A10R381 | 313-1270-00 |  |  | RES,FXD,FILM:27 OHM $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-27R00J |
| A10R382 | 322-3193-00 |  |  | RES,FXD,FILM: 1 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G10000F |
| A10R383 | 313-1151-00 |  |  | RES,FXD,FILM:150 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-150ROJ |


| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R384 | 313-1512-00 |  |  | RES,FXD,FILM:5.1K OHM,5\%,0.2W | 91637 | CCF50-2-51000J |
| A10R385 | 313-1162-00 |  |  | RES,FXD,FILM:1.6K OHM,5\%,0.2W | 91637 | CCF50-2-16000J |
| A10R386 | 313-1162-00 |  |  | RES,FXD,FILM:1.6K OHM,5\%,0.2W | 91637 | CCF50-2-16000J |
| A10R387 | 313-1820-00 |  |  | RES,FXD,FILM:82 OHM,5\%,0.2W | 91637 | CCF50-2-82R00J |
| A10R388 | 313-1820-00 |  |  | RES,FXD,FILM:82 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-82R00J |
| A10R390 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10R391 | 313-1514-00 | B100100 | B100823 | RES,FXD,FILM:510K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF5051002J |
| A10R392 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10R393 | 313-1471-00 |  |  | RES,FXD,FILM:470 OHM,5\%,0.2W | 91637 | CCF50-2-470ROJ |
| A10R394 | 313-1471-00 |  |  | RES,FXD,FILM:470 OHM,5\%,0.2W | 91637 | CCF50-2-470R0J |
| A10R395 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A10R396 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A10R401 | 313-1101-00 |  |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100R0J |
| A10R402 | 313-1101-00 | B100100 | B110099 | RES,FXD,FILM: 100 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-100ROJ |
| A10R402 | 313-1101-00 | B700100 | B710099 | $\begin{aligned} & \text { RES, FXD,FILM: } 100 \text { OHM, } 5 \%, 0.2 \mathrm{~W} \\ & (2246 \text { TY) } \end{aligned}$ | 91637 | CCF50-2-100ROJ |
| A10R403 | 313-1511-00 | B110100 |  | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-510ROJ |
| A10R403 | 313-1511-00 | B710100 |  | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-510ROJ |
| A10R410 | 313-1511-00 | B100100 | B110099 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-510ROJ |
| A10R410 | 313-1331-00 | B110100 |  | RES,FXD,FILM:330 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-330ROJ |
| A10R410 | 313-1511-00 | 8700100 | B710099 | RES,FXD,FILM:510 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-510ROJ |
| A10R410 | 313-1331-00 | B710100 |  | $\begin{aligned} & \text { RES,FXD,FILM:330 OHM,5\%,0.2W } \\ & \text { (2246 1Y) } \end{aligned}$ | 91637 | CCF50-2-330ROJ |
| A10R411 | 313-1101-00 | B100100 | B110099 | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-100ROJ |
| A10R411 | 313-1151-00 | B110100 |  | RES,FXD,FILM: 150 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-150ROJ |
| A10R411 | 313-1101-00 | B700100 | B710099 | ```RES,FXD,FILM:100 OHM,5%,0.2W (2246 1Y)``` | 91637 | CCF50-2-100ROJ |
| A10R411 | 313-1151-00 | B710100 |  | $\begin{aligned} & \text { RES,FXD,FILM: } 150 \text { OHM, } 5 \%, 0.2 \mathrm{~W} \\ & \text { (2246 1Y) } \end{aligned}$ | 91637 | CCF50-2-150ROJ |
| A10R412 | 313-1101-00 | B100100 | B110099 | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-100R0J |
| A10R412 | 313-1151-00 | B110100 |  | RES,FXD,FILM:150 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-150ROJ |
| A10R412 | 313-1101-00 | B700100 | B710099 | $\begin{aligned} & \text { RES,FXD,FILM: } 100 \text { OHM, } 5 \%, 0.2 \mathrm{~W} \\ & \text { (2246 1Y) } \end{aligned}$ | 91637 | CCF50-2-100R0J |
| A10R412 | 313-1151-00 | B710100 |  | RES,FXD,FILM: 150 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-150ROJ |
| A10R413 | 313-1511-00 | B100100 | B110099 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 MOD A.2Y) | 91637 | CCF50-2-510ROJ |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix <br> Part No. | Serlal Effectlve | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R413 | 313-1331-00 | B110100 |  | RES, FXD,FILM: 330 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A. 2 Y ) | 91637 | CCF50-2-330ROJ |
| A10R413 | 313-1511-00 | B700100 | B710099 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-510ROJ |
| A10R413 | 313-1331-00 | B710100 |  | RES,FXD,FILM:330 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-330ROJ |
| AtOR414 | 313-1511-00 | B100100 | B110099 | RES,FXD,FILM:510 OHM,5\%,0.2W ( 2246 MOD A, $2 Y$ ) | 91637 | CCF50-2-510ROJ |
| A10R414 | 313-1331-00 | B110100 |  | RES,FXD,FILM:330 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-330ROJ |
| A10R414 | 313-1511-00 | B700100 | B710099 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-510ROJ |
| A10R414 | 313-1331-00 | $B 710100$ |  | RES,FXD,FILM:330 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-330ROJ |
| A10R415 | 313-1101-00 | B100100 | B110099 | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,1Y) | 91637 | CCF50-2-100ROJ |
| A10R415 | 313-1151-00 | B110100 |  | RES,FXD,FILM: 150 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-150ROJ |
| A10R415 | 313-1101-00 | B700100 | B710099 | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-100R0J |
| A10R415 | 313-1151-00 | B710100 |  | RES,FXD,FILM:150 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-150R0J |
| A10R416 | 313-1101-00 | B100100 | B110099 | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-100ROJ |
| A10R416 | 313-1151-00 | B110100 |  | RES,FXD,FILM: 150 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-150ROJ |
| A10R416 | 313-1101-00 | B700100 | B710099 | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-100R0J |
| A10R416 | 313-1151-00 | B710100 |  | RES,FXD,FILM:150 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-150ROJ |
| A10R417 | 313-1511-00 | B100100 | B110099 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 MOD A.2Y) | 91637 | CCF50-2-510ROJ |
| A10R417 | 313-1331-00 | B110100 |  | RES,FXD,FILM:330 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-330ROJ |
| A10R417 | 313-1511-00 | B700100 | B710099 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-510ROJ |
| A10R417 | 313-1331-00 | B710100 |  | RES,FXD,FILM:330 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-330ROJ |
| A10R420 | 313-1271-00 |  |  | RES,FXD,FILM:270 OHM,5\%,0.2W | 91637 | CCF50-2-270ROJ |
| A10R421 | 322-3279-00 |  |  | RES,FXD,FILM:7.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G78700F |
| A10R422 | 322-3279-00 |  |  | RES,FXD,FILM:7.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G78700F |
| A10R423 | 322-3279-00 |  |  | RES,FXD,FILM:7.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G78700F |
| A10R424 | 322-3279-00 |  |  | RES, FXD,FILM:7.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G78700F |
| A10R425 | 313-1750-00 |  |  | RES,FXD,FILM: 75 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-75R00J |
| A10R426 | 313-1101-00 |  |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R430 | 313-1271-00 |  |  | RES,FXD,FILM:270 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-270ROJ |
| A10R431 | 313-1750-00 |  |  | RES,FXD,FILM: 75 OHM,5\%,0.2W | 91637 | CCF50-2-75R00J |
| A10R432 | 322-3074-00 |  |  | RES,FXD,FILM:57.6 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G57R60F |
| A10R440 | 313-1104-00 |  |  | RES,FXD,FILM:100K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10002J |


| Component Number | Tektronix <br> Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R441 | 313-1621-00 |  |  | RES,FXD,FILM:620 OHM,5\%,0.2W | 91637 | CCF50-2-620ROJ |
| A10R442 | 313-1562-00 |  |  | RES,FXD,FILM:5.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-56000 J |
| A10R443 | 313-1562-00 |  |  | RES,FXD,FILM:5.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-56000J |
| A10R444 | 313-1561-00 |  |  | RES,FXD,FILM: 560 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-560ROJ |
| A10R445 | 322-3143-00 |  |  | RES,FXD,FILM:301 OHM, 1\%,0.2W,TC = TOMI | 91637 | CCF501G301ROF |
| A10R446 | 313-1331-00 |  |  | RES,FXD,FILM:330 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-330ROJ |
| A10R447 | 322-3285-00 | B110100 |  | RES,FXD,FILM:9.09K OHM, $1 \%, 0.2 \mathrm{~W}$, TC $=$ TOMI (2246 MOD A,2Y) | 91637 | CCF501G90900F |
| A10R447 | 322-3285-00 | B710100 |  | RES,FXD,FILM:9.09K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 1Y) | 91637 | CCF501G90900F |
| A10R448 | 322-3333-02 | B110100 |  | RES,FXD,FILM:28.7K OHM,0.2W,.5\% (2246 MOD A,2Y) | 91637 | CCF501D28701D |
| A10R448 | 322-3333-02 | B710100 |  | RES,FXD,FILM:28.7K OHM,0.2W,.5\% (2246 1Y) | 91637 | CCF501D28701D |
| A10R449 | 313-1392-00 |  |  | RES,FXD,FILM:3.9K OHM,5\%,0.2W | 91637 | CCF50-2-39000J |
| A10R450 | 313-1271-00 |  |  | RES,FXD,FILM:270 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-270ROJ |
| A10R451 | 322-3279-00 |  |  | RES,FXD,FILM: 7.87 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G78700F |
| A10R452 | 322-3279-00 |  |  | RES,FXD,FILM:7.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G78700F |
| A10R453 | 322-3279-00 |  |  | RES,FXD,FILM:7.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G78700F |
| A10R454 | 322-3279-00 |  |  | RES,FXD,FILM:7.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G78700F |
| A10R455 | 311-2230-00 |  |  | RES,VAR,NONWW:TRMR,500 OHM,20\%,0.50 LINEAR | TK2073 | GFO6UT2 501 ML |
| A10R456 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R460 | 313-1271-00 | B100100 | B113237 | RES,FXD,FILM:270 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-270ROJ |
| A10R460 | 313-1681-00 | B113238 |  | RES,FXD,FILM:680 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-680ROJ |
| A10R460 | 313-1271-00 | 8700100 | B711093 | RES,FXD,FILM:270 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-270ROJ |
| A10R460 | 313-1681-00 | B711094 |  | RES,FXD,FILM:680 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-680ROJ |
| A10R461 | 313-1101-00 | B100100 | B120199 | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-100ROJ |
| A10R461 | 322-3143-00 | B120200 |  | RES, FXD,FILM:301 OHM, $1 \%, 0.2 W$, TC $=$ TOMI (2246 MOD A.2Y) | 91637 | CCF501G301ROF |
| A10R461 | 313-1101-00 | B700100 | B714999 | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-100ROJ |
| A10R461 | 322-3143-00 | B715000 |  | $\begin{aligned} & \text { RES,FXD,FILM:301 OHM, } 1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=\text { TOMI } \\ & (2246 \text { 1Y) } \end{aligned}$ | 91637 | CCF501G301ROF |
| A10R462 | 322-3074-00 |  |  | RES,FXD,FILM:57.6 OHM, $1 \%, 0.2 \mathrm{~W}$, TC = TOMI | 91637 | CCF501G57R60F |
| A10R463 | 313-1120-00 |  |  | RES,FXD,FILM:12 OHM,5\%,0.2W | 91637 | CCF50-2-12R00J |
| A10R470 | 313-1104-00 |  |  | RES,FXD,FILM:100K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10002J |
| A10R471 | 313-1621-00 |  |  | RES,FXD,FILM:620 OHM,5\%,0.2W | 91637 | CCF50-2-620ROJ |
| A10R472 | 313-1562-00 |  |  | RES,FXD,FILM:5.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-56000J |
| A10R473 | 313-1562-00 |  |  | RES,FXD,FILM: 5.6 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-56000J |
| A10R474 | 313-1561-00 |  |  | RES,FXD,FILM:560 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-560ROJ |
| A10R475 | 322-3328-02 | B110100 |  | RES,FXD,FILM:25.5K OHM, $0.5 \%, 0.2 \mathrm{~W}, \mathrm{TC}=\mathrm{T} 2$ ( 2246 MOD A, 2 Y) | 91637 | CCF501D25501D |
| A10R475 | 322-3328-02 | B710100 |  | RES,FXD,FILM:25.5K OHM,0.5\%,0.2W,TC $=$ T2 (2246 1Y) | 91637 | CCF501D25501D |


| Component Number | Tektronlx Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R476 | 313-1392-00 |  |  | RES,FXD,FILM:3.9K OHM, 5\%,0.2W | 91637 | CCF50-2-39000J |
| A10R477 | 322-3285-00 | B100100 | B110099 | RES,FXD,FILM:9.09K OHM, $1 \%, 0.2 \mathrm{~W}$, TC $=$ TOMI (2246 MOD A,2Y) | 91637 | CCF501G90900F |
| A10R477 | 322-3284-00 | B110100 |  | RES,FXD,FILM:8.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 MOD A,2Y) | 91637 | CCF501G88700F |
| A10R477 | 322-3285-00 | B700100 | B710099 | RES,FXD,FILM:9.09K OHM,1\%,0.2W,TC = TOMI $(2246 i Y)$ | 91637 | CCF501G90900F |
| A10R477 | 322-3284-00 | B710100 |  | RES,FXD,FILM:8.87K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 1Y) | 91637 | CCF501G88700F |
| A10R478 | 322-3333-02 | B100100 | B110099 | RES,FXD,FILM:28.7K OHM,0.2W,.5\% (2246 MOD A,2Y) | 91637 | CCF501D28701D |
| A10R478 | 322-3232-00 | B110100 |  | RES,FXD,FILM:2.55K OHM, $1 \%, 0.2 \mathrm{~W}$, TC $=$ TOMI (2246 MOD A,2Y) | 91637 | CCF501G25500F |
| A10R478 | 322-3333-02 | B700100 | B710099 | RES,FXD,FILM:28.7K OHM,0.2W..5\% (2246 1Y) | 91637 | CCF501D28701D |
| A10R478 | 322-3232-00 | B710100 |  | RES,FXD,FILM:2.55K OHM, $1 \%, 0.2 \mathrm{~W}$, TC $=$ TOMI (2246 1Y) | 91637 | CCF501G25500F |
| A10R479 | 322-3143-00 |  |  | RES,FXD,FILM:301 OHM, 1\%,0.2W,TC = TOMI | 91637 | CCF501G301ROF |
| A10R480 | 313-1051-00 | B110100 |  | RES,FXD,FILM:5.1 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CT3-5R100J |
| A10R480 | 313-1051-00 | B710100 |  | RES,FXD,FILM:5.1 OHM,5\%,0.2W (2246 1Y) | 91637 | CT3-5R100J |
| A10R481 | 313-1051-00 |  |  | RES,FXD,FILM:5.1 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CT3-5R100J |
| A10R483 | 313-1151-00 |  |  | RES,FXD,FILM:150 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-150ROJ |
| A10R484 | 313-1202-00 |  |  | RES,FXD,FILM:2K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-20000J |
| A10R485 | 313-1392-00 |  |  | RES,FXD,FILM:3.9K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-39000」 |
| A10R486 | 313-1512-00 |  |  | RES,FXD,FILM:5.1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-51000J |
| A10R487 | 313-1331-00 |  |  | RES,FXD,FILM:330 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-330ROJ |
| A10R490 | 313-1511-00 |  |  | RES,FXD,FILM: 510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A10R491 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM,5\%,0.2W | 91637 | CCF50-2-510ROJ |
| A10R492 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM,5\%,0.2W | 91637 | CCF50-2-510ROJ |
| A10R493 | 313-1511-00 |  |  | RES,FXD,FILM: 510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A10R494 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM,5\%,0.2W | 91637 | CCF50-2-510ROJ |
| A10R495 | 313-1511-00 |  |  | RES,FXD,FILM: 510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A10R496 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A10R497 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM,5\%,0.2W | 91637 | CCF50-2-510ROJ |
| A10R498 | 313-1511-00 |  |  | RES,FXD,FILM:510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A10R501 | 307-0446-00 |  |  | RES NTWK,FXD,FI:10K OHM, 20\%,(9)RES | 11236 | 750-101-R10K |
| A10R502 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R503 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A10R504 | 313-1101-00 |  |  | RES,FXD,FILM:100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100R0J |
| A10R505 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R506 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R507 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A10R508 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronlx Part No. | Serlal Effective | No. Dscont | Name \& Description | Mfr. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A108510 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R512 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R601 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000 」 |
| A10R602 | 322-3193-00 |  |  | RES,FXD,FILM: 1 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G10000F |
| A10R603 | 322-3193-00 |  |  | RES,FXD,FILM: 1 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G10000F |
| A10R604 | 322-3231-00 |  |  | RES,FXD,FILM:2.49K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF50-1G24900F |
| A10R605 | 322-3193-00 |  |  | RES,FXD,FILM: 1 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G10000F |
| A10R606 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A10R607 | 313-1510-00 | B100100 | B110099 | RES,FXD,FILM:51 OHM,5\%,0.2W (2246 MOD A,2V) | 91637 | CCF501G51R00. |
| A10R607 | 313-1510-00 | B700100 | B710099 | RES,FXD,FILM:51 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF501G51R00.」 |
| A10R609 | 313-1101-00 |  |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R610 | 313-1391-00 |  |  | RES,FXD,FILM:390 OHM,5\%,0.2W | 91637 | CCF50-2-390ROJ |
| A10R611 | 313-1101-00 |  |  | RES,FXD,FILM:100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R612 | 313-1391-00 |  |  | RES,FXD,FILM:390 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-390ROJ |
| A10R613 | 313-1101-00 |  |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A10R614 | 313-1391-00 |  |  | RES,FXD,FILM:390 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-390ROJ |
| A10R615 | 313-1471-00 |  |  | RES,FXD,FILM:470 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-470ROJ |
| A10R616 | 313-1471-00 |  |  | RES,FXD,FILM: 470 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-470ROJ |
| A10R617 | 313-1471-00 |  |  | RES,FXD,FILM:470 OHM,5\%,0.2W | 91637 | CCF50-2-470ROJ |
| A10R618 | 313-1821-00 |  |  | RES,FXD,FILM:820 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-820ROJ |
| A10R619 | 313-1821-00 |  |  | RES,FXD,FILM:820 OHM,5\%,0.2W | 91637 | CCF50-2-820RO. |
| A10R620 | 313-1821-00 |  |  | RES,FXD,FILM: 820 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-820ROJ |
| A10R621 | 313-1302-00 |  |  | RES,FXD,FILM:3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-30000J |
| A10R622 | 313-1302-00 |  |  | RES,FXD,FILM:3K ОНM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-30000J |
| A10R623 | 313-1302-00 |  |  | RES,FXD,FILM:3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 . | CCF50-2-30000 J |
| A10R624 | 313-1102-00 |  |  | RES,FXD,FILM:1K ОНM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000.J |
| A10R625 | 313-1201-00 |  |  | RES,FXD,FILM:200 OHM,5\%,0.2W | 91637 | CCF50-2-200ROJ |
| A10R626 | 313-1390-00 |  |  | RES,FXD,FILM:39 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-39ROOJ |
| A10R627 | 313-1390-00 |  |  | RES,FXD,FILM:39 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-39R00J |
| A10R628 | 307-0503-00 |  |  | RES NTWK,FXD,FI:(9) 510 OHM $, 20 \%, 0.125 \mathrm{WTC}=50$ PPM/DEG C | 11236 | 750-101-R510 OR |
| A10R630 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R631 | 313-1102-00 |  |  | RES,FXD,FILM:1K ОНM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A10R636 | 313-1303-00 |  |  | RES,FXD,FILM:30K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-30001J |
| A10R637 | 313-1822-00 |  |  | RES,FXD,FILM:8.2K,OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-82000J |
| A10R638 | 313-1753-00 |  |  | RES,FXD,FILM:75K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-75001J |
| A10R639 | 313-1512-00 |  |  | RES,FXD,FILM:5.1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-51000J |
| A10R640 | 313-1512-00 |  |  | RES,FXD,FILM:5.1K OHM,5\%,0.2W | 91637 | CCF50-2-51000J |
| A10R641 | 313-1821-00 |  |  | RES,FXD,FILM:820 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-820ROJ |
| A10R642 | 313-1821-00 |  |  | RES,FXD,FILM:820 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-820ROJ |
| A10R643 | 313-1562-00 |  |  | RES,FXD,FILM:5.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-56000 |
| A10R644 | 313-1562-00 |  |  | RES,FXD,FILM:5.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-56000J |
| A10R645 | 313-1562-00 |  |  | RES,FXD,FILM:5.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-56000 J |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial No. Effective Dscont | Name \& Description | Mir. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10R646 | 313-1562-00 |  | RES,FXD, FILM:5.6K OHM,5\%,0.2W | 91637 | CCF50-2-56000J |
| A10R647 | 313-1821-00 |  | RES,FXD,FILM: 820 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-820ROJ |
| A10R648 | 313-1471-00 |  | RES,FXD,FILM:470 OHM,5\%,0.2W | 91637 | CCF50-2-470ROJ |
| A10R649 | 313-1302-00 |  | RES,FXD,FILM:3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-30000J |
| A10R650 | 313-1751-00 |  | RES,FXD,FILM:750 OHM,5\%,0.2W | 91637 | CCF50-2-750ROJ |
| A10R651 | 313-1331-00 |  | RES,FXD.FILM:330 OHM, 5\%,0.2W | 91637 | CCF50-2-330ROJ |
| A10R652 | 313-1331-00 |  | RES,FXD,FILM: 330 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-330ROJ |
| A10R653 | 313-1471-00 |  | RES,FXD,FILM:470 OHM,5\%,0.2W | 91637 | CCF50-2-470ROJ |
| A10R654 | 313-1302-00 |  | RES,FXD,FILM:3K OHM,5\%,0.2W | 91637 | CCF50-2-30000J |
| A10R655 | 313-1821-00 |  | RES,FXD,FILM:820 OHM, 5\%,0.2W | 91637 | CCF50-2-820R0J |
| A10R656 | 313-1201-00 |  | RES,FXD,FILM:200 OHM,5\%,0.2W | 91637 | CCF50-2-200R0J |
| A10R657 | 313-1201-00 |  | RES,FXD,FILM:200 OHM,5\%,0.2W | 91637 | CCF50-2-200ROJ |
| A10R658 | 313-1821-00 |  | RES,FXD,FILM:820 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-820ROJ |
| A10R659 | 313-1821-00 |  | RES,FXD,FILM:820 OHM,5\%,0.2W | 91637 | CCF50-2-820R0J |
| A10R662 | 313-1393-00 |  | RES,FXD,FILM:39K OHM,5\%,0.2W | 91637 | CCF50-2-39001J |
| A10R663 | 313-1393-00 |  | RES,FXD,FILM:39K OHM,5\%,0.2W | 91637 | CCF50-2-39001J |
| A10R664 | 313-1393-00 |  | RES,FXD,FILM:39K OHM,5\%,0.2W | 91637 | CCF50-2-39001J |
| A10R665 | 313-1393-00 |  | RES,FXD,FILM:39K OHM,5\%,0.2W | 91637 | CCF50-2-39001J |
| A10R666 | 313-1393-00 |  | RES,FXD,FILM:39K OHM,5\%,0.2W | 91637 | CCF50-2-39001J |
| A10R669 | 313-1511-00 |  | RES,FXD,FILM:510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A10R670 | 313-1511-00 |  | RES,FXD,FILM:510 OHM,5\%,0.2W | 91637 | CCF50-2-510ROJ |
| A10R671 | 313-1200-00 |  | RES,FXD,FILM:20 OHM,5\%,0.2W | 91637 | CCF50-2-20R00J |
| A10R672 | 313-1333-00 |  | RES,FXD,FILM:33K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-33001J |
| A10R673 | 313-1331-00 | B110100 | RES,FXD,FILM:330 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-330ROJ |
| A10R673 | 313-1331-00 | B710100 | RES,FXD,FILM:330 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-330ROJ |
| A10R674 | 313-1201-00 | B110100 | RES,FXD,FILM:200 OHM,5\%,0.2W ( 2246 MOD A,2Y) | 91637 | CCF50-2-200ROJ |
| A10R674 | 313-1201-00 | B710100 | RES,FXD,FILM:200 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-200ROJ |
| A10R686 | 313-1511-00 | B110100 | RES,FXD,FILM:510 OHM.5\%,0.2W ( 2246 MOD A,2Y) | 91637 | CCF50-2-510ROJ |
| A10R686 | 313-1511-00 | B710100 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-510ROJ |
| A10R687 | 313-1511-00 | B110100 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-510ROJ |
| A10R687 | 313-1511-00 | B710100 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-510ROJ |
| A10R688 | 313-1511-00 | B110100 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-510ROJ |
| A10R688 | 313-1511-00 | B710100 | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-510ROJ |
| A10R689 | 313-1750-00 | B110100 | RES,FXD,FILM: 75 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-75R00.J |
| A10R689 | 313-1750-00 | B710100 | RES,FXD,FILM: 75 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-75R00J |


| Component Number | Tektronlx Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mir. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R690 | 313-1750-00 | B110100 |  | $\begin{aligned} & \text { RES,FXD,FILM: } 75 \text { OHM, } 5 \%, 0.2 \mathrm{~W} \\ & \text { (2246 MOD A,2Y) } \end{aligned}$ | 91637 | CCF50-2-75ROOJ |
| A10R690 | 313-1750-00 | B710100 |  | RES,FXD,FILM: 75 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-75R00J |
| A10R691 | 313-1750-00 | B110100 |  | RES,FXD,FILM:75 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-75R00J |
| A10R691 | 313-1750-00 | B710100 |  | RES,FXD,FILM:75 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-75R00J |
| A10R692 | 313-1750-00 | B110100 |  | RES,FXD,FILM: 75 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-75R00J |
| A10R692 | 313-1750-00 | B710100 |  | RES,FXD,FILM:75 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-75R00J |
| A10R693 | 313-1750-00 | B110100 |  | RES,FXD,FILM:75 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-75R00J |
| A10R693 | 313-1750-00 | B710100 |  | RES,FXD,FILM: 75 OHM,5\%,0.2W ( 2246 1Y) | 91637 | CCF50-2-75R00J |
| A10R694 | 313-1750-00 | B110100 |  | RES,FXD,FILM: 75 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-75ROOJ |
| A10R694 | 313-1750-00 | B710100 |  | RES,FXD,FILM: 75 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-75R00J |
| A10R701 | 322-3226-00 |  |  | RES, FXD,FILM:2.21K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G22100F |
| A10R702 | 322-3226-00 |  |  | RES,FXD,FILM: 2.21 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G22100F |
| A10R703 | 311-2230-00 |  |  | RES,VAR,NONWW:TRMR,500 OHM,20\%,0.50 LINEAR | TK2073 | GF06UT2 501 ML |
| A10R706 | 322-3085-00 |  |  | RES,FXD,FILM:75 OHM,1\%,0.2W,TC = T0 | 91637 | CCF501G75R00F |
| A10R707 | 322-3085-00 |  |  | RES,FXD, FILM:75 OHM, $1 \%, 0.2 W$,TC $=$ T0 | 91637 | CCF501G75R00F |
| A10R708 | 307-2130-00 |  |  | RES NTWK,FXD,FI:DUAL LOAD RESISTOR | 80009 | 307213000 |
| A10R709 | 313-1027-00 |  |  | RES,FXD,FILM:2.7 OHM,5\%,0.2W | 57668 | TR20JE 02E7 |
| A10R710 | 313-1134-00 |  |  | RES,FXD,FILM:130K OHM 5\%,0.2W | 91637 | CCF50-2-13002J |
| A10R711 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10R00J |
| A10R712 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10R715 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A10R716 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R717 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A10R718 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A10R719 | 313-1200-00 |  |  | RES,FXD,FILM:20 OHM,5\%,0.2W | 91637 | CCF50-2-20R00J |
| A10R720 | 313-1200-00 |  |  | RES,FXD,FILM:20 OHM,5\%,0.2W | 91637 | CCF50-2-20R00J |
| A10R721 | 313-1134-00 |  |  | RES,FXD,FILM:130K OHM 5\%,0.2W | 91637 | CCF50-2-13002J |
| A10R722 | 313-1134-00 |  |  | RES,FXD,FILM:130K OHM 5\%,0.2W | 91637 | CCF50-2-13002J |
| A10R723 | 313-1027-00 |  |  | RES,FXD,FILM:2.7 OHM,5\%,0.2W | 57668 | TR20JE 02E7 |
| A10R724 | 311-2234-00 |  |  | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 502 ML |
| A10R725 | 313-1102-00 | 8100100 | B119999 | RES,FXD,FILM:1K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10000 |
| A10R725 | 313-1751-00 | B120000 |  | RES,FXD,FILM:750 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-750ROJ |
| A10R725 | 313-1102-00 | 8700100 | B714999 | RES,FXD,FILM:1K OHM,5\%,0.2W ( 2246 1Y) | 91637 | CCF50-2-10000J |
| A10R725 | 313-1751-00 | B715000 |  | RES,FXD,FILM:750 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-750ROJ |
| A10R726 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, 5\%,0.2W | 91637 | CCF50-2-10001J |


| Component Number | Tektronlx Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R727 | 313-1104-00 |  |  | RES,FXD,FILM:100K OHM,5\%,0.2W | 91637 | CCF50-2-10002J |
| A10R728 | 313-1824-00 |  |  | RES,FXD,FILM:820K OHM,0.2W,5\% | 91637 | CCF50-2-82002J |
| A10R729 | 313-1202-00 |  |  | RES,FXD,FILM:2K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-20000J |
| A10R730 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A10R731 | 313-1750-00 |  |  | RES,FXD,FILM:75 OHM,5\%,0.2W | 91637 | CCF50-2-75R00J |
| A10R732 | 313-1750-00 |  |  | RES,FXD,FILM: 75 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-75R00J |
| A10R733 | 313-1027-00 |  |  | RES,FXD,FILM:2.7 OHM,5\%,0.2W | 57668 | TR20JE 02E7 |
| A10R734 | 313-1120-00 |  |  | RES,FXD,FILM:12 OHM,5\%,0.2W | 91637 | CCF50-2-12R00J |
| A10R801 | 313-1431-00 | B100100 | B110099 | RES,FXD,FILM:430 OHM,5\%,0.2W (2246 MOD A.2Y) | 91637 | CCF50-2-430ROJ |
| A10R801 | 313-1681-00 | B110100 |  | RES,FXD,FILM: 680 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-680ROJ |
| A10R801 | 313-1431-00 | B700100 | B710099 | $\begin{aligned} & \text { RES,FXD,FILM:430 OHM, } 5 \%, 0.2 \mathrm{~W} \\ & (2246 \mathrm{Y}) \end{aligned}$ | 91637 | CCF50-2-430ROJ |
| A10R801 | 313-1681-00 | B710100 |  | RES,FXD,FILM:680 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-680ROJ |
| A10R802 | 313-1750-00 |  |  | RES,FXD,FILM:75 OHM,5\%,0.2W | 91637 | CCF50-2-75R00J |
| A10R803 | 313-1562-00 | B100100 | B110099 | RES,FXD,FILM:5.6K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-56000J |
| A10R803 | 313-1272-00 | B110100 |  | RES,FXD,FILM:2.7K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-27000J |
| A10R803 | 313-1562-00 | 8700100 | B710099 | RES,FXD,FILM:5.6K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-56000J |
| A10R803 | 313-1272-00 | B710100 |  | RES,FXD,FILM:2.7K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-27000 J |
| A10R804 | 313-1104-00 | B100100 | B110099 | RES,FXD,FILM:100K OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-10002J |
| A10R804 | 313-1273-00 | 8110100 |  | RES,FXD,FILM:27K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-27001J |
| A10R804 | 313-1104-00 | B700100 | B710099 | RES,FXD,FILM:100K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10002J |
| A10R804 | 313-1273-00 | B710100 |  | RES,FXD,FILM:27K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-27001J |
| A10R805 | 313-1162-00 | 8100100 | B110099 | RES,FXD,FILM:1.6K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-16000J |
| A10R805 | 313-1273-00 | B110100 |  | RES,FXD,FILM:27K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-27001J |
| A10R805 | 313-1162-00 | 8700100 | B710099 | RES,FXD,FILM:1.6K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-16000J |
| A10R805 | 313-1273-00 | B710100 |  | ```RES,FXD,FILM:27K OHM,5%,0.2W (2246 1Y)``` | 91637 | CCF50-2-27001J |
| A10R806 | 323-0310-00 |  |  | RES,FXD,FILM: 16.5 K OHM, $1 \%, 0.5 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CMF65116G16501F |
| A10R807 | 313-1750-00 |  |  | RES,FXD,FILM:75 OHM,5\%,0.2W | 91637 | CCF50-2-75R00J |
| A10R808 | 313-1272-00 |  |  | RES,FXD,FILM:2.7K OHM,5\%,0.2W | 91637 | CCF50-2-27000J |
| A10R809 | 311-2234-00 |  |  | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 502 ML |
| A10R810 | 313-1272-00 |  |  | RES,FXD,FILM:2.7K OHM,5\%,0.2W | 91637 | CCF50-2-27000J |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effectlve | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R811 | 322-3266-00 | B100100 | B110099 | RES,FXD,FILM:5.76K OHM,1\%,0.2W,TC = TOMI (2246 MOD A.2Y) | 91637 | CCF501G57600F |
| A10R811 | 322-3265-00 | B110100 |  | RES,FXD,FILM:5.62K OHM, $1 \%, 0.2 \mathrm{~W}$, TC $=$ TOMI (2246 MOD A,2Y) | 91637 | CCF501G56200F |
| A10R811 | 322-3266-00 | B700100 | B710099 | RES,FXD,FILM:5.76K OHM, $1 \%, 0.2 \mathrm{~W}$, TC $=$ TOMI (2246 1Y) | 91637 | CCF501G57600F |
| A10R811 | 322-3265-00 | B710100 |  | RES,FXD,FILM:5.62K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 1Y) | 91637 | CCF501G56200F |
| A10R812 | 322-3266-00 | B100100 | B110099 | RES,FXD,FILM:5.76K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 MOD A,2Y) | 91637 | CCF501G57600F |
| A10R812 | 322-3265-00 | B110100 |  | RES,FXD,FILM:5.62K OHM, $1 \%, 0.2 \mathrm{~W}$, TC $=$ TOMI (2246 MOD A.2Y) | 91637 | CCF501G56200F |
| A10R812 | 322-3266-00 | B700100 | B710099 | $\begin{aligned} & \text { RES,FXD,FILM:5.76K OHM,1\%,0.2W,TC = TOMI } \\ & \text { (2246 iY) } \end{aligned}$ | 91637 | CCF501G57600F |
| A10R812 | 322-3265-00 | B710100 |  | RES,FXD,FILM:5.62K OHM, $1 \%, 0.2 W, T C=$ TOMI (2246 1Y) | 91637 | CCF501G56200F |
| A10R813 | 313-1431-00 | B100100 | B110099 | RES,FXD,FILM:430 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-430ROJ |
| A10R813 | 313-1272-00 | B110100 |  | RES,FXD,FILM:2.7K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-27000」 |
| A10R813 | 313-1431-00 | B700100 | 8710099 | RES,FXD,FILM:430 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-430ROJ |
| A10R813 | 313-1272-00 | B710100 |  | RES,FXD,FILM:2.7K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-27000」 |
| A10R814 | 313-1562-00 | B100100 | B110099 | RES,FXD,FILM:5.6K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-56000J |
| A10R814 | 313-1272-00 | B110100 |  | RES,FXD,FILM:2.7K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-27000J |
| A10R814 | 313-1562-00 | B700100 | B710099 | RES,FXD,FILM:5.6K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-56000J |
| A10R814 | 313-1272-00 | B710100 |  | RES,FXD,FILM:2.7K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-27000J |
| A10R815 | 313-1104-00 | B100100 | B110099 | RES,FXD,FILM:100K OHM,5\%,0.2W (2246 MOD A.2Y) | 91637 | CCF50-2-10002J |
| A10R815 | 313-1104-00 | B700100 | B710099 | RES,FXD,FILM:100K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10002J |
| A10R816 | 323-0310-00 | B100100 | B110099 | RES,FXD,FILM:16.5K OHM, $1 \%, 0.5 \mathrm{~W}, \mathrm{TC}=$ то (2246 MOD A,2Y) | 91637 | CMF65116G16501F |
| A10R816 | 323-0310-00 | B700100 | B710099 | $\begin{aligned} & \text { RES,FXD,FILM:16.5K OHM, } 1 \%, 0.5 \mathrm{~W}, \mathrm{TC}=\text { T0 } \\ & (2246 ~ \uparrow Y) \end{aligned}$ | 91637 | CMF65116G16501F |
| A10R819 | 313-1750-00 | B100100 | B110099 | RES,FXD,FILM:75 OHM,5\%,0.2W ( 2246 MOD A, 2 Y) | 91637 | CCF50-2-75R00J |
| A10R819 | 313-1750-00 | B700100 | B710099 | RES,FXD,FILM:75 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-75ROOJ |
| A10R820 | 322-3402-00 | B100100 | B119999 | RES,FXD,FILM: 150 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 MOD A. $2 Y$ ) | 91637 | CCF50G15002F |
| A10R820 | 322-0402-00 | B120000 |  | RES,FXD,FILM:150K OHM, $1 \%, 0.25 \mathrm{~W}$, TC $=$ TO (2246 MOD A,2Y) | 91637 | CMF6042G15002F |
| A10R820 | 322-3402-00 | B700100 | $B 714999$ | $\begin{aligned} & \text { RES,FXD,FILM:150K OHM, } 1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=\text { TOMI } \\ & (22461 Y) \end{aligned}$ | 91637 | CCF50G15002F |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronlx Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R820 | 322-0402-00 | B715000 |  | $\begin{aligned} & \text { RES,FXD,FILM: } 150 \mathrm{~K} \text { OHM, } 1 \%, 0.25 \mathrm{~W}, \mathrm{TC}=\mathrm{TO} \\ & (2246 \mathrm{Y}) \end{aligned}$ | 91637 | CMF6042G15002F |
| A10R821 | 322-3402-00 | B100100 | B119999 | RES,FXD,FILM:150K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 MOD A,2Y) | 91637 | CCF50G15002F |
| A10R821 | 322-0402-00 | B120000 |  | RES,FXD,FILM:150K OHM,1\%,0.25W,TC $=$ T0 (2246 MOD A,2Y) | 91637 | CMF6042G15002F |
| A10R821 | 322-3402-00 | B700100 | 8714999 | RES,FXD,FILM: 150 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 1Y) | 91637 | CCF50G15002F |
| A10R821 | 322-0402-00 | B715000 |  | $\begin{aligned} & \text { RES,FXD,FILM:150K OHM, } 1 \%, 0.25 \mathrm{~W}, \mathrm{TC}=\text { TO } \\ & (2246 \mathrm{TY}) \end{aligned}$ | 91637 | CMF6042G15002F |
| A10R822 | 322-3265-00 |  |  | RES,FXD.FILM:5.62K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G56200F |
| A10R823 | 311-2230-00 |  |  | RES,VAR,NONWW:TRMR,500 OHM,20\%,0.50 LINEAR | TK2073 | GF06UT2 501 ML |
| A10R825 | 311-2234-00 |  |  | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 502 M L |
| A10R826 | 311-2234-00 |  |  | RES,VAR,NONWW:TRMR,5K OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 502 M L |
| A10R827 | 311-2229-00 |  |  | RES,VAR,NONWW:TRMR,250 OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 251 ML |
| A10R828 | 301-0203-00 | 8100100 | B110099 | RES,FXD,FILM:20K OHM,5\%,0.5W (2246 MOD A,2Y) | TK1727 | SFR30 2322-182- |
| A10R828 | 313-1133-00 | 8110100 |  | RES,FXD,FILM:13K,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-13001J, |
| A10R828 | 301-0203-00 | B700100 | B710099 | RES,FXD,FILM:20K OHM,5\%,0.5W (2246 1Y) | TK1727 | SFR30 2322-182- |
| A10R828 | 313-1133-00 | 8710100 |  | RES,FXD,FILM: $13 \mathrm{~K}, 5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-13001J, |
| A10R829 | 301-0203-00 | B100100 | B110099 | RES,FXD,FILM:20K OHM,5\%,0.5W (2246 MOD A,2Y) | TK1727 | SFR30 2322-182- |
| A10R829 | 313-1133-00 | B110100 |  | RES,FXD,FILM:13K,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-13001J, |
| A10R829 | 301-0203-00 | B700100 | B710099 | RES,FXD,FILM:20K OHM,5\%,0.5W (2246 1Y) | TK1727 | SFR30 2322-182- |
| A10R829 | 313-1133-00 | B710100 |  | RES, FXD,FILM: $13 \mathrm{~K}, 5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-13001J. |
| A10R830 | $313-1133-00$ | B110100 |  | RES,FXD,FILM: $13 \mathrm{~K}, 5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-13001J, |
| A10R830 | 313-1133-00 | B710100 |  | $\begin{aligned} & \text { RES,FXD,FILM: } 13 \mathrm{~K}, 5 \%, 0.2 \mathrm{~W} \\ & (2246 \mathrm{TY}) \end{aligned}$ | 91637 | CCF50-2-13001J, |
| A10R831 | 313-1133-00 | B110100 |  | RES,FXD,FILM:13K,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-13001J, |
| A10R831 | 313-1133-00 | B710100 |  | $\begin{aligned} & \text { RES,FXD,FILM: } 13 \mathrm{~K}, 5 \%, 0.2 \mathrm{~W} \\ & (22461 \mathrm{Y}) \end{aligned}$ | 91637 | CCF50-2-13001J, |
| A10R836 | 322-3152-00 |  |  | RES,FXD,FILM:374 OHM, 1\%,0.2W,TC = TOMI | 91637 | CCF501G374ROF |
| A10R837 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10R840 | 313-1470-00 | B100100 | B110099 | RES,FXD,FILM:47 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-47R00J |


| Component Number | Tektronlx Part No. | Serial Effectlve | No. <br> Dscont | Name a Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R840 | 313-1470-00 | B700100 | B710099 | $\begin{aligned} & \text { RES,FXD,FILM: } 47 \text { OHM,5\%,0.2W } \\ & \text { (2246 1Y) } \end{aligned}$ | 91637 | CCF50-2-47R00J |
| A10R841 | 313-1100-00 | B100100 | B110099 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10R00.J |
| A10R841 | 313-1100-00 | B700100 | B710099 | RES,FXD,FILM: 10 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-10R00J |
| A10R842 | 313-1103-00 | B100100 | B110099 | RES,FXD,FILM:10K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10001J |
| A10R842 | 313-1103-00 | B700100 | B710099 | RES,FXD,FILM:10K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10001J |
| A10R843 | 313-1751-00 | B100100 | B110099 | RES,FXD,FILM:750 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-750ROJ |
| A10R843 | 313-1751-00 | B700100 | B710099 | RES,FXD,FILM:750 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-750ROJ |
| A10R844 | 313-1751-00 | B100100 | B110099 | RES,FXD,FILM: 750 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-750ROJ |
| A10R844 | 313-1751-00 | B700100 | B710099 | RES,FXD,FILM: 750 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-750R0J |
| A10R845 | 313-1100-00 | B100100 | B110099 | RES,FXD.FILM:10 OHM.5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10R00J |
| A10R845 | 313-1100-00 | B700100 | B710099 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10R00J |
| A10R846 | 322-3058-00 | B100100 | B110099 | RES,FXD,FILM:39.2 OHM,1\%,0.2W.TC = TOMI (2246 MOD A,2Y) | 91637 | CCF50-1G39R20F |
| A10R846 | 322-3058-00 | B700100 | B710099 | RES,FXD,FILM:39.2 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 1Y) | 91637 | CCF50-1G39R20F |
| A10R847 | 313-1100-00 | B100100 | B110099 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10R00J |
| A10R847 | 313-1100-00 | B700100 | B710099 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10R00J |
| A10R848 | 301-0222-00 | B100100 | B110099 | RES,FXD,FILM:2.2K OHM,5\%,0.5W (2246 MOD A.2Y) | TK1727 | SFR30 2322-182- |
| A10R848 | 301-0222-00 | B700100 | B710099 | RES,FXD,FILM:2.2K OHM,5\%,0.5W (2246 1Y) | TK1727 | SFR30 2322-182- |
| A10R849 | 301-0222-00 | B100100 | B110099 | RES,FXD,FILM:2.2K OHM,5\%,0.5W (2246 MOD A,2Y) | TK1727 | SFR30 2322-182- |
| A10R849 | 301-0222-00 | B700100 | B710099 | RES,FXD,FILM:2.2K OHM,5\%,0.5W (2246 1Y) | TK1727 | SFR30 2322-182- |
| A10R850 | 313-1392-00 | B100100 | B110099 | RES,FXD,FILM:3.9K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-39000J |
| A10R850 | 313-1392-00 | 8700100 | B710099 | RES,FXD,FILM:3.9K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-39000J |
| A10R851 | 313-1100-00 | B100100 | B110099 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10R00J |
| A10R851 | 313-1100-00 | B700100 | B710099 | $\begin{aligned} & \text { RES,FXD,FILM:10 OHM,5\%,0.2W } \\ & \text { (2246 1Y) } \end{aligned}$ | 91637 | CCF50-2-10R00J |
| A10R852 | 322-3074-00 | B100100 | B110099 | RES,FXD,FILM:57.6 OHM,1\%,0.2W,TC = TOMI (2246 MOD A,2Y) | 91637 | CCF501G57R60F |
| A10R852 | 322-3074-00 | B700100 | B710099 | RES,FXD,FILM:57.6 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 1Y) | 91637 | CCF501G57R60F |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R853 | 313-1470-00 | B100100 | B110099 | RES,FXD,FILM:47 OHM,5\%,0.2W (2246 MOD A, 2 Y ) | 91637 | CCF50-2-47R00J |
| A10R853 | 313-1470-00 | B700100 | B710099 | RES,FXD,FILM:47 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-47ROOJ |
| A10R854 | 313-1201-00 |  |  | RES,FXD,FILM:200 OHM,5\%,0.2W | 91637 | CCF50-2-200R0J |
| A10R855 | 313-1201-00 |  |  | RES,FXD,FILM:200 OHM,5\%,0.2W | 91637 | CCF50-2-200ROJ |
| A10R856 | 322-3289-00 |  |  | RES,FXD,FILM:10K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF50G10001F |
| A10R857 | 322-3265-00 |  |  | RES,FXD,FILM:5.62K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G56200F |
| A10R858 | 322-3143-00 | B100100 | B110099 | RES,FXD,FILM: 301 OHM, $1 \%, 0.2 W$, TC $=$ TOMI ( 2246 MOD A,2Y) | 91637 | CCF501G301ROF |
| A10R858 | 322-3143-00 | B700100 | B710099 | $\begin{aligned} & \text { RES,FXD,FILM:301 OHM, } 1 \%, 0.2 W, T C=\text { TOMI } \\ & (22461 Y) \end{aligned}$ | 91637 | CCF501G301ROF |
| A10R860 | 313-1103-00 | B110100 |  | RES,FXD,FILM:10K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10001J |
| A10R860 | 313-1103-00 | B710100 |  | RES,FXD,FILM:10K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10001J |
| A10R861 | 313-1272-00 | B110100 |  | RES,FXD,FILM:2.7K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-27000J |
| A10R861 | 313-1272-00 | B710100 |  | RES,FXD.FILM: 2.7 K OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-27000J |
| A10R862 | 322-3077-00 | B110100 |  | RES,FXD,FILM: 61.9 OHM, $1 \%, 0.2 W, T C=$ TOMI (2246 MOD A,2Y) | 91637 | CCF501G61R9OF |
| A10R862 | 322-3077-00 | B710100 |  | RES,FXD,FILM: 61.9 OHM, $1 \%, 0.2 W$, TC $=$ TOMI (2246 1Y) | 91637 | CCF501G61R9OF |
| A10R863 | 313-1271-00 | B110100 |  | RES,FXD,FILM:270 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-270R0J |
| A10R863 | 313-1271-00 | B710100 |  | RES,FXD,FILM:270 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-270ROJ |
| A10R871 | 313-1103-00 | B110100 |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-10001J |
| A10R871 | 313-1103-00 | B710100 |  | RES,FXD,FILM:10K OHM,5\%,0.2W (2246 TV) | 91637 | CCF50-2-10001J |
| A10R872 | 313-1510-00 | B110100 |  | RES,FXD,FILM: 51 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF501G51R00J |
| A10R872 | 313-1510-00 | B710100 |  | RES,FXD,FILM:51 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF501G51R00J |
| A10R873 | 313-1271-00 | B110100 |  | RES,FXD,FILM:270 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-270ROJ |
| A10R873 | 313-1271-00 | B710100 |  | RES,FXD,FILM:270 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-270ROJ |
| A10R891 | 313-1681-00 | B110100 |  | RES,FXD,FILM:680 OHM,5\%,0.2W (2246 MOD A.2Y) | 91637 | CCF50-2-680ROJ |
| A10R891 | 313-1681-00 | B710100 |  | RES,FXD,FILM: 680 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-680ROJ |
| A10R892 | 313-1750-00 | B110100 |  | RES,FXD,FILM:75 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-75R00J |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No． | Serial No． Effective Dscont | Name \＆Description | Mfr． Code | Mfr．Part No． |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10R892 | 313－1750－00 | B710100 | $\begin{aligned} & \text { RES,FXD,FILM:75 OHM,5\%,0.2W } \\ & \text { (2246 1Y) } \end{aligned}$ | 91637 | CCF50－2－75R00J |
| A10R893 | 313－1391－00 | B110100 | RES，FXD，FILM：390 OHM，5\％，0．2W （2246 MOD A，2Y） | 91637 | CCF50－2－390R0」 |
| A10R893 | 313－1391－00 | B710100 | RES，FXD，FILM：390 OHM，5\％，0．2W （2246 1Y） | 91637 | CCF50－2－390R0」 |
| A10R894 | 313－1100－00 | B110100 | RES，FXD，FILM：10 OHM，5\％，0．2W （2246 MOD A，2Y） | 91637 | CCF50－2－10R00． |
| A10R894 | 313－1100－00 | B710100 | RES，FXD，FILM： 10 OHM， $5 \%, 0.2 W$ （2246 iY） | 91637 | CCF50－2－10R00J |
| A10R896 | 323－0310－00 | 8110100 | RES，FXD，FILM：16．5K OHM， $1 \%, 0.5 W, T C=T 0$ （2246 MOD A，2Y） | 91637 | CMF65116G16501F |
| A10R896 | 323－0310－00 | 8710100 | ```RES,FXD,FILM:16.5K OHM,1%,0.5W,TC = TO (2246 1Y)``` | 91637 | CMF65116G16501F |
| A10R897 | 313－1100－00 | 8110100 | RES，FXD，FILM： 10 OHM， $5 \%, 0.2 W$ （2246 MOD A，2Y） | 91637 | CCF50－2－10R00S |
| A10R897 | 313－1100－00 | 8710100 | RES，FXD，FILM：10 OHM，5\％，0．2W （2246 1Y） | 91637 | CCF50－2－10R00J |
| A10R898 | 313－1100－00 | B110100 | RES，FXD，FILM： 10 OHM，5\％，0．2W （2246 MOD A，2Y） | 91637 | CCF50－2－10R00J |
| A10R898 | 313－1100－00 | 8710100 | RES，FXD，FILM：10 OHM，5\％，0．2W （2246 1Y） | 91637 | CCF50－2－10R00J |
| A10R906 | 313－1120－00 |  | RES，FXD，FILM：12 OHM，5\％，0．2W | 91637 | CCF50－2－12ROOJ |
| A10R907 | 313－1120－00 |  | RES，FXD，FILM：12 OHM，5\％，0．2W | 91637 | CCF50－2－12R00J |
| A10R908 | 313－1120－00 |  | RES，FXD，FILM：12 OHM，5\％，0．2W | 91637 | CCF50－2－12ROOJ |
| A10R909 | 313－1101－00 |  | RES，FXD，FILM：100 OHM，5\％，0．2W | 91637 | CCF50－2－100ROJ |
| A10R910 | 313－1101－00 |  | RES，FXD，FILM：100 OHM，5\％，0．2W | 91637 | CCF50－2－100ROJ |
| A10R911 | 311－1239－00 |  | RES，VAR，NONWW：TRMR，2．5K OHM，0．5W | 32997 | 3386X－1－252 |
| A10R915 | 322－3289－00 |  | RES，FXD，FILM： 10 K OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF50G10001F |
| A10R916 | 322－3289－00 |  | RES，FXD，FILM：10K OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF50G10001F |
| A10R920 | 311－2228－00 |  | RES，VAR，NONWW：TRMR，200 OHM，20\％，0．5W LINEAR | TK2073 | GF06UT2 201 ML |
| A10R921 | 307－2131－00 |  | RES NTWK，FXD，FI：PRECESION VOLTAGE DIVIDER H2131 | 80009 | 307213100 |
| A10R922 | 313－1103－00 |  | RES，FXD，FILM：10K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－10001J |
| A10R923 | 313－1103－00 |  | RES，FXD，FILM：10K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－10001J |
| A10R924 | 313－1103－00 |  | RES，FXD，FILM：10K OHM，5\％，0．2W | 91637 | CCF50－2－10001J |
| A10R925 | 313－1472－00 | B120000 | RES，FXD，FILM：4．7K OHM，5\％，0．2W （2246 MOD A，2Y） | 91637 | CCF50－2－47000」 |
| A10R925 | 313－1472－00 | B715000 | RES，FXD，FILM：4．7K OHM，5\％，0．2W （2246 1Y） | 91637 | CCF50－2－47000J |
| A10R930 | 313－1751－00 |  | RES，FXD，FILM：750 OHM，5\％，0．2W | 91637 | CCF50－2－750ROJ |
| A10R931 | 322－3193－02 |  | RES，FXD，FILM： 1 K OHM， $0.5 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T2 | 91637 | CCF501D10000D |
| A10R932 | 322－3237－00 |  | RES，FXD，FILM： 2.87 K OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G28700F |
| A10R933 | 313－1272－00 |  | RES，FXD，FILM：2．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－27000J |
| A10R934 | 313－1122－00 |  | RES，FXD，FILM：1．2K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－12000J |
| A10R935 | 313－1243－00 |  | RES，FXD，FILM：24K OHM， $5 \%, 0.2 \mathrm{~W}$ | 57668 | CCF50－1－G2402 J |
| A10R936 | 322－3489－02 |  | RES，FXD，FILM：3．52K OHM，0．2W，5\％ | 91637 | CCF501D35200D |
| A10R937 | 322－3126－02 |  | RES，FXD，FILM：200 OHM，0．5\％，0．2W，TC＝T2 | 91637 | CCF501D200R0D |
| A10R938 | 313－1752－00 |  | RES，FXD，FILM：7．5K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－75000J |

Replaceable Electrical Parts
22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R939 | 313-1152-00 |  |  | RES,FXD,FILM:1.5K OHM,5\%,0.2W | 91637 | CCF50-2-15000J |
| A10R940 | 313-1122-00 |  |  | RES,FXD,FILM:1.2K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-12000J |
| A10R941 | 311-2229-00 |  |  | RES,VAR,NONWW:TRMR,250 OHM, $20 \%, 0.5 W$ LINEAR | TK2073 | GF06UT2 251 ML |
| A10R1001 | 322-3232-00 |  |  | RES,FXD,FILM:2.55K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G25500F |
| A10R1002 | 322-3232-00 |  |  | RES,FXD,FILM:2.55K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G25500F |
| A10R1003 | 313-1512-00 |  |  | RES,FXD,FILM:5.1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-51000J |
| A10R1004 | 322-3232-00 |  |  | RES,FXD,FILM:2.55K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G25500F |
| A10R1005 | 322-3251-00 |  |  | RES,FXD,FILM: 4.02 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G40200F |
| A10R1006 | 322-3184-00 |  |  | RES,FXD,FILM:806 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G806FOR |
| A10R1007 | 322-3251-00 |  |  | RES,FXD,FILM:4.02K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G40200F |
| A10R1008 | 322-3184-00 |  |  | RES,FXD,FILM:806 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G806FOR |
| A10R1009 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A10R1010 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R1020 | 313-1272-00 |  |  | RES,FXD,FILM:2.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-27000J |
| A10R1021 | 313-1512-00 |  |  | RES,FXD,FILM:5.1K OHM,5\%,0.2W | 91637 | CCF50-2-51000」 |
| A10R1022 | 313-1272-00 |  |  | RES,FXD,FILM:2.7K OHM,5\%,0.2W | 91637 | CCF50-2-27000J |
| A10R1023 | 313-1512-00 |  |  | RES,FXD,FILM:5.1K OHM,5\%,0.2W | 91637 | CCF50-2-51000J |
| A10R1024 | 313-1272-00 |  |  | RES,FXD,FILM:2.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-27000J |
| A10R1025 | 313-1512-00 |  |  | RES,FXD,FILM: 5.1 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-51000J |
| A10R1026 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10ROOJ |
| A10R1027 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10ROOJ |
| A10R1028 | 313-1102-00 |  |  | RES,FXD,FILM:4K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A10R1101 | 313-1100-00 | B100100 | B111186 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10ROOJ |
| A10R1101 | 313-1100-00 | B700100 | B710630 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10R00J |
| A10R1102 | 313-1100-00 | B100100 | B111186 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10R00. |
| A10R1102 | 313-1100-00 | B700100 | B710630 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10ROOJ |
| A10R1103 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A10R1104 | 313-1682-00 |  |  | RES,FXD,FILM: 6.8 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-68000J |
| A10R1105 | 313-1511-00 | B110100 |  | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-510ROJ |
| A10R1105 | 313-1511-00 | B710100 |  | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-510ROJ |
| A10R1106 | 313-1511-00 | B110100 |  | RES,FXD,FILM:510 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-510ROJ |
| A10R1106 | 313-1511-00 | B710100 |  | RES,FXD,FILM:510 OHM,5\%,0,2W (2246 1Y) | 91637 | CCF50-2-510ROJ |
| A10R1108 | 313-1100-00 | B710630 |  | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10ROOJ |
| A10R1108 | 313-1100-00 | B111186 |  | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 MOD A.2Y) | 91637 | CCF50-2-10ROOJ |
| A10R1110 | 313-1682-00 |  |  | RES,FXD,FILM:6.8K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-68000J |


| Component Number | Tektronlx <br> Part No. | Serial No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10R1111 | 313-1303-00 |  | RES,FXD,FILM:30K OHM,5\%,0.2W | 91637 | CCF50-2-30007J |
| A10R1112 | 313-1302-00 |  | RES,FXD,FILM:3K ОНM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-30000J |
| A10R1113 | 313-1101-00 |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R1114 | 313-1103-00 |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R1115 | 313-1682-00 |  | RES,FXD,FILM:6.8K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-68000J |
| A10R1116 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, 5\%,0.2W | 91637 | CCF50-2-10000J |
| A10R1117 | 313-1162-00 |  | RES,FXD,FILM:1.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-16000 J |
| A10R1118 | 313-1751-00 |  | RES,FXD,FILM:750 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-750ROJ |
| A1ORI120 | 313-1682-00 |  | RES,FXD,FILM:6.8K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-68000J |
| A10R1121 | 313-1303-00 |  | RES,FXD,FILM:30K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-30001J |
| A10R1122 | 313-1302-00 |  | RES,FXD,FILM:3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-30000J |
| A10R1123 | 313-1101-00 |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A10R1124 | 313-1103-00 |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R1125 | 313-1682-00 |  | RES,FXD,FILM: 6.8 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-68000 J |
| A10R1126 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000 J |
| A10R1127 | 313-1162-00 |  | RES,FXD,FILM:1.6K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-16000 J |
| A10R1128 | 313-1751-00 |  | RES,FXD,FILM:750 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-750ROJ |
| A10R1131 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000 J |
| A10R1132 | 313-1223-00 |  | RES,FXD,FILM:22K OHM,5\%,0.2W | 91637 | CCF50-2-22001J |
| A10R1133 | 313-1104-00 |  | RES,FXD,FILM: 100 K OHM,5\%,0.2W | 91637 | CCF50-2-10002 J |
| A10R1134 | 313-1472-00 |  | RES, FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000J |
| A10R1135 | 313-1204-00 |  | RES,FXD,FILM:200K,5\%,0.2W | 91637 | CCF50-2-20002 J |
| A10R1136 | 313-1204-00 |  | RES,FXD,FILM:200K,5\%,0.2W | 91637 | CCF50-2-20002 J |
| A10R1142 | 313-1223-00 |  | RES,FXD,FILM:22K,OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-22001J |
| A10R1143 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000 J |
| A10R1144 | 313-1753-00 |  | RES,FXD,FILM:75K OHM,5\%,0.2W | 91637 | CCF50-2-75001. |
| A10R1145 | 313-1102-00 |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A10R1150 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000. |
| A10R1154 | 315-0205-00 |  | RES,FXD,FILM:2M OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R1155 | 315-0205-00 |  | RES,FXD,FILM:2M OHM,5\%,0.25W | TK1727 | SFR25 2322-181- |
| A10R1158 | 313-1100-00 |  | RES,FXD,FILM: 10 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10RO0. |
| A10R1159 | 313-1100-00 |  | RES,FXD,FILM:10 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10R00. |
| A10R1160 | 313-1100-00 | B110100 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10R00. |
| A10R1160 | 313-1100-00 | B710100 | RES,FXD,FILM:10 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10R00」 |
| A10R1162 | 313-1302-00 |  | RES,FXD,FILM:3K OHM,5\%,0.2W | 91637 | CCF50-2-30000J |
| A10R1163 | 313-1152-00 |  | RES,FXD,FILM:1.5K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-15000J |
| A10R1170 | 313-1152-00 |  | RES,FXD,FILM:1.5K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-15000J |
| A10R1210 | 313-1101-00 | B120000 | RES,FXD,FILM:100 OHM, $5 \%, 0.2 \mathrm{~W}$ ( 2246 MOD A, 2 Y ) | 91637 | CCF50-2-100ROJ |
| A10R1210 | 313-1101-00 | B715000 | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-100ROJ |
| A10R1211 | 313-1101-00 | B120000 | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-100ROJ |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R1211 | 313-1101-00 | B715000 |  | RES,FXD,FILM: 100 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-100ROJ |
| A10R1220 | 313-1101-00 | B120000 |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A.2Y) | 91637 | CCF50-2-100ROS |
| A10R1220 | 313-1101-00 | B715000 |  | RES,FXD,FILM: 100 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-100ROJ |
| A10R1221 | 313-1101-00 | B120000 |  | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-100ROJ |
| A10R1221 | 313-1101-00 | B715000 |  | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-100ROJ |
| A10R1230 | 313-1101-00 | B120000 |  | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 MOD A.2Y) | 91637 | CCF50-2-100ROJ |
| A10R1230 | 313-1101-00 | 8715000 |  | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-100ROJ |
| A10R1231 | 313-1101-00 | B120000 |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-100ROJ |
| A10R1231 | 313-1101-00 | B715000 |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-100ROJ |
| A10R1240 | 313-1101-00 | B120000 |  | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-100ROJ |
| A10R1240 | 313-1101-00 | B715000 |  | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-100ROJ |
| A10R1241 | 313-1101-00 | B120000 |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 MOD A,2Y) | 91637 | CCF50-2-100ROJ |
| A10R1241 | 313-1101-00 | B715000 |  | RES,FXD,FILM:100 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-100ROJ |
| A10R1250 | 322-3059-00 | B120000 |  | RES,FXD,FILM:40.2 ОНM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI (2246 MOD A,2Y) | 91637 | CCF50-2G40R20FT |
| A10R1250 | 322-3059-00 | B715000 |  | $\begin{aligned} & \text { RES,FXD,FILM: } 40.2 \text { OHM, } 1 \%, 0.2 \mathrm{~W}, \text { TC }=\text { TOMI } \\ & (2246 ~ 1 Y) \end{aligned}$ | 91637 | CCF50-2G40R20FT |
| A10R1251 | 322-3059-00 | B120000 |  | $\text { RES,FXD,FILM:40.2 OHM, } 1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=\text { TOMI }$ (2246 MOD A.2Y) | 91637 | CCF50-2G40R20FT |
| A10R1251 | 322-3059-00 | B715000 |  | $\begin{aligned} & \text { RES,FXD,FILM:40.2 OHM, } 1 \%, 0.2 \mathrm{~W}, \text { TC }=\text { TOMI } \\ & (2246 \mathrm{TY}) \end{aligned}$ | 91637 | CCF50-2G40R20FT |
| A10R1260 | 313-1102-00 | B120000 |  | RES,FXD,FILM:1K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-10000J |
| A10R1260 | 313-1102-00 | B715000 |  | RES,FXD,FILM:1K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-10000J |
| A10R1284 | 313-1820-00 | B120000 |  | RES,FXD,FILM:82 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-82R00J |
| A10R1284 | 313-1820-00 | B715000 |  | RES,FXD,FILM:82 OHM, $5 \%, 0.2 \mathrm{~W}$ (2246 1Y) | 91637 | CCF50-2-82R00J |
| A10R1285 | 313-1820-00 | B120000 |  | RES,FXD,FILM:82 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-82R00J |
| A10R1285 | 313-1820-00 | B715000 |  | RES,FXD,FILM:82 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-82R00J |
| A10R2116 | 311-2329-00 | B700100 | B710099 | RES,VAR,NONWW:TRIMMER,5K OHM,10\% (2246 1Y) | 32997 | 3386R-EA5-502 |
| A10R2116 | 311-2329-00 | B100100 | B110099 | RES,VAR,NONWW:TRIMMER,5K OHM,10\% (2246 MOD A.2Y) | 32997 | 3386R-EA5-502 |

Replaceable Electrical Parts

| Component Number | Tektronlx <br> Part No. | Serial No. Effective Dscont | Name \& Description | Mfr. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10R2701 | 322-3112-00 |  | RES,FXD,FILM:143 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G143R0F |
| A10R2702 | 313-1393-00 |  | RES, FXD,FILM:39K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-39001J |
| A10R2703 | 322-3282-00 |  | RES,FXD,FILM: 8.45 K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G84500F |
| A10R2704 | 322-3164-00 |  | RES,FXD,FILM: 499 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOM | 91637 | CCF501G499ROF |
| A10R2705 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A10R2706 | 313-1103-00 |  | RES,FXD,FILM: 10 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A10R2708 | 322-3289-00 |  | RES,FXD,FILM:10K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF50G10001F |
| A10R2709 | 322-3289-00 |  | RES,FXD,FILM:10K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF50G10001F |
| A10R2710 | 313-1331-00 |  | RES,FXD,FILM:330 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-330ROJ |
| A10R2711 | 313-1333-00 |  | RES,FXD,FILM:33K OHM,5\%,0.2W | 91637 | CCF50-2-33001J |
| A10R2712 | 313-1333-00 |  | RES,FXD,FILM:33K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-33001J |
| A10R2713 | 313-1333-00 |  | RES,FXD,FILM:33K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-33001J |
| A10R2714 | 313-1333-00 |  | RES,FXD,FILM:33K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-33001J |
| A10R2715 | 313-1104-00 |  | RES,FXD,FILM:100K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10002J |
| A10R2716 | 313-1333-00 |  | RES,FXD,FILM:33K OHM,5\%,0.2W | 91637 | CCF50-2-33001J |
| A10R2717 | 313-1562-00 |  | RES,FXD,FILM: 5.6 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-56000 J |
| A10R2718 | 313-0750-00 |  | RES,FXD,CMPSN:75 OHM, $5 \%, 0.17 \mathrm{~W}$ | 56845 | CCF50-2-G68ROJ |
| A10R2719 | 311-2236-00 |  | RES,VAR,NONWW:TRMR,2OK OHM, $20 \%, 0.5 \mathrm{~W}$ LINEAR | TK2073 | GF06UT2 203 ML |
| A10R2720 | 315-0203-00 |  | RES,FXD,FILM:20K OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R2721 | 315-0472-00 |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R2722 | 315-0244-00 |  | RES,FXD,FILM:240K OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R2723 | 315-0511-00 |  | RES,FXD,FILM:510 OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R2724 | 315-0625-00 |  | RES,FXD,FILM:6.2M OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R2726 | 313-1101-00 |  | RES,FXD,FILM: 100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A10R2727 | 322-3213-00 |  | RES,FXD,FILM:1.62K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G16200F |
| A10R2728 | 313-1200-00 |  | RES,FXD,FILM:20 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-20R00J |
| A10R2729 | 322-3210-00 |  | RES,FXD,FILM:1.5K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G15000F |
| A10R2733 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000 J |
| A10R2734 | 313-1101-00 |  | RES,FXD,FILM: 100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A10R2735 | 315-0122-00 |  | RES,FXD,FILM:1.2K OHM,5\%,0.25W | TK1727 | SFR25 2322-181- |
| A10R2736 | 301-0203-00 |  | RES,FXD,FILM:20K OHM,5\%,0.5W | TK1727 | SFR30 2322-182- |
| A10R2737 | 313-1104-00 |  | RES,FXD,FILM:100K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10002J |
| A10R2738 | 313-1333-00 |  | RES, FXD,FILM:33K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-33001J |
| A10R2739 | 313-1752-00 |  | RES,FXD,FILM:7.5K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-75000 J |
| A10R2740 | 315-0750-00 |  | RES,FXD,FILM:75 OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R2741 | 315-0472-00 |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R2742 | 315-0244-00 |  | RES,FXD,FILM:240K OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R2743 | 315-0122-00 |  | RES,FXD,FILM:1.2K OHM, $5 \%, 0,25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R2745 | 315-0122-00 |  | RES,FXD,FILM:1.2K OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |
| A10R2750 | 315-0511-00 |  | RES,FXD,FILM:510 OHM, $5 \%, 0.25 \mathrm{~W}$ | TK1727 | SFR25 2322-181- |

Replaceable Electrical Parts
22461Y and 2246 MOD A Service

| Component Number | Tektronlx Part No. | Serlal Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10R2751 | 315-0625-00 |  |  | RES,FXD,FILM:6.2M OHM,5\%,0.25W | TK1727 | SFR25 2322-181- |
| A10R2758 | 311-1933-00 |  |  | RES,VAR,NONWW:PNL,5M OHM, 10\%,0.5W | 12697 | 23M909 |
| A10R2760 | 307-2173-00 |  |  | RES NTWK,FXD,FI:HIGH VOLTAGE,FINISHEDH2173 | 80009 | 307217300 |
| A10R2765 | 322-3188-00 |  |  | RES,FXD,FILM:887 OHM,1\%,0.2W,TC = TOMI | 91637 | CCF501G887R0F |
| A10R2783 | 313-1101-00 |  |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A10R2784 | 311-2239-00 |  |  | RES,VAR,NONWW:TRMR,100K OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 104 M L |
| A10R2785 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A10R2786 | 313-1753-00 |  |  | RES,FXD,FILM:75K OHM,5\%,0.2W | 91637 | CCF50-2-75001J |
| A10R2787 | 313-1333-00 |  |  | RES,FXD,FILM:33K OHM,5\%,0.2W | 91637 | CCF50-2-33001J |
| A10R2788 | 311-2239-00 |  |  | RES,VAR,NONWW:TRMR,100K OHM,20\%,0.5W LINEAR | TK2073 | GF06UT2 104 M L |
| A10R2789 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A10R2795 | 322-3268-00 |  |  | RES,FXD,FILM:6.04K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G60400F |
| A10R2796 | 313-1100-00 |  |  | RES,FXD,FILM:10 OHM,5\%,0.2W | 91637 | CCF50-2-10R00J |
| A10RT201 | 307-0181-00 |  |  | RES,THERMAL:100K OHM,10\%,NTC | 14193 | K10-10002K |
| A10U112 | 165-2232-00 | B100100 | B101582 | MICROCKT,LINEAR:BUFFER AMPLIFIER (2246 MOD A,2Y) | 80009 | 165223200 |
| A10U112 | 165-2232-01 | B101583 |  | MICROCKT,LINEAR:BUFFER AMPLIFIER W/3 GAIN SETTINGS <br> (2246 MOD A,2Y) | 80009 | 165223201 |
| A10U112 | 165-2232-00 | B700100 | B700493 | MICROCKT,LINEAR:BUFFER AMPLIFIER (2246 1Y) | 80009 | 165223200 |
| A10U112 | 165-2232-01 | B700494 |  | MICROCKT,LINEAR:BUFFER AMPLIFIER W/3 GAIN SETTINGS $(22461 Y)$ | 80009 | 165223201 |
| A10U122 | 165-2232-00 | B100100 | B101582 | MICROCKT,LINEAR:BUFFER AMPLIFIER (2246 MOD A,2Y) | 80009 | 165223200 |
| A10U122 | 165-2232-01 | B101583 |  | MICROCKT,LINEAR:BUFFER AMPLIFIER W/3 GAIN SETTINGS <br> (2246 MOD A,2Y) | 80009 | 165223201 |
| A10U122 | 165-2232-00 | B700100 | B700493 | MICROCKT,LINEAR:BUFFER AMPLIFIER (2246 1Y) | 80009 | 165223200 |
| A10U122 | 165-2232-01 | B700494 |  | MICROCKT,LINEAR:BUFFER AMPLIFIER W/3 GAIN SETTINGS $(2246 \text { 1Y) }$ | 80009 | 165223201 |
| A10U171 | 156-0796-00 |  |  | IC,DIGITAL:CMOS,SHIFT REGISTER;8-STAGE SHIFT/STORE, 3-STATE | 04713 | MC14094BCP |
| A10U172 | 156-0796-00 |  |  | IC,DIGITAL:CMOS,SHIFT REGISTER;8-STAGE SHIFT/STORE, 3-STATE | 04713 | MC14094BCP |
| A10U173 | 156-0796-00 | B100100 | B119999 | IC,DIGITAL:CMOS,SHIFT REGISTER;8-STAGE SHIFT/STORE, 3-STATE <br> (2246 MOD A,2Y) | 04713 | MC14094BCP |


| Component Number | Tektronix Part No. | Serial | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10U173 | 156-3944-00 | B120000 |  | IC,DIGITAL:HCTCMOS,REGISTER;8-STAGE SHIFT AND STORE BUS REGISTER <br> ( 2246 MOD A,2Y) | 18324 | 74HCT4094N |
| A10U173 | 156-0796-00 | B700100 | B714999 | IC,DIGITAL:CMOS,SHIFT REGISTER;8-STAGE SHIFT/STORE, 3-STATE <br> (2246 1Y) | 04713 | MC14094BCP |
| A10U173 | 156-3944-00 | B715000 |  | IC,DIGITAL:HCTCMOS,REGISTER;8-STAGE SHIFT AND STORE BUS REGISTER (2246 1Y) | 18324 | 74HCT4094N |
| A10U174 | 156-1190-00 |  |  | IC.LINEAR:BIPOLAR,TRANSISTOR <br> ARRAY;SEVEN,NPN,COMMON COLLECTOR,OPEN EMITTER | 34371 | САЗ082-98 |
| A10U175 | 156-1190-00 |  |  | IC.LINEAR:BIPOLAR,TRANSISTOR <br> ARRAY;SEVEN,NPN,COMMON COLLECTOR,OPEN EMITTER | 34371 | СА3082-98 |
| A10U201 | 156-2571-00 |  |  | IC,MISC:HCMOS,ANALOG MUX;TRIPLE SPDT | 04713 | MC74HC4053N/J |
| A10U202 | 156-2571-00 |  |  | IC,MISC:HCMOS,ANALOG MUX;TRIPLE SPDT | 04713 | MC74HC4053N/J |
| A10U203 | 156-2667-00 |  |  | MICROCKT,LINEAR:QUAD LOW PWR, OPERATIONAL AMPLIFIERS MC3403,14 DIP,MI | 01295 | MC3403N |
| A10U210 | 234-0238-20 |  |  | QUICK CHIP:VERTICAL PREAMP,PACKAGE IC | 80009 | 234023820 |
| A10U220 | 234-0238-20 |  |  | QUICK CHIP:VERTICAL PREAMP,PACKAGE IC | 80009 | 234023820 |
| A10U230 | 234-0238-20 |  |  | QUICK CHIP:VERTICAL PREAMP,PACKAGE IC | 80009 | 234023820 |
| A10U240 | 234-0238-20 |  |  | QUICK CHIP:VERTICAL PREAMP,PACKAGE IC | 80009 | 234023820 |
| A10U260 | 156-0067-00 |  |  | IC,LINEAR:BIPOLAR,OP-AMP | 04713 | MC1741CP1 |
| A10U280 | 156-1349-00 |  |  | IC,LINEAR:BIPOLAR,AMPLIFIER;DUAL INDEPENDENT DIFFERENTIAL AMPLIFIER | 34371 | CA3054-98 |
| A10U301 | 156-2571-00 |  |  | IC,MISC:HCMOS,ANALOG MUX;TRIPLE SPDT | 04713 | MC74HC4053N/J |
| A10U302 | 156-0796-00 |  |  | IC,DIGITAL:CMOS,SHIFT REGISTER;8-STAGE SHIFT/STORE, 3-STATE | 04713 | MC14094BCP |
| A10U303 | 156-0796-00 |  |  | IC,DIGITAL:CMOS,SHIFT REGISTER;8-STAGE SHIFT/STORE, 3-STATE | 04713 | MC14094BCP |
| A10U304 | 156-2873-00 |  |  | MICROCKT,LINEAR:DUAL BIFET,OPERATIONAL AMPLIFIER | 04713 | MC34082AP (D/C |
| A10U307 | 156-0514-00 |  |  | IC,MISC:CMOS,ANALOG MUX;DUAL 4 CHANNEL | 04713 | MC14052BCP |
| A10U308 | 156-0514-00 |  |  | IC,MISC:CMOS,ANALOG MUX;DUAL 4 CHANNEL | 04713 | MC14052BCP |
| A10U309 | 156-0158-07 | B100100 | B119999 | IC,LINEAR:BIPOLAR,OP-AMP (2246 MOD A,2Y) | 01295 | MC1458P |
| A10U309 | 156-0158-00 | B120000 |  | IC,LINEAR:BIPOLAR,OP-AMP;DUAL (2246 MOD A,2Y) | 04713 | MC1458P1 |
| A10U309 | 156-0158-07 | B700100 | B714999 | IC,LINEAR:BIPOLAR,OP-AMP (2246 1Y) | 01295 | MC1458P |
| A10U309 | 156-0158-00 | B715000 |  | IC,LINEAR:BIPOLAR,OP-AMP;DUAL (2246 1Y) | 04713 | MC1458P1 |
| A10U310 | 156-0514-00 |  |  | IC,MISC:CMOS,ANALOG MUX;DUAL 4 CHANNEL | 04713 | MC14052BCP |
| A10U311 | 156-0514-00 |  |  | IC,MISC:CMOS,ANALOG MUX;DUAL 4 CHANNEL | 04713 | MC14052BCP |
| A10U313 | 156-1349-00 |  |  | IC,LINEAR:BIPOLAR,AMPLIFIER;DUAL INDEPENDENT DIFFERENTIAL AMPLIFIER | 34371 | CA3054-98 |
| A10U315 | 156-1640-00 |  |  | IC,DIGITAL:ECL,RECEIVER;TRIPLE LINE | 04713 | MC10H116(L OR P |
| A10U316 | 156-0308-00 |  |  | IC,DIGITAL:ECL,RECEIVER;QUAD DIFFERENTIAL LINE | 04713 | MC10115L OR P |
| A10U421 | 234-0239-20 | B700100 | B710099 | QUICK CHIP:TRIGGER,IC PACKAGE (2246 1Y) | 80009 | 234023920 |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effectlve | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10U421 | 234-0239-30 | 8710100 | B710999 | QUICK CHIP:TRIGGER IC PACKAGE (2246 1Y) | 80009 | 234023930 |
| A10U421 | 234-0239-31 | B711000 |  | QUICK CHIP:TRIGGER CIRCUIT,28PLCC W/AU LEAD (2246 1Y) | 80009 | 234023931 |
| A10U421 | 234-0239-20 | B100100 | B110099 | QUICK CHIP:TRIGGER,IC PACKAGE (2246 MOD A,2Y) | 80009 | 234023920 |
| A10U421 | 234-0239-30 | B110100 | B112868 | QUICK CHIP:TRIGGER IC PACKAGE (2246 MOD A,2Y) | 80009 | 234023930 |
| A10U421 | 234-0239-31 | B112869 |  | QUICK CHIP:TRIGGER CIRCUIT,28PLCC W/AU LEAD <br> (2246 MOD A,2Y) | 80009 | 234023931 |
| A10U431 | 234-0239-20 | B700100 | B710099 | QUICK CHIP:TRIGGER,IC PACKAGE (2246 1Y) | 80009 | 234023920 |
| A10U431 | 234-0239-30 | B710100 | B710999 | QUICK CHIP:TRIGGER IC PACKAGE (2246 1Y) | 80009 | 234023930 |
| A10U431 | 234-0239-31 | B711000 |  | QUICK CHIP:TRIGGER CIRCUIT,28PLCC W/AU LEAD $(22461 Y)$ | 80009 | 234023931 |
| A10U431 | 234-0239-20 | B100100 | B110099 | QUICK CHIP:TRIGGER,IC PACKAGE (2246 MOD A,2Y) | 80009 | 234023920 |
| A10U431 | 234-0239-30 | B110100 | B112868 | QUICK CHIP:TRIGGER IC PACKAGE (2246 MOD A,2Y) | 80009 | 234023930 |
| A10U431 | 234-0239-31 | B112869 |  | QUICK CHIP:TRIGGER CIRCUIT,28PLCC W/AU LEAD <br> (2246 MOD A,2Y) | 80009 | 234023931 |
| A10U441 | 156-2027-00 |  |  | IC,DIGITAL:HCMOS,GATE;HEX INV | 27014 | MM74HC04N |
| A10U442 | 156-1640-00 | B110100 |  | IC,DIGITAL:ECL,RECEIVER;TRIPLE LINE (2246 MOD A,2Y) | 04713 | MC10H116(L OR P |
| A10U442 | 156-1640-00 | B710100 |  | IC,DIGITAL:ECL,RECEIVER;TRIPLE LINE (2246 1Y) | 04713 | MC1OH116(L OR P |
| A10U501 | 156-0469-00 |  |  | IC,DIGITAL:LSTTL,DEMUXJDECODER;3-TO-8 DECODER | 01295 | SN74LS138 (N OR |
| A10U502 | 156-0768-00 |  |  | IC,DIGITAL:LSTTL,SHIFT REGISTER;8-BIT BIDIRECTIONAL UNIVERSAL | 01295 | SN74LS194AN |
| A10U503 | 156-0804-00 |  |  | IC,DIGITAL:LSTTL,LATCH;QUADRUPLE S-R | 01295 | SN74LS279AN |
| A10U506 | 156-0513-00 |  |  | IC,MISC:CMOS,ANALOG MUX;8 CHANNEL | 04713 | MC14051B (CP OR |
| A10U600 | 156-2655-00 |  |  | MICROCKT,DGTL:CMOS,SEMI CUSTOM,STD CELL,SLOW LOGIC | 66302 | VF4247PC CC0001 |
| A10U601 | 156-1126-00 |  |  | IC,LINEAR:BIPOLAR,COMPARATOR;OPEN COLLECTOR,200NS | 01295 | LM311P |
| A10U602 | 156-2654-00 |  |  | MICROCKT,DGTL:ECL,SEMI CUSTOM,FAST LOGIC SCX32205LB04,40 DIPCERAMIC ONLY | 04713 | SC32205LB04 |
| A10U603 | 156-0631-00 |  |  | IC.DIGITAL:ECL,GATE;QUAD 2-INPUT OR/NOR | 04713 | MC10101(L OR P) |
| A10U604 | 156-0860-00 |  |  | IC,DIGITAL:ECL,RECEIVER;TRIPLE LINE | 04713 | MC10116P |
| A10U606 | 156-0140-00 |  |  | IC,DIGITAL:TTL,BUFFER/DRIVER;HEX BUFFER, OC | 01295 | SN7417N |
| A10U701 | 155-0322-00 |  |  | MICROCKT,LINEAR:VERTICAL OUTPUT AMPLIFIERM429 | 80009 | 155032200 |


| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10U702 | 156-1126-00 |  |  | IC,LINEAR:BIPOLAR,COMPARATOR;OPEN COLLECTOR,200NS | 01295 | LM311P |
| A10U801 | 156-0158-07 | B100100 | B119999 | IC,LINEAR:BIPOLAR,OP-AMP (2246 MOD A, 2 Y) | 01295 | MC1458P |
| A10U801 | 156-0158-00 | B120000 |  | IC,LINEAR:BIPOLAR,OP-AMP;DUAL (2246 MOD A,2Y) | 04713 | MC1458P1 |
| A10U801 | 156-0158-07 | B700100 | B714999 | IC,LINEAR:BIPOLAR,OP-AMP (2246 1Y) | 01295 | MC1458P |
| A10U801 | 156-0158-00 | B715000 |  | IC,LINEAR:BIPOLAR,OP-AMP;DUAL (2246 1Y) | 04713 | MC1458P1 |
| A10U802 | 234-0401-21 |  |  | QUICK CHIP:GPS HORIZ PREAMP | 80009 | 234040121 |
| A10U901 | 156-2702-00 |  |  | IC,LINEAR:BIPOLAR,OP-AMP;DUAL,HIGH OUTPUT CURRENT | 57668 | BA4560 |
| A10U930 | 156-0158-07 | B100100 | B119999 | IC,LINEAR:BIPOLAR,OP-AMP (2246 MOD A,2Y) | 01295 | MC1458P |
| A10U930 | 156-0158-00 | B120000 |  | IC,LINEAR:BIPOLAR,OP-AMP;DUAL (2246 MOD A,2Y) | 04713 | MC1458P1 |
| A10U930 | 156-0158-07 | B700100 | B714999 | IC,LINEAR:BIPOLAR,OP-AMP (2246 1Y) | 01295 | MC1458P |
| A10U930 | 156-0158-00 | B715000 |  | IC,LINEAR:BIPOLAR,OP-AMP;DUAL (2246 1Y) | 04713 | MC1458P1 |
| A10U931 | 156-2605-00 |  |  | IC,MISC:HCMOS,ANALOG MUX;8 CHANNEL | 04713 | MC74HC4051N/J |
| A10U932 | 156-1173-00 |  |  | IC,LINEAR:BIPOLAR,VOLTAGE REFERENCE;POSITIVE,2.5V,1.0\%,40PPM,SERIES | 04713 | MC1403U |
| A10U1001 | 156-0495-00 |  |  | IC,LINEAR:BIPOLAR,OP-AMP;QUAD,SINGLE SUPPLY | 01295 | LM324N |
| A10U1101 | 156-2873-00 |  |  | MICROCKT,LINEAR:DUAL BIFET,OPERATIONAL AMPLIFIER | 04713 | MC34082AP (D/C |
| A10U1102 | 156-1225-00 |  |  | IC,LINEAR:BIPOLAR,COMPARATOR;DUAL,OPEN COLLECTOR,300NS | 01295 | LM393P |
| A10U1103 | 156-0796-00 |  |  | IC,DIGITAL:CMOS,SHIFT REGISTER;8-STAGE SHIFT/STORE, 3-STATE | 04713 | MC14094BCP |
| A10U1104 | 156-0515-00 |  |  | IC,MISC:CMOS,ANALOG MUX;TRIPLE SPDT | 04713 | MC14053BCP |
| A10U1106 | 156-0515-00 |  |  | IC,MISC:CMOS,ANALOG MUX;TRIPLE SPDT | 04713 | MC14053BCP |
| A10VR301 | 152-0437-00 |  |  | DIODE,ZENER:,;8.2V,2\%,0.4W | 04713 | 1N959CRL |
| A10VR302 | 152-0437-00 |  |  | DIODE,ZENER:;8.2V,2\%,0.4W | 04713 | 1N959CRL |
| A10VR303 | 152-0437-00 |  |  | DIODE,ZENER:;8.2V,2\%,0.4W | 04713 | 1N959CRL |
| A10VR304 | 152-0437-00 |  |  | DIODE,ZENER:;8.2V,2\%,0.4W | 04713 | 1N959CRL |
| A10VR308 | 152-0437-00 |  |  | DIODE,ZENER:,;8.2V,2\%,0.4W | 04713 | 1N959CRL |
| A10VR309 | 152-0166-00 |  |  | DIODE,ZENER:,;6.2V,5\%,0.4W | 04713 | SZ11738RL |
| A10VR310 | 152-0166-00 |  |  | DIODE,ZENER:;6.2V,5\%,0.4W | 04713 | SZ11738RL |
| A10VR311 | 152-0168-00 |  |  | DIODE,ZENER:,;12V,5\%,0.4W | 04713 | 1N9638RL |
| A10VR312 | 152-0168-00 |  |  | DIODE,ZENER:,,12V,5\%,0.4W | 04713 | 1N963BRL |
| A10VR801 | 152-0243-00 | B100100 | B110099 | DIODE,ZENER:;;15V,5\%,0.4W (2246 MOD A,2Y) | 04713 | SZ13203 (1N965B |
| A10VR801 | 152-0055-00 | B110100 |  | DIODE,ZENER:;;11V,5\%,0.4W (2246 MOD A,2Y) | 04713 | SZG35009K1 1N96 |
| A10VR801 | 152-0243-00 | B700100 | B710099 | $\begin{aligned} & \text { DIODE,ZENER:;;15V,5\%,0.4W } \\ & (2246 \text { iY) } \end{aligned}$ | 04713 | SZ13203 (TN965B |
| A10VR801 | 152-0055-00 | B710100 |  | $\begin{aligned} & \text { DIODE,ZENER:;;11V,5\%,0.4W } \\ & (2246 \text { iY) } \end{aligned}$ | 04713 | SZG35009K1 1N96 |

Replaceable Electrical Parts 22461Y and 2246 MOD A Service

| Component Number | Tektronix <br> Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10VR802 | 152-0265-00 | B100100 | B110099 | DIODE,ZENER:; ;24V,5\%,0.4W (2246 MOD A, $2 Y$ ) | 04713 | 1N970BRL |
| A10VR802 | 152-0265-00 | B700100 | B710099 | DIODE,ZENER:,;24V,5\%,0.4W (2246 1Y) | 04713 | 1N970BRL |
| A10VR2701 | 152-0306-00 |  |  | SEMICOND DVC,DI:ZEN,SI,9.1V,5\%,0.4W,DO-71N9 60B,M1 | 04713 | 1N960BRL |
| A10W9 | 198-5523-00 |  |  | WIRE SET,ELEC:SOCKET ASSY CRT, | OJ7N9 | PER TEK DRAWING |
| A10W17 | 196-3069-00 |  |  | LEAD,ELECTRICAL:22 AWG,5.0 L,9-N | OJ7N9 | PER TEK DRAWING |
| A10W18 | 196-3069-00 |  |  | LEAD,ELECTRICAL:22 AWG, 5.0 L,9-N | 0.J7N9 | PER TEK DRAWING |
| A10W19 | 196-3069-00 |  |  | LEAD, ELECTRICAL:22 AWG,5.0 L.9-N | OJ7N9 | PER TEK DRAWING |
| A10W20 | 196-3069-00 |  |  | LEAD, ELECTRICAL:22 AWG,5.0 L,9-N | OJ7N9 | PER TEK DRAWING |
| A10W100 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W101 | 131-0566-00 | B110100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A.2Y) | 24546 | OMA 07 |
| A10W101 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD X 0.225L (2246 1Y) | 24546 | OMA 07 |
| A10W102 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, $0.094 \mathrm{OD} \times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W103 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W200 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W201 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W202 | 131-0566-00 | B100100 | B119999 | BUS,CONDUCTOR:DUMMY RES,0.094 OD $\times 0.225 \mathrm{~L}$ ( 2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W202 | 131-0566-00 | B700100 | B714999 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y | 24546 | OMA 07 |
| A10W203 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W204 | 131-0566-00 | B120000 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W204 | 131-0566-00 | B715000 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W205 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W206 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W207 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W208 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W209 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W210 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W211 | 195-0970-00 |  |  | LEAD,ELECTRICAL:26 AWG, 3.0 L, $0-\mathrm{N}$ | 80009 | 195097000 |
| A10W223 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W231 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W232 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W235 | 131-0566-00 | B100100 | B119999 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225 L ( 2246 MOD A. 2 Y ) | 24546 | OMA 07 |
| A10W235 | 131-0566-00 | B700100 | B714999 | BUS,CONDUCTOR:DUMMY RES,0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W304 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W305 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |


| Component Number | Tektronix Part No. | Serial Effectlve | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10W401 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W403 | 131-0566-00 | B100100 | B110099 | BUS.CONDUCTOR:DUMMY RES,0.094 OD X 0.225L (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W403 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L (2246 1Y) | 24546 | OMA 07 |
| A10W404 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 L$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W404 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES,0.094 OD X 0.225L (2246 iY) | 24546 | OMA 07 |
| A10W405 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES,0.094 OD X 0.225L (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W405 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L (2246 1Y) | 24546 | OMA 07 |
| A10W406 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W407 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES,0.094 OD X 0.225L (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W407 | 131-0566-00 | 8700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L (2246 1Y) | 24546 | OMA 07 |
| A10W408 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES,0.094 OD X $0.225 L$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W408 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W410 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 L$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W410 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W411 | 131-0566-00 | 8100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W411 | 131-0566-00 | 8700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L (2246 1Y) | 24546 | OMA 07 |
| A10W412 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W412 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES,0.094 OD X 0.225L (2246 1Y) | 24546 | OMA 07 |
| A10W413 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W414 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W415 | 174-0733-01 |  |  | CA ASSY,SP,ELEC:4,26 AWG,4.5 L,RIBBON | 0.37N9 | ORDER BY DESC |
| A10W416 | 174-0732-01 |  |  | CA ASSY,SP,ELEC:4,26 AWG,3.0 L,RIBBON | 0.J7N9 | ORDER BY DESC |
| A10W501 | 131-0566-00 | B110100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W501 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W502 | 131-0566-00 | B110100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W502 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ ( 2246 1Y) | 24546 | OMA 07 |
| A10W503 | 131-0566-00 | B110100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W503 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W504 | 131-0566-00 | B110100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W504 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |

Replaceable Electrical Parts
22461Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10W505 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W506 | 131-0566-00 | B110100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ ( 2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W506 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L (2246 1Y) | 24546 | OMA 07 |
| A10W507 | 131-0566-00 | B110100 |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD X0.225L (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W507 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD X0.225L (2246 1Y) | 24546 | OMA 07 |
| A10W510 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W603 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W604 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W605 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W606 | 131-0566-00 | B110100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W606 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W607 | 131-0566-00 | B110100 |  | BUS.CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A, 2 Y ) | 24546 | OMA 07 |
| A10W607 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD X 0.225L (2246 1Y) | 24546 | OMA 07 |
| A10W610 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$. | 24546 | OMA 07 |
| A10W611 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W612 | 131-0566-00 | B100100 | B119999 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W612 | 131-0566-00 | B700100 | B714999 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W802 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$. | 24546 | OMA 07 |
| A10W805 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W806 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W807 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W808 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W810 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W811 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W815 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W820 | 131-0566-00 | B110100 | B119999 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225 L (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W820 | 131-0566-00 | 8710100 | B714999 | BUS,CONDUCTOR:DUMMY RES,0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W821 | 131-0566-00 | B110100 | B119999 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W821 | 131-0566-00 | B710100 | B714999 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W1000 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1010 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W1010 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W1101 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1102 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1103 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |


| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mir. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10W1104 | 131-0566-00 | B710630 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225 L ( 22461 Y ) | 24546 | OMA 07 |
| A10W1104 | 131-0566-00 | B111186 |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD X 0.225L (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W1105 | 131-0566-00 | B710630 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W1105 | 131-0566-00 | B111186 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L ( 2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W1106 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W1107 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W1107 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W1108 | 131-0566-00 | B110100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ ( 2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W1108 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W 1120 | 131-0566-00 | B100100 | B110099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A.2Y) | 24546 | OMA 07 |
| A10W1120 | 131-0566-00 | B700100 | B710099 | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W1200 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W1201 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W1202 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W1203 | 131-0566-00 | B110100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 MOD A,2Y) | 24546 | OMA 07 |
| A10W1203 | 131-0566-00 | B710100 |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ (2246 1Y) | 24546 | OMA 07 |
| A10W1204 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1205 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1209 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W1210 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1216 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1217 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1218 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1221 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1222 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W1223 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1231 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1237 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1247 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1248 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1249 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1250 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1251 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W1252 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W1255 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES, 0.094 OD X 0.225L | 24546 | OMA 07 |

Replaceable Electrical Parts
22461 Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mir. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10W1277 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD X 0.225L | 24546 | OMA 07 |
| A10W1288 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10W2302 | 174-1041-00 |  |  | CA ASSY,SP,ELEC:18 COND,8.5 L,RIBBON | 00779 | 487755-2 |
| A10W2304 | 174-1039-00 |  |  | CA ASSY,SP,ELEC:12 COND,11.3 L,RIBBON | 00779 | 487755-3 |
| A10W2502 | 175-9903-00 |  |  | CA ASSY,SP,ELEC:25,27 AWG,6.0 L | 00779 | ORDER BY DESC |
| A10W2701 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD $\times 0.225 \mathrm{~L}$ | 24546 | OMA 07 |
| A10X421 | 136-1005-00 | B100100 | B112868 | SKT,PL-IN ELEK:PLCC,PCB, 28 POS,0.05 CTR, 0.360 H $\times 0.125$ TAIL,TIN, $0.55-0.75$ IC SHOULDER; TUBE/18EA, (2246 MOD A,2Y) | 00779 | 3-821581-1 |
| A10X421 | 136-1065-00 | B112869 |  | SKT,PL-IN ELEK:MICROCKT,28 PIN,LOW PROFILE (2246 MOD A,2Y) | 00779 | 641746-2 |
| A10×421 | 136-1005-00 | B700100 | B710999 | SKT,PL-IN ELEK:PLCC,PCB, 28 POS, 0.05 CTR, $0.360 \mathrm{H} \times 0.125$ TAIL,TIN,0.55-0.75 IC SHOULDER; TUBE/18 EA, (2246 1Y) | 00779 | 3-821581-1 |
| A10×421 | 136-1065-00 | B711000 |  | SKT,PL-IN ELEK:MICROCKT,28 PIN,LOW PROFILE $(22461 Y)$ | 00779 | 641746-2 |
| A10×431 | 136-1005-00 | B100100 | B112868 | SKT,PL-IN ELEK:PLCC,PCB, 28 POS, 0.05 CTR, $0.360 \mathrm{H} \times 0.125 \mathrm{TALL}, \mathrm{TiN}, 0.55-0.75$ IC SHOULDER; TUBE/18 EA, (2246 MOD A,2Y) | 00779 | 3-821581-1 |
| A10×431 | 136-1065-00 | B112869 |  | SKT,PL-IN ELEK:MICROCKT,28 PIN,LOW PROFILE (2246 MOD A) | 00779 | 641746-2 |
| A10X431 | 136-1005-00 | B700100 | B710999 | SKT,PL-IN ELEK:PLCC,PCB, 28 POS, 0.05 CTR, 0.360 H $\times 0.125$ TAIL, TIN, $0.55-0.75$ IC SHOULDER; TUBE/18 EA, (2246 1Y) | 00779 | 3-821581-1 |
| A10×431 | 136-1065-00 | B711000 |  | SKT,PL-IN ELEK:MICROCKT,28 PIN,LOW PROFILE (2246 1Y) | 00779 | 641746-2 |
| A10Y600 | 119-2051-00 |  |  | RESONATOR,CER:10MHZ | 51406 | CSA 10:00 MX11 |

Replaceable Electrical Parts 2246 1Y and 2246 MOD A Service

| Component <br> Number | Tektronix <br> Part No. | Serial <br> Effective |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| A12 |  |  |
| A12J2105 |  |  |

Replaceable Electrical Parts $22461 Y$ and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A14 | 670-9399-01 |  | CIRCUIT BD ASSY:SWITCH | 80009 | 670939901 |
| A14CR2001 | 152-0141-02 |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A14CR2002 | 152-0141-02 |  | DIODE,SIG;,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A14CR2003 | 152-0141-02 |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A14CR2004 | 152-0141-02 |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A14CR2005 | 152-0141-02 |  | DIODE,SIG:ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A14CR2006 | 152-0141-02 |  | DIODE,SIG:ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A14DS2001 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2002 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2003 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2004 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2005 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2006 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2007 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2008 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2009 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2010 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2011 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2012 | 150-1161-00 |  | LT EMITTING DIO:YELLOW | 50434 | QLMP-1487 |
| A14DS2013 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2014 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2015 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2020 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2021 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2022 | 150-1161-00 |  | LT EMITTING DIO:YELLOW | 50434 | QLMP-1487 |
| A14DS2023 | 150-1161-00 |  | LT EMITTING DIO:YELLOW | 50434 | QLMP-1487 |
| A14DS2025 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2026 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2027 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2028 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2029 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2030 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2031 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2032 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2033 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2034 | 150-1161-00 |  | LT EmItting dio:yellow | 50434 | QLMP-1487 |
| A14DS2035 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2036 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2037 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2038 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2039 | 150-1161-00 |  | LT Emitting dio:yellow | 50434 | QLMP-1487 |
| A14DS2041 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2042 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2043 | 150-1161-00 |  | LT EMITTING DIO:YELLOW | 50434 | QLMP-1487 |
| A14DS2044 | 150-1161-00 |  | LT EMITTING DIO:YELLOW | 50434 | QLMP-1487 |


| Component Number | Tektronix Part No. | Serial No. Effectlve Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A14DS2045 | 150-1161-00 |  | LT EMITTING DIO:YELLOW | 50434 | OLMP-1487 |
| A14DS2046 | 150-1161-00 |  | LT EMITTING DIO:YELLOW | 50434 | QLMP-1487 |
| A14DS2047 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14DS2048 | 150-1160-00 |  | LT EMITTING DIO:GREEN | 50434 | QLMP 1587 |
| A14R2001 | 307-0675-00 |  | RES NTWK,FXD,FI:(9),1K OHM,2\%,1.25W | 91637 | CSC10A01-102G |
| A14R2002 | 307-0675-00 |  | RES NTWK,FXD,FI:(9),1K OHM, $2 \%, 1.25 \mathrm{~W}$ | 91637 | CSC10A01-102G |
| A14U2001 | 156-0789-00 |  | IC,DIGITAL:LSTTL,SHIFT REGISTER;8-BIT PISO | 01295 | SN74LS165N |
| A14U2002 | 156-0789-00 |  | IC,DIGITAL:LSTTL,SHIFT REGISTER;8-BIT PISO | 01295 | SN74LS165N |
| A14W2501 | 175-9902-00 |  | CA ASSY,SP,ELEC:20,27 AWG,8.05 L | 0.J7N9 | PER TEK DRAWING |

Replaceable Electrical Parts 2246 1Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A16 | 672-1306-00 | B700100 | B711092 | CIRCUIT BD ASSY:PROCESSOR (2246 1Y) <br> (WITH U2519 FIRMWARE) | 80009 | 672130600 |
| A16 | 672-1306-01 | B711093 |  | CIRCUIT BD ASSY:PROCESSOR,671-1086-XX WIRED $(22461 Y)$ <br> (WITH U2519 FIRMWARE) | 80009 | 672130601 |
| A16 | 672-0254-00 | 8100100 | B113797 | CIRCUIT BD ASSY: PROCESSOR (2246 MOD A,2Y) <br> (WITH U2519 FIRMWARE) | 80009 | 672025400 |
| A16 | 672-0254-01 | B113798 |  | CIRCUIT BD ASSY:PROCESSOR,671-1239-XX WIRED (2246 MOD A,2Y) <br> (WITH U2519 FIRMWARE) | 80009 | 672025401 |
| A16U2519 | 160-5063-07 | 8100100 | B101548 | ```MICROCKT,DGTL:NMOS,131072 X 8 EPROM,PRGM 27010,DIP32.6 (2246 MOD A,2Y)``` | TK2172 | 160-5063-07 |
| A16U2519 | 160-5063-08 | B101549 | B101769 | MICROCKT,DGTL:NMOS,131072 X 8 EPROM,PRGM 27010,DIP32.6 <br> (2246 MOD A,2Y) | TK2172 | 160-5063-08 |
| A16U2519 | 160-5063-09 | B101770 | B110314 | MICROCKT,DGTL:NMOS,131072 X 8 EPROM,PRGM (2246 MOD A,2Y) | TK0161 | 160-5063-09 |
| A16U2519 | 160-5063-10 | B110315 | B113200 | ```MICROCKT,DGTL:NMOS,131072 X }8\mathrm{ EPROM,PRGM 21010,DIP32.6 (2246 MOD A,2Y)``` | TK0161 | 160-5063-10 |
| A16U2519 | 160-5063-11 | B113201 |  | MICROCKT,DGTL:NMOS, $131072 \times 8$ EPROM,PEGM 21010,DIP32.6 <br> (2246 MOD A,2Y) | TK0161 | 93369300 |
| A16U2519 | 160-5063-08 | B700100 | B700632 | MICROCKT,DGTL:NMOS, $131072 \times 8$ EPROM,PRGM 27010,DIP32.6 <br> (2246 1Y) | TK2172 | 160-5063-08 |
| A16U2519 | 160-5063-09 | B700633 | B710184 | MICROCKT,DGTL:NMOS,131072 X 8 EPROM,PRGM (2246 TY) | TK0161 | 160-5063-09 |
| A16U2519 | 160-5063-10 | B710185 | B711085 | MICROCKT,DGTL:NMOS,131072 X 8 EPROM,PRGM 21010,DIP32.6 <br> (2246 IY) | TK0161 | 160-5063-10 |
| A16U2519 | 160-5063-11 | B711086 |  | MICROCKT,DGTL:NMOS, $131072 \times 8$ EPROM,PEGM 21010,DIP32.6 <br> (2246 1Y) | TK0161 | 93369300 |
| A16A1 | 671-1086-00 | B700100 | B711092 | CIRCUIT BD ASSY:PROCESSOR (2246 1Y) <br> (WITHOUT U2519 FIRMWARE) | 80009 | 671108600 |
| A16A1 | 671-1086-01 | B711093 |  | CIRCUIT BD ASSY:A16 PROCESSOR (2246 1Y) <br> (WITHOUT U2519 FIRMWARE) | 80009 | 671108601 |
| A16A1 | 671-0947-00 | B100100 | B101981 | CIRCUIT BD ASSY:PROCESSOR (2246 MOD A,2Y) <br> (WITHOUT U2519 FIRMWARE) | 80009 | 671094700 |
| A16A1 | 671-1239-00 | B101982 | B113797 | CIRCUIT BD ASSY:PROCESSOR (2246 MOD A,2Y) <br> (WITHOUT U2519 FIRMWARE) | 80009 | 671123900 |
| A16A1 | 671-1239-01 | B113798 |  | CIRCUIT BD ASSY:A16 PROCESSOR (2246 MOD A,2Y) <br> (WITHOUT U2519 FIRMWARE) | 80009 | 671123901 |
| A16A1BT2501 | 146-0055-00 |  |  | BATTERY,DRY:3.OV,1200 MAH,LITHIUM, (2246 MOD A.2Y) | 61058 | BR-2/3A-E2P |
| A16A1BT2501 | 146-0067-00 | B700100 | 8715000 | BATTERY,DRY:SILVER OXIDE,MODEL \#391 (2246 1Y) | TK2190 | V391 |
| A16A1BT2501 | 146-0086-00 | B715001 |  | BATTERY,DRY:3.OV,LITHIUM (2246 1Y) | ODWW6 | BR2/3AA |


| Component Number | Tektronix Part No. | $\begin{gathered}\text { Serlal } \\ \text { Effective }\end{gathered}$ | No. Dscont | Name a Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A16A1BT2502 | 146-0067-00 | B700100 | B715000 | $\begin{aligned} & \text { BATTERY,DRY:SILVER OXIDE,MODEL \#391 } \\ & (2246 \mathrm{y}) \end{aligned}$ | TK2190 | V391 |
| A16A1C2300 | 281-0759-00 |  |  | CAP,FXD,CER DI:22PF, $10 \%$,100V | 04222 | SA101A220KAA |
| A16A1C2301 | 285-1300-01 |  |  | CAP,FXD,MTLZD:0.1UF,10\%,63V | TK1913 | MKS2 0.1/63/10 |
| A16A1C2302 | 285-1300-01 |  |  | CAP,FXD,MTLZD:0.1UF,10\%,63V | TK1913 | MKS2 0.1/63/10 |
| A16A1C2303 | 285-1300-01 |  |  | CAP,FXD,MTLZD:0.1UF,10\%,63V | TK1913 | MKS2 0.1/63/10 |
| A16A1C2304 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2305 | 285-1300-01 |  |  | CAP,FXD,MTLZD:0.1UF,10\%,63V | TK1913 | MKS2 0.1/63/10 |
| A16A1C2306 | 285-1300-01 |  |  | CAP,FXD,MTLZD:0.1UF,10\%,63V | TK1913 | MKS2 0.1/63/10 |
| A16A1C2307 | 285-1301-01 |  |  | CAP,FXD,MTLZD:0.47UF,10\%,50V | TK1913 | MKS $2.47 / 50$ OR |
| A16A1C2308 | 285-1348-00 |  |  | CAP,FXD,MTLZD:0.22UF,10\%,63V | TK1913 | MKS2 0.22 MFD 1 |
| A16A1C2309 | 285-1301-01 |  |  | CAP,FXD,MTLZD:0.47UF,10\%,50V | TK1913 | MKS $2.47 / 50$ OR |
| A16A1C2310 | 285-1348-00 |  |  | CAP,FXD,MTLZD:0.22UF,10\%,63V | TK1913 | MKS2 0.22 MFD 1 |
| A16A1C2311 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2312 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2313 | 281-0903-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2314 | 281-0775-01 |  |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A16A1C2315 | 281-0775-01 |  |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A16A1C2316 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2317 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2318 | 281-0809-00 |  |  | CAP,FXD,CER DI:200 PF,5\%,100V | 04222 | SA101A201JAA |
| A16A1C2319 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2320 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 1Y) | TK1743 | CGB510GEN |
| A16A1C2320 | 281-0763-00 | B710100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2320 | 281-0798-00 | B100100 | B110099 | CAPPXD,CER DI:51PF,1\%,100V ( 2246 MOD A, 2 Y) | TK1743 | CGB510GEN |
| A16A1C2320 | 281-0763-00 | B110100 |  | CAP,FXD,CER DI:47PF,10\%,100V (2246 MOD A,2Y) | 04222 | SA102A470KAA |
| A16A1C2321 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2322 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF,1\%,100V (2246 1Y) | TK1743 | CGb510gen |
| A16A1C2322 | 281-0763-00 | B710100 |  | CAP,FXD,CER DI:47PF,10\%,100V (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2322 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF,1\%,100V (2246 MOD A, 2 Y ) | TK1743 | CGB510GEN |
| A16A1C2322 | 281-0763-00 | B110100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA102A470KAA |
| A16A1C2323 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF,1\%,100V (2246 1Y) | TK1743 | CGB5t0GEN |
| A16A1C2323 | 281-0763-00 | B710100 |  | CAP,FXD,CER DI:47PF,10\%,100V (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2323 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 MOD A, 2 Y ) | TK1743 | CGB510GEN |
| A16A1C2323 | 281-0763-00 | B110100 |  | CAP,FXD,CER DI:47PF,10\%,100V (2246 MOD A,2Y) | 04222 | SA102A470KAA |
| A16A1C2324 | 285-1300-01 |  |  | CAP,FXD,MTLZD:0.1UF,10\%,63V | TK1913 | MKS2 0.1/63/10 |

Replaceable Electrical Parts
2246 1Y and 2246 MOD A Service

| Component Number | Tektronlx Part No. | Serlal Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A16A1C2401 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2402 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2403 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2404 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2405 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2406 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2407 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2408 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2409 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2410 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2411 | 281-0809-00 |  |  | CAP,FXD,CER OI:200 PF,5\%,100V | 04222 | SA101A201JAA |
| A16A1C2412 | 281-0809-00 |  |  | CAP,FXD,CER DI:200 PF, $5 \%, 100 \mathrm{~V}$ | 04222 | SA101A201JAA |
| A16A1C2415 | 281-0775-01 |  |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A16A1C2416 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF,1\%,100V (2246 1Y) | TK1743 | CGB510GEN |
| A16A1C2416 | 281-0763-00 | B710100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2416 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | TK1743 | CGB510GEN |
| A16A1C2416 | 281-0763-00 | 8110100 |  | CAP,FXD,CER DI:47PF,10\%,100V (2246 MOD A,2Y) | 04222 | SA102A470KAA |
| A16A1C2417 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 1Y) | TK1743 | CGB510GEN |
| A16A1C2417 | 281-0763-00 | 8710100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2417 | 281-0798-00 | 8100100 | B110099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | TK1743 | CGB510GEN |
| A16A1C2417 | 281-0763-00 | B110100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA102A470KAA |
| A16A1C2418 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF,1\%,100V (2246 1Y) | TK1743 | CGB510GEN |
| A16A1C2418 | 281-0763-00 | B710100 |  | CAP,FXD,CER DI:47PF,10\%,100V (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2418 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | TK1743 | CGB510GEN |
| A16A1C2418 | 281-0763-00 | B110100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA102A470KAA |
| A16A1C2419 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 1Y) | TK1743 | CGB510GEN |
| A16A1C2419 | 281-0763-00 | B710100 |  | CAP,FXD,CER DI:47PF,10\%,100V (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2419 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | TK1743 | CGB510GEN |
| A16A1C2419 | 281-0763-00 | B110100 |  | CAP,FXD,CER DI:47PF,10\%,100V (2246 MOD A,2Y) | 04222 | SA102A470KAA |
| A16A1C2420 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF,1\%,100V (2246 1Y) | TK1743 | CGB510GEN |

Replaceable Electrical Parts 2246 1Y and 2246 MOD A Service

| Component Number | Tektronix <br> Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A16A1C2420 | 281-0763-00 | B710100 |  | CAP.FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2420 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ ( 2246 MOD A,2Y) | TK1743 | CGb510GEN |
| A16A1C2420 | 281-0763-00 | B110100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ ( 2246 MOD A, 2 Y ) | 04222 | SA102A470KAA |
| A16A1C2501 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2502 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2503 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2504 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2505 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2506 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2507 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2508 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2509 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2510 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2511 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2514 | 281-0759-00 |  |  | CAP,FXD,CER DI:22PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101A220KAA |
| A16A1C2515 | 281-0759-00 |  |  | CAP,FXD,CER DI: $22 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 04222 | SA101A220KAA |
| A16A1C2516 | 285-1301-01 |  |  | CAP,FXD,MTLZD:0.47UF,10\%,50V | TK1913 | MKS 2 .47/50 OR |
| A16A1C2517 | 281-0775-01 |  |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A16A1C2518 | 285-1301-01 |  |  | CAP,FXD,MTLZD:0.47UF, $10 \%$,50V | TK1913 | MKS $2.47 / 50$ OR |
| A16A1C2521 | 281-0772-00 |  |  | CAP,FXD,CER DI: $4700 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2522 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2523 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2524 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF. $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2525 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2526 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2530 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2531 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2532 | 281-0909-00 |  |  | CAP,FXD,CER DI:0.022UF,20\%,50V | 04222 | SA105C223MAA |
| A16A1C2541 | 290-0748-00 |  |  | CAP,FXD,ELCTLT:10UF, +50-20\%,25WVDC | 1W344 | SME25T10RM5X12F |
| A16A1C2543 | 281-0772-00 |  |  | CAP,FXD,CER DI: $4700 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2544 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2545 | 281-0772-00 |  |  | CAP,FXD, CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2546 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2547 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2548 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2549 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2550 | 281-0772-00 |  |  | CAP,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$ | 04222 | SA101C472KAA |
| A16A1C2551 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 1Y) | TK1743 | CGB510GEN |
| A16A1C2551 | 281-0763-00 | B710100 |  | CAP,FXD,CER DI:47PF,10\%,100V (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2551 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | TK1743 | CGB510GEN |

## Replaceable Electrical Parts

2246 1Y and 2246 MOD A Service

| Component Number | Tektronix <br> Part No. | Serial | No. Dscont | Name \& Deseription | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A16A1C2551 | 281-0763-00 | B110100 |  | CAP,FXD,CER DI:A7PF, $10 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA102A470KAA |
| A16A1C2552 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF,1\%,100V (2246 1Y) | TK1743 | CGB510GEN |
| A16A1C2552 | 281-0763-00 | B710100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2552 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | TK1743 | CGB510GEN |
| A16A1C2552 | 281-0763-00 | B110100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | 04222 | SA102A470KAA |
| A16A1C2553 | 281-0798-00 | 8700100 | B710099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 1Y) | TK1743 | CGB5t0GEN |
| A16A1C2553 | 281-0763-00 | B710100 |  | CAPFXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2553 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF,1\%,100V (2246 MOD A.2Y) | TK1743 | CGB510GEN |
| A16A1C2553 | 281-0763-00 | B110100 |  | CAP,FXD.CER DI:47PF, $10 \%, 100 \mathrm{~V}$ ( 2246 MOD A.2Y) | 04222 | SA102A470KAA |
| A16A1C2554 | 281-0798-00 | B700100 | B710099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 1Y) | TK1743 | CGB510GEN |
| A16A1C2554 | 281-0763-00 | B710100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2554 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ (2246 MOD A,2Y) | TK1743 | CGB510GEN |
| A16A1C2554 | 281-0763-00 | B110100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 MOD A.2Y) | 04222 | SA102A470KAA |
| A16A1C2555 | 281-0798-00 | B700100 | B710099 | CAPPKD,CER DI:51PF,1\%,100V (2246 1Y) | TK1743 | CGB510GEN |
| A16A1C2555 | 281-0763-00 | B710100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ (2246 1Y) | 04222 | SA102A470KAA |
| A16A1C2555 | 281-0798-00 | B100100 | B110099 | CAP,FXD,CER DI:51PF, $1 \%, 100 \mathrm{~V}$ ( 2246 MOD A, 2 Y) | TK1743 | CGB510GEN |
| A16A1C2555 | 281-0763-00 | 8110100 |  | CAP,FXD,CER DI:47PF, $10 \%, 100 \mathrm{~V}$ ( 2246 MOD A,2Y) | 04222 | SA102A470KAA |
| A16A1CR2501 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A16A1CR2502 | 152-0951-00 |  |  | DIODE,SIG:SCHTKY;60V,2.25PF | 50088 | 1N6263 |
| A16A1CR2504 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A16A1CR2505 | 152-0951-00 |  |  | DIODE,SIG:SCHTKY:;6V,2.25PF | 50088 | 1N6263 |
| A16A1DS2501 | 150-1161-00 |  |  | LT EMITTING DIO:YELLOW | 50434 | QLMP-1487 |
| A16A1J2302 | 136-0949-00 |  |  | SOCKET,SIP::PCB,;STR, $1 \times 18,0.1$ CTR, 0.210 H | 00779 | 2-643650-3 |
| A16A1J2304 | 136-0948-00 |  |  | SOCKET,SIP::PCB, STR, $^{\text {P }}$ X 12,0.1 CTR, 0.210 H | 00779 | 2-643644-3 |
| A16A1J2501 | 131-3624-00 |  |  | CONN,BOX::PCB,SIP:FEMALE,STR, $1 \times 20,0.1$ CTR, $0.210 \mathrm{H} \times 0.12$ TAIL,TIN, BECU,BD RETENTION | 00779 | 2-643652-3 |
| A16A1J2502 | 131-3623-00 |  |  | CONN,BOX::PCB,SIP;FEMALE,STR, $1 \times 25,0.1$ CTR, $0.21 \mathrm{H} \times 0.120$ TAIL,TIN, BECU,BD RETENTION | 00779 | 2-643657-3 |
| A16A1J2503 | 131-4529-00 |  |  | CONN,HDR::PCB,;MALE,STR, $2 \times 5,0.1$ CTR, 0.365 | 53387 | 3654-6002 |
| A16A1J2601 | 131-3623-00 |  |  | CONN,BOX::PCB,SIP;FEMALE,STR, $1 \times 25,0.1$ CTR, 0.21 H ' 0.120 TAIL, TIN, BECU, BD RETENTION | 00779 | 2-643657-3 |
| A16A1Q2501 | 151-0716-01 |  |  | TRANSISTOR:NPN,SI,PWR DARLINGTON,1W,TO-92 PLUS | 04713 | MPS6725RLRP |
| A16A1Q2502 | 151-0716-01 |  |  | TRANSISTOR:NPN,SI,PWR DARLINGTON,1W,TO-92 PLUS | 04713 | MPS6725RLRP |


| Component Number | Tektronix Part No. | Serial No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A16A1Q2503 | 151-0716-01 |  | TRANSISTOR:NPN,S1,PWR DARLINGTON,1W,TO-92 PLUS | 04713 | MPS6725RLRP |
| A16A1Q2504 | 151-0716-01 |  | TRANSISTOR:NPN,SI,PWR DARLINGTON,1W,TO-92 PLUS | 04713 | MPS6725RLRP |
| A16A1Q2505 | 151-0716-01 |  | TRANSISTOR:NPN,SI,PWR DARLINGTON,1W,TO-92 PLUS | 04713 | MPS6725RLRP |
| A16A1Q2506 | 151-0716-01 |  | TRANSISTOR:NPN,SI,PWR DARLINGTON,1W,TO-92 PLUS | 04713 | MPS6725RLRP |
| A16A1Q2507 | 151-0190-00 |  | TRANSISTOR,SIG:BIPOLAR,NPN;40V,200MA, 300MHZ,AMPLIFIER | 04713 | 2N3904 |
| A16A1R2301 | 322-3260-00 |  | RES,FXD,FILM:4.99K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G49900F |
| A16A1R2302 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2303 | 322-3231-00 |  | RES,FXD,FILM:2.49K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF50-1G24900F |
| A16A1R2304 | 313-1203-00 |  | RES,FXD,FILM:20K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-20001J |
| A16A1R2305 | 313-1203-00 |  | RES,FXD,FILM:20K OHM,5\%,0.2W | 91637 | CCF50-2-20001J |
| A16A1R2306 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2307 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2308 | 313-1621-00 |  | RES,FXD,FILM:620 OHM,5\%,0.2W | 91637 | CCF50-2-620ROJ |
| A16A1R2309 | 313-1101-00 |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A16A1R2310 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2311 | 322-3231-00 |  | RES,FXD,FILM:2.49K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMi | 91637 | CCF50-1G24900F |
| A16A1R2312 | 322-3252-00 |  | RES,FXD,FILM:4.12K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ T0 | 91637 | CCF501G41200F |
| A16A1R2313 | 322-3252-00 |  | RES,FXD,FILM:4.12K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF501G41200F |
| A16A1R2314 | 322-3231-00 |  | RES,FXD,FILM:2.49K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF50-1G24900F |
| A16A1R2315 | 322-3238-00 |  | RES,FXD,FILM:2.94K OHM,1\%,0.2W,TC = T0 | 91637 | CCF501G29400F |
| A16A1R2316 | 322-3231-00 |  | RES,FXD,FILM:2.49K OHM,1\%,0.2W,TC = TOMI | 91637 | CCF50-1G24900F |
| A16A1R2317 | 322-3231-00 |  | RES,FXD,FILM:2.49K OHM,1\%,0.2W,TC = TOMI | 91637 | CCF50-1G24900F |
| A16A1R2318 | 322-3238-00 |  | RES,FXD,FILM:2.94K OHM,1\%,0.2W,TC = T0 | 91637 | CCF501G29400F |
| A16A1R2319 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000J |
| A16A1R2320 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000J |
| A16A1R2321 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2322 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2324 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000J |
| A16A1R2325 | 307-0499-00 |  | $\begin{aligned} & \text { RES,FXD,FILM:9,100K OHM, } 5 \%, 0.125 \mathrm{WTC}=200 \\ & \text { PPM/DEG C } \end{aligned}$ | 11236 | 750-101-R100K O |
| A16A1R2328 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2329 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2330 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000J |
| A16A1R2331 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2332 | 313-1220-00 |  | RES,FXD, FILM:22 OHM,5\%,0.2W | 91637 | CCF50-2-22R00J |
| A16A1R2333 | 313-1220-00 |  | RES,FXD, FILM:22 OHM,5\%,0.2W | 91637 | CCF50-2-22R00J |
| A16A1R2334 | 313-1220-00 |  | RES,FXD,FILM:22 OHM,5\%,0.2W | 91637 | CCF50-2-22R00J |
| A16A1R2335 | 313-1220-00 |  | RES,FXD,FILM:22 OHM,5\%,0.2W | 91637 | CCF50-2-22R00J |
| A16A1R2336 | 313-1220-00 |  | RES,FXD,FILM:22 OHM,5\%,0.2W | 91637 | CCF50-2-22R00J |
| A16A1R2337 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2338 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM,5\%,0.2W | 91637 | CCF50-2-47000J |


| Component Number | Tektronix Part No． | Serlal No． Effective Dscont | Name \＆Description | Mfr． Code | Mfr．Part No． |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A16A1R2339 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM，5\％，0．2W | 91637 | CCF50－2－47000J |
| A16A1R2340 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000J |
| A16A1R2341 | 313－1472－00 |  | RES，FXD．FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000 J |
| A16A1R2342 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000J |
| A16A1R2343 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000 J |
| A16A1R2344 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000． |
| A16A1R2345 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000J |
| A16A1R2346 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000 J |
| A16A1R2347 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM，5\％，0．2W | 91637 | CCF50－2－47000」 |
| A16A1R2348 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000 J |
| A16A1R2349 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000」 |
| A16A1R2350 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000．J |
| A16A1R2351 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000」 |
| A16A1R2352 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000J |
| A16A1R2355 | 313－1102－00 |  | RES，FXD，FILM：1K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－10000 J |
| A16A1R2356 | 313－1102－00 |  | RES，FXD，FILM： 1 K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－10000J |
| A16A1R2357 | 313－1102－00 |  | RES，FXD，FILM：1K OHM，5\％，0．2W | 91637 | CCF50－2－10000」 |
| A16A1R2361 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000 J |
| A16A1R2362 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM，5\％，0．2W | 91637 | CCF50－2－47000J |
| A16A1R2363 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000J |
| A16A1R2364 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000． |
| A16A1R2365 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM，5\％，0．2W | 91637 | CCF50－2－47000J |
| A16A1R2400 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000J |
| A16A1R2401 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM，5\％，0．2W | 91637 | CCF50－2－47000J |
| A16A1R2402 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM，5\％，0．2W | 91637 | CCF50－2－47000J |
| A16A1R2404 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM，5\％，0．2W | 91637 | CCF50－2－47000J |
| A16A1R2405 | 313－1621－00 |  | RES，FXD，FILM：620 OHM，5\％，0．2W | 91637 | CCF50－2－620ROJ |
| A16A1R2406 | 322－3220－00 |  | RES，FXD，FILM：1．91K OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G19100F |
| A16A1R2407 | 322－3176－00 |  | RES，FXD，FILM： 665 OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF50－2 |
| A16A1R2408 | 322－3172－00 |  | RES，FXD，FILM： 604 OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G604ROF |
| A16A1R2409 | 322－3220－00 |  | RES，FXD，FILM：1．91K OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G19100F |
| A16A1R2410 | 322－3172－00 |  | RES，FXD，FILM： 604 OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G604ROF |
| A16A1R2411 | 322－3220－00 |  | RES，FXD，FILM：1．91K OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G19100F |
| A16A1R2412 | 313－1102－00 |  | RES，FXD，FILM：1K OHM，5\％，0．2W | 91637 | CCF50－2－10000J |
| A16A1R2413 | 322－3202－00 |  | RES，FXD，FILM：1．24K OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOM | 91637 | CCF50－1G12400F |
| A16A1R2414 | 313－1102－00 |  | RES，FXD，FILM：1K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－10000．J |
| A16A1R2415 | 322－3202－00 |  | RES，FXD，FILM：1．24K OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF50－1G12400F |
| A16A1R2416 | 313－1102－00 |  | RES，FXD，FILM： 1 K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－10000J |
| A16A1R2417 | 322－3202－00 |  | RES，FXD，FILM：1．24K OHM， $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF50－1G12400F |
| A16A1R2418 | 322－3077－00 |  | RES，FXD，FILM：61．9 OHM，1\％，0．2W，TC＝TOMI | 91637 | CCF501G61R9OF |
| A16A1R2419 | 313－1101－00 |  | RES，FXD，FILM： 100 OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－100ROJ |
| A16A1R2420 | 313－1101－00 |  | RES，FXD，FILM：100 OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－100R0J |
| A16A1R2421 | 313－1621－00 |  | RES，FXD，FILM：620 OHM，5\％，0．2W | 91637 | CCF50－2－620ROJ |
| A16A1R2501 | 313－1621－00 |  | RES，FXD，FILM：620 OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－620ROJ |
| A16A1R2502 | 313－1472－00 |  | RES，FXD，FILM：4．7K OHM， $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50－2－47000J |

Replaceable Electrical Parts 2246 1Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serlal No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A16A1R2503 | 313-1621-00 |  | RES,FXD, FILM: 620 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-620ROJ |
| A16A1R2504 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000J |
| A16A1R2505 | 313-1621-00 |  | RES,FXD,FILM: $620 \mathrm{OHM}, 5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-620ROJ |
| A16A1R2506 | 313-1621-00 |  | RES,FXD,FILM: 620 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-620ROJ |
| A16A1R2507 | 313-1101-00 |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100R0J |
| A16A1R2508 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A16A1R2509 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A16A1R2510 | 313-1511-00 |  | RES, FXD, FILM: 510 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-510ROJ |
| A16A1R2511 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A16A1R2512 | 313-1472-00 |  | RES,FXD,FILM:4.7K OHM, 5\%,0.2W | 91637 | CCF50-2-47000J |
| A16A1R2513 | 313-1472-00 |  | RES, FXD,FILM:4.7K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-47000 J |
| A16A1R2514 | 313-1621-00 |  | RES,FXD,FILM:620 OHM,5\%,0.2W | 91637 | CCF50-2-620ROJ |
| A16A1R2515 | 313-1621-00 |  | RES,FXD,FILM:620 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-620ROJ |
| A16A1R2516 | 313-1621-00 |  | RES,FXD,FILM:620 OHM,5\%,0.2W | 91637 | CCF50-2-620ROJ |
| A16A1R2517 | 313-1621-00 |  | RES,FXD,FILM:620 OHM,5\%,0.2W | 91637 | CCF50-2-620ROJ |
| A16A1R2518 | 313-1102-00 |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A16A1R2519 | 313-1621-00 |  | RES,FXD,FILM:620 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-620ROJ |
| A16A1R2520 | 307-0499-00 |  | RES,FXD,FILM:9,100K OHM, $5 \%, 0.125 W T C=200$ PPM/DEG C | 11236 | 750-101-R100K 0 |
| A16A1R2521 | 307-0499-00 |  | RES,FXD,FILM:9,100K OHM,5\%,0.125WTC $=200$ PPM/DEG C | 11236 | 750-101-R100K 0 |
| A16A1R2522 | 307-0499-00 |  | RES,FXD,FILM:9,100K OHM,5\%,0.125WTC $=200$ PPM/DEG C | 11236 | 750-101-R100K 0 |
| A16A1R2523 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A16A1R2524 | 313-1102-00 |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A16A1R2525 | 313-1621-00 |  | RES, FXD,FILM: 620 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-620ROJ |
| A16A1R2526 | 313-1621-00 |  | RES,FXD,FILM:620 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-620ROJ |
| A16A1R2527 | 313-1621-00 |  | RES,FXD,FILM: 620 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-620ROJ |
| A16A1R2528 | 313-1621-00 |  | RES,FXD,FILM: 620 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-620ROJ |
| A16A1R2529 | 313-1621-00 |  | RES,FXD,FILM:620 OHM,5\%,0.2W | 91637 | CCF50-2-620ROJ |
| A16A1R2532 | 313-1102-00 |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000, |
| A16A1R2534 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A16A1R2536 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A16A1R2538 | 313-1102-00 |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A16A1R2540 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000」 |
| A16A1R2542 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A16A1R2545 | 313-1220-00 | B113798 | RES,FXD,FILM:22 ОНM,5\%,0.2W ( 2246 MOD A,2Y) | 91637 | CCF50-2-22R00J |
| A16A1R2545 | 313-1220-00 | B711093 | RES,FXD,FILM:22 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-22R00J |
| A16A1R2546 | 313-1220-00 |  | RES,FXD,FILM:22 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-22R00 |
| A16A1R2547 | 313-1220-00 |  | RES,FXD,FILM:22 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-22R00J |
| A16A1R2548 | 313-1220-00 |  | RES,FXD,FILM:22 OHM,5\%,0.2W | 91637 | CCF50-2-22R00J |
| A16A1R2549 | 313-1220-00 |  | RES,FXD,FILM:22 OHM,5\%,0.2W | 91637 | CCF50-2-22R00J |
| A16A1R2550 | 313-1220-00 |  | RES,FXD,FILM:22 OHM,5\%,0.2W | 91637 | CCF50-2-22ROOJ |

Replaceable Electrical Parts
2246 1Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A16A1R2551 | 313-1220-00 |  | RES,FXD,FILM:22 OHM,5\%,0.2W | 91637 | CCF50-2-22R00J |
| A16A1R2552 | 313-1220-00 |  | RES,FXD,FILM:22 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-22ROOJ |
| A16A1R2553 | 313-1220-00 |  | RES,FXD,FILM:22 OHM,5\%,0.2W | 91637 | CCF50-2-2.2R00J |
| A16A1R2554 | 313-1102-00 |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A16A1R2555 | 313-1101-00 |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A16A1R2560 | 313-1101-00 |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A16A1R2561 | 313-1101-00 |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A16A1R2562 | 313-1101-00 |  | RES,FXD,FILM: 100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A16A1R2563 | 313-1101-00 |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A16A1R2564 | 313-1101-00 |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A16A1R2565 | 313-1621-00 | B113798 | RES,FXD,FILM:620 OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-620ROJ |
| A16A1R2565 | $313-1621-00$ | B711093 | RES,FXD,FILM:620 OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-620ROJ |
| A16A1U2300 | 156-1646-00 |  | IC,DIGITAL:HCTCMOS,FLIP FLOP;OCTAL D-TYPE,3-STATE | 18324 | 74HCT374N |
| A16A1U2301 | 156-1646-00 |  | IC,DIGITAL:HCTCMOS,FLIP FLOP;OCTAL D-TYPE,3-STATE | 18324 | 74HCT374N |
| A16A1U2302 | 156-1589-00 |  | IC,CONVERTER:BIPOLAR,D/A; 12 BIT,CURRENT OUT,MULTIPLYING | 24355 | DAC312-040P (ST |
| A16A1U2303 | 156-0513-00 |  | IC,MISC:CMOS,ANALOG MUX;8 CHANNEL | 04713 | MC14051B (CP OR |
| A16A1U2304 | 156-1200-00 |  | IC,LINEAR:BIFET,OP-AMP;QUAD | 01295 | TL074CN |
| A16A1U2305 | 156-1200-00 |  | IC,LINEAR:BIFET,OP-AMP;QUAD | 01295 | TL074CN |
| A16A1U2306 | 156-1126-00 |  | IC,LINEAR:BIPOLAR,COMPARATOR;OPEN COLLECTOR,200NS | 01295 | LM311P |
| A16A1U2308 | 156-0513-00 |  | IC,MISC:CMOS,ANALOG MUX;8 CHANNEL | 04713 | MC14051B (CP OR |
| A16A1U2309 | 156-0513-00 |  | IC,MISC:CMOS,ANALOG MUX;8 CHANNEL | 04713 | MC14051B (CP OR |
| A16A1U2313 | 156-1646-00 |  | IC,DIGITAL:HCTCMOS,FLIP FLOP:OCTAL D-TYPE,3-STATE | 18324 | 74HCT374N |
| A16A1U2314 | 156-1149-00 |  | IC,LINEAR:BIFET,OP-AMP; | 27014 | LF351N/GLEA134 |
| A16A1U2400 | 160-3493-00 |  | MICROCKT,DGTL:8 BIT MICROCOMPUTER, MASKED FOR READOUT SYSTEM | 04713 | SC84477P |
| A16A1U2401 | 156-1646-00 |  | IC,DIGITAL:HCTCMOS,FLIP FLOP;OCTAL D-TYPE,3-STATE | 18324 | 74HCT374N |
| A16A1U2402 | 156-1646-00 |  | IC,DIGITAL:HCTCMOS,FLIP FLOP;OCTAL D-TYPE,3-STATE | 18324 | 74HCT374N |
| A16A1U2403 | 156-1646-00 |  | IC,DIGITAL:HCTCMOS,FLIP FLOP;OCTAL D-TYPE,3-STATE | 18324 | 74HCT374N |
| A16A1U2404 | 156-0412-00 |  | IC,DIGITAL:LSTTL,COUNTER;SYNCH 4-BIT UP/DOWN BINARY | 01295 | SN74LS193N |
| A16A1U2405 | 156-0412-00 |  | IC,DIGITAL:LSTTL,COUNTER;SYNCH 4-BIT UP/DOWN BINARY | 01295 | SN74LS193N |
| A16A1U2406 | 156-1594-00 | B100100 B102625 | IC,MEMORY:NMOS,SRAM;2K X 8,150NS (2246 MOD A,2Y) | S0482 | CXK5816PN-12 |


| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A16A1U2406 | 156-2016-00 | B102626 |  | IC,MEMORY:NMOS,SRAM;2K X 8,100NS,OE (2246 MOD A.2Y) | 65786 | CY6116-55PC |
| A16A1U2406 | 156-1594-00 | B700100 | B710186 | IC,MEMORY:NMOS,SRAM;2K X 8,150NS (2246 1Y) | S0482 | CXK5816PN-12 |
| A16A1U2406 | 156-2016-00 | B710187 |  | IC,MEMORY:NMOS,SRAM;2K X 8,100NS,OE (2246 1Y) | 65786 | CY6116-55PC |
| A16A1U2407 | 156-1172-00 |  |  | IC,DIGITAL:LSTTL,COUNTER;DUAL 4-BIT BINARY | 01295 | SN74LS393N |
| A16A1U2408 | 160-5391-00 |  |  | MICROCKT,DGTL:NMOS,4096 $\times 8$ EPROM,PRGM2732A,DIP24.6 | TK2172 | 160-5391-00 |
| A16A1U2409 | 156-1172-00 |  |  | IC,DIGITAL:LSTTL.COUNTER;DUAL 4-BIT BINARY | 01295 | SN74LS393N |
| A16A1U2410 | 160-4085-00 |  |  | MICROCKT,DGTL:TTL,OCTAL 16 INPUT REGISTERED | 34335 | PAL16R8A4CN H07 |
| A16A1U2411 | 156-1646-00 |  |  | IC,DIGITAL:HCTCMOS,FLIP FLOP;OCTAL D-TYPE,3-STATE | 18324 | 74HCT374N |
| A16A1U2412 | 156-1255-00 |  |  | IC,CONVERTER:BIPOLAR,D/A;8 BIT,85NS, CURRENT | 24355 | DAC08-415P (STD |
| A16A1U2413 | 156-1255-00 |  |  | IC,CONVERTER:BIPOLAR,D/A;8 BIT,85NS, CURRENT | 24355 | DAC08-415P (STD |
| A16A1U2414 | 156-0514-00 |  |  | IC,MISC:CMOS,ANALOG MUX;DUAL 4 CHANNEL | 04713 | MC14052BCP |
| A16A1U2415 | 156-0514-00 |  |  | IC,MISC:CMOS,ANALOG MUX;DUAL 4 CHANNEL | 04713 | MC14052BCP |
| A16A1U2416 | 156-1200-00 |  |  | IC,LINEAR:BIFET,OP-AMP;QUAD | 01295 | TL074CN |
| A16A1U2417 | 156-0382-00 |  |  | IC,DIGITAL:LSTTL,GATES;QUAD 2-INPUT NAND | 01295 | SN74LS00N |
| A16A1U2501 | 156-2003-01 |  |  | MICROCKT,DGTL:MOS,8 BIT MICROPRC, 8 MHZ80188,LCC68 | 34335 | R80188 |
| A16A1U2502 | 156-2396-00 |  |  | IC,MISC:BIPOLAR,PWR SUPPLY SUPERVISOR;MPU RESET GENERATOR,5V SUPPLY SENSING | 01295 | TL7705 ACP |
| A16A1U2503 | 156-0479-00 |  |  | IC,DIGITAL:LSTTL,GATES;QUAD 2-INPUT OR | 01295 | SN74LS32N |
| A16A1U2506 | 156-0382-00 |  |  | IC,DIGITAL:LSTTL,GATES; QUAD 2-INPUT NAND | 01295 | SN74LSOON |
| A16A1U2512 | 156-1065-00 |  |  | IC,DIGITAL:LSTTL,LATCH;OCTAL D TRANSPARENT | 01295 | SN74LS373N |
| A16A1U2513 | 156-1065-00 |  |  | IC,DIGITAL:LSTTL,LATCH;OCTAL D TRANSPARENT | 01295 | SN74LS373N |
| A16A1U2514 | 156-1111-00 |  |  | IC,DIGITAL:LSTTL,TRANSCEIVER;OCTAL NONINV, 3-STATE | 01295 | SN74LS245N |
| A16A1U2515 | 156-1111-00 |  |  | IC,DIGITAL:LSTTL,TRANSCEIVER;OCTAL NONINV, 3-STATE | 01295 | SN74LS245N |
| A16A1U2517 | 156-0469-00 |  |  | IC,DIGITAL:LSTTL,DEMUX/DECODER;3-TO-8 DECODER | 01295 | SN74LS138 (N OR |
| A16A1U2518 | 156-0469-00 |  |  | IC,DIGITAL:LSTTL,DEMUX/DECODER;3-TO-8 DECODER | 01295 | SN74LS138 (N OR |
| A16A1U2521 | 156-2473-00 |  |  | IC,MEMORY:CMOS,SRAM; $8 \mathrm{~K} \times 8,200 \mathrm{NS}$,10UA,OE | 0JR04 | TC5564PL-20 |
| A16A1U2523 | 156-1646-00 |  |  | IC.DIGITAL:HCTCMOS,FLIP FLOP;OCTAL D-TYPE,3-STATE | 18324 | 74HCT374N |
| A16A1U2524 | 156-1646-00 |  |  | IC,DIGITAL:HCTCMOS,FLIP FLOP;OCTAL D-TYPE,3-STATE | 18324 | 74HCT374N |
| A16A1U2525 | 156-1058-00 |  |  | IC,DIGITAL:STTL,BUFFER/DRIVER;OCTAL INV, 3-STATE | 01295 | SN74S240N |
| A16A1W2105 | 174-1040-0 |  |  | CA ASSY,SP,ELEC:17 COND,5.1 L,RIBBON | 00779 | 487839-1 |
| A16A1XU2400 | 136-0755-00 |  |  | SOCKET,DIP::PCB, 28 POS, $2 \times 14,0.1 \times 0.6$ CTR, 0.175 H $\times 0.13$ TAIL,BECU,TIN | 09922 | DILB28P-108 |
| A16A1XU2501 | 136-0813-00 |  |  | SKT,PL-IN ELEK:CHIP CARRIER,68 CONTACTS | 53387 | 268-5400-00-110 |

Replaceable Electrical Parts 2246 1Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A16A1XU2519 | 136-0963-00 |  |  | SOCKET,DIP:PCB;,STR, $2 \times 16,32$ POS,0. $1 \times 0.6$ M 0.008-0.014 THK IC LEAD | 00779 | 2-644018-3 |
| A16A1Y2501 | 119-2936-00 | B100100 | B113058 | RESONATOR:16MHZ,CER (2246 MOD A.2Y) | 51406 | CSA 16.00MX11 |
| A16A1Y2501 | 119-2936-01 | B113059 |  | RESONATOR:16MHZ,CER (2246 MOD A.2Y) | TK2058 | 82501009 |
| A16A1Y2501 | 119-2936-00 | B700100 | B711085 | RESONATOR:16MHZ,CER (2246 1Y) | 51406 | CSA 16.00MX11 |
| A16A1Y2501 | 119-2936-01 | B711086 |  | RESONATOR: $16 \mathrm{MHZ}, \mathrm{CER}$ (2246 1Y) | TK2058 | 82501009 |

Replaceable Electrical Parts 2246 1Y and 2246 MOD A Service

| Component Number | Tektronix <br> Part No. | Serial No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A18 | 670-9398-04 |  | CIRCUIT BD ASSY:LV POWER SUPPLY (2246 YY) | 80009 | 670939804 |
| A18 | 670-9398-04 |  | CIRCUIT BD ASSY:LV POWER SUPPLY (2246 MOD A,2Y) | 80009 | 670939804 |
| A18C2201 | 285-1177-01 |  | CAP,FXD,PLASTIC:1UF,10\%,450V | 84411 | TEK326 1.0104 |
| A18C2202 | 290-1206-00 |  | CAP,FXD,ELCTLT:270UF,20\%,450V, | 1W344 | SMG450VR271M35 |
| A18C2203 | 290-0922-01 |  | CAP,FXD,ELCTLT: $1000 \mathrm{UF}, 4100 \%-10 \%, 50 \mathrm{~V}$ | 01884 | ORDER BY DESC |
| A18C2204 | 290-1151-00 |  | CAP,FXD,ELCTLT:100UF,20\%,63V | 55680 | UEB1J101MRAICA |
| A18C2206 | 281-0775-01 |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A18C2207 | 281-0775-01 |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A18C2208 | 290-1144-00 |  | CAP,FXD,ELCTLT:4.7UF,20\%,100V | 1W344 | SM100VB4R7M6X11 |
| A18C2209 | 281-0773-00 |  | CAP,FXD,CER DI:0.01UF,10\%,100V,,TUBULAR,MI | 04222 | SA201C103KAA |
| A18C2210 | 290-1144-00 |  | CAP,FXD,ELCTLT:4.7UF,20\%,100V | 1W344 | SM100VB4R7M6X11 |
| A18C2211 | 281-0773-00 |  | CAP,FXD,CER DI:0.01UF, $10 \%, 100 \mathrm{~V}$, ,TUBULAR,MI | 04222 | SA201C103KAA |
| A18C2212 | 281-0761-00 |  | CAP,FXD,CER DI:27PF,5\%,100V | 04222 | SA101A270JAA |
| A18C2213 | 285-1381-00 |  | CAP,FXD,MTLZD:1500PF, $10 \%, 250 \mathrm{~V}$ | TK0515 | PME271Y415 |
| A18C2214 | 285-1381-00 |  | CAP,FXD,MTLZD:1500PF, $10 \%, 250 \mathrm{~V}$ | TK0515 | PME271Y415 |
| A18C2215 | 285-1252-00 |  | CAP,FXD,PLASTIC:0.15UF,10\%,250VAC, | D5243 | F1772-415-2000 |
| A18C2216 | 285-1252-00 |  | CAP,FXD,PLASTIC:0.15UF,10\%,250VAC, | D5243 | F1772-415-2000 |
| A18C2217 | 285-1381-00 |  | CAP,FXD,MTLZD:1500PF, $10 \%, 250 \mathrm{~V}$ | TK0515 | PME271Y415 |
| A18C2218 | 281-0813-00 |  | CAP,FXD,CER DI:0.047UF,20\%,50V | 04222 | SA105E473MAA |
| A18C2219 | 281-0773-00 |  | CAP,FXD,CER DI:0.01UF, $10 \%, 100 \mathrm{~V}$, ,TUBULAR,MI | 04222 | SA201C103KAA |
| A18C2221 | 290-1129-00 |  | CAP,FXD,ELCTLT: 1000 UF, $+100 \%-10 \%, 12 \mathrm{~V}$ | 24165 | 672D108H012DS2A |
| A18C2222 | 290-1129-00 |  | CAP,FXD,ELCTLT: 1000 UF, $+100 \%-10 \%, 12 \mathrm{~V}$ | 24165 | 672D108H012DS2A |
| A18C2223 | 290-1129-00 |  | CAP,FXD,ELCTLT: $1000 \mathrm{UF},+100 \%-10 \%, 12 \mathrm{~V}$ | 24165 | 672D108H012DS2A |
| A18C2224 | 290-1129-00 |  | CAP,FXD,ELCTLT: 1000 UF, $+100 \%-10 \%, 12 \mathrm{~V}$ | 24165 | 672D108H012DS2A |
| A18C2225 | 290-1129-00 |  | CAP,FXD,ELCTLT: 1000 UF, $+100 \%-10 \%, 12 \mathrm{~V}$ | 24165 | 672D108H012DS2A |
| A18C2226 | 290-1129-00 |  | CAP,FXD,ELCTLT: 1000 UF $+100 \%-10 \%, 12 \mathrm{~V}$ | 24165 | 672D108H012DS2A |
| A18C2227 | 290-1129-00 |  | CAP,FXD,ELCTLT: 1000 UF $+100 \%-10 \%, 12 \mathrm{~V}$ | 24165 | 672D108H012DS2A |
| A18C2228 | 290-1129-00 |  | CAP,FXD,ELCTLT: $1000 \mathrm{UF},+100 \%-10 \%, 12 \mathrm{~V}$ | 24165 | 672D108H012DS2A |
| A18C2229 | 290-1128-00 |  | CAP,FXD,ELCTLT:470UF, $+100 \%$,25V | 24165 | 672D477H025DS2A |
| A18C2230 | 290-1128-00 |  | CAP,FXD, ELCTLT:470UF, $+100 \%$,25V | 24165 | 672D477H025DS2A |
| A18C2232 | 290-1130-00 |  | CAP,FXD,ELCTLT:39UF, $+100 \%-10 \%, 150 \mathrm{~V}$ | 24165 | 672D396H150ET2A |
| A18C2233 | 290-1130-00 |  | CAP,FXD,ELCTLT:39UF $+100 \%-10 \%, 150 \mathrm{~V}$ | 24165 | 672D396H150ET2A |
| A18C2234 | 290-1128-00 |  | CAP,FXD,ELCTLT:470UF, $+100 \%$, 25 V | 24165 | 672D477H025DS2A |
| A18C2236 | 290-1128-00 |  | CAP,FXD,ELCTLT:470UF, $+100 \%, 25 \mathrm{~V}$ | 24165 | 672D477H025DS2A |
| A18C2238 | 290-1144-00 |  | CAP,FXD,ELCTLT:4.7UF,20\%,100V | 1W344 | SM100VB4R7M6X11 |
| A18C2239 | 281-0775-01 |  | CAP,FXD,CER DI:0.1UF,20\%,50V | 04222 | SA105E104MAA |
| A18C2243 | 281-0770-00 |  | CAP,FXD,CER DI: $1000 \mathrm{PF}, 20 \%, 100 \mathrm{~V}$ | 04222 | SA101C102MAA |
| A18C2244 | 285-1184-01 |  | CAP,FXD,MTLZD:0.01UF,20\%,4KV | 84411 | TEK183.01 204 |
| A18C2245 | 285-1184-01 |  | CAP,FXD,MTLZD:0.01UF,20\%,4KV | 84411 | TEK183.0120 4 |
| A18C2248 | 290-1151-00 |  | CAP,FXD,ELCTLT:100UF,20\%,63V | 55680 | UEB1J101MRAICA |

Replaceable Electrical Parts
22461 Y and 2246 MOD A Service

| Component Number | Tektronix <br> Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A18C2249 | 281-0773-00 |  |  | CAP,FXD,CER DI:0.01UF, $10 \%, 100 \mathrm{~V}$, ,TUBULAR,MI | 04222 | SA201C103KAA |
| A18CR2201 | 152-0661-01 |  |  | SEMICOND DVC,DI:RECT,SI,600V,3A | 04713 | MUR460RL |
| A18CR2202 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A18CR2204 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A18CR2205 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A18CR2206 | 152-0582-00 |  |  | DIODE,RECT:SCHTKY; 20V,3A, 475VF,80A IFSM | 04713 | 1N5820 |
| A18CR2207 | 152-0582-00 |  |  | DIODE,RECT:SCHTKY; 20V,3A..475VF,80A IFSM | 04713 | 1N5820 |
| A18CR2208 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A18CR2209 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A18CR2210 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V, 1A,200NS | 14552 | MB2501 |
| A18CR2211 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V, 1A,200NS | 14552 | MB2501 |
| A18CR2212 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V, 1A,200NS | 14552 | MB2501 |
| A18CR2213 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A18CR2214 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A18CR2215 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY; $400 \mathrm{~V}, 1 \mathrm{~A}, 200 \mathrm{NS}$ | 14552 | MB2501 |
| A18CR2216 | 152-0400-00 |  |  | DIODE, RECT:,FAST RCVRY;400V, 1A,200NS | 14552 | MB2501 |
| A18CR2218 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V, 1A,200NS | 14552 | MB2501 |
| A18CR2219 | 152-0581-00 |  |  | DIODE,RECT:SCHTKY; 20V, 1 A, , 450VF,25A IFSM | 04713 | 1N5817 |
| A18CR2220 | 152-0581-00 |  |  | DIODE,RECT:SCHTKY; 20V,1A, 450VF,25A IFSM | 04713 | 1N5817 |
| A18CR2227 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V, 1A,200NS | 14552 | MB2501 |
| A18CR2228 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V,1A,200NS | 14552 | MB2501 |
| A18CR2231 | 152-0040-00 |  |  | DIODE,RECT:,760V,1A,50A IFSM | 14936 | GP15J-069 |
| A18CR2232 | 152-0040-00 |  |  | DIODE, RECT:;600V,1A,50A IFSM | 14936 | GP15J-069 |
| A18CR2233 | 152-0040-00 |  |  | DIODE,RECT:,600V,1A,50A IFSM | 14936 | GP15J-069 |
| A18CR2234 | 152-0040-00 |  |  | DIODE,RECT:;600V,1A,50A IFSM | 14936 | GP15J-069 |
| A18CR2235 | 152-0400-00 |  |  | DIODE,RECT:,FAST RCVRY;400V, 1A,200NS | 14552 | MB2501 |
| A18CR2236 | 152-0400-00 |  |  | DIODE,RECT, FAST RCVRY;400V, 1A,200NS | 14552 | MB2501 |
| A18CR2237 | 152-0141-02 |  |  | DIODE,SIG:,ULTRA FAST;40V,150MA,4NS,2PF | 27014 | FDH9427 |
| A18DS2201 | 150-0035-00 |  |  | LAMP,GLOW:90V MAX,0.3MA,AID-T,WIRE LD | TK0213 | JH005/3011JA |
| A18.12208 | 131-3645-00 |  |  | CONN,HDR:PCB,;MALE,STR, $1 \times 3,0.1$ CTR,0.295 MLG $\times 0.140$ TAIL,TIN | 00779 | 644292-3 |
| A18, 22225 | 131-3486-00 |  |  | CONN,HDR:PCB; $M A L E, S T R, 1 \times 2,0.1$ CTR,0.295 MLG $\times 0.140$ TAIL,TIN | 00779 | 644292-2 |
| A18L2201 | 108-1324-00 |  |  | COIL,RF:FXD,33UH,POWER | 54583 | OL1338-330K5R0 |
| A18L2202 | 108-1319-00 |  |  | INDUCTOR,FIXED:33UH,10\%,1.8A | 54583 | TSL1110-330K 1R |
| A18L2203 | 108-1319-00 |  |  | INDUCTOR,FIXED:33UH,10\%,1.8A | 54583 | TSL1110-330K 1R |
| A18L2204 | 108-1319-00 |  |  | INDUCTOR,FIXED:33UH,10\%,1.8A | 54583 | TSL1110-330K 1R |
| A18L2205 | 108-1319-00 |  |  | INDUCTOR,FIXED:33UH,10\%,1.8A | 54583 | TSL1110-330K 1R |
| A18L2206 | 108-1396-00 | B700100 | B711084 | INDUCTOR,FIXED:150UH, $10 \%, 0.82 \mathrm{~A}$, RADIAL LEAD (2246 1Y) | TK2058 | TSL1110-151KR82 |
| A18L2206 | 108-1319-00 | B711085 |  | INDUCTOR,FIXED:33UH, $10 \%, 1.8 \mathrm{~A}$ (2246 1Y) | 54583 | TSL1110-330K 1R |
| A18L2206 | 108-1396-00 | B100100 | B113046 | INDUCTOR,FIXED:150UH, $10 \%, 0.82 \mathrm{~A}$, RADIAL LEAD <br> (2246 MOD A,2Y) | TK2058 | TSL1110-151KR82 |


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| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A18L2206 | 108-1319-00 | B113047 |  | INDUCTOR,FIXED:33UH,10\%,1.8A (2246 MOD A,2Y) | 54583 | TSL1110-330K 1R |
| A18L2207 | 108-1357-00 |  |  | COIL,RF:FXD,POWER | TK1441 | 86-343-2 |
| A18L2208 | 108-1357-00 |  |  | COIL,RF:FXD,POWER | TK1441 | 86-343-2 |
| A18P2204 | 131-3637-00 |  |  | CONN,HDR PWR::PCB,;MALE,STR, $1 \times 13,0.156$ CTR,0. 953 MLG $\times 0.120$ TAIL | 27264 | A-41661-A-13E50 |
| A18Q2201 | 151-1214-00 |  |  | TRANSISTOR,PWR:MOS,N-CH;500V,4.5A,1.5 OHM | 04713 | STP4063 |
| A18Q2202 | 151-0190-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN;4OV,200MA, 300MHZ,AMPLIFIER | 04713 | 2N3904 |
| A1802203 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A18Q2204 | 151-0190-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN:40V,200MA, 300MHZ,AMPLIFIER | 04713 | 2N3904 |
| A18Q2206 | 151-0565-00 |  |  | THYRISTOR,SCR:8A,200V,SENS GATE,TO-220 W/LEADFORM | 04713 | SCR2117 |
| A18Q2208 | 151-0190-00 |  |  | TRANSISTOR,SIG:BIPOLAR,NPN;4OV,200MA, $300 \mathrm{MHZ}, \mathrm{AMPLIFIER}$ | 04713 | 2N3904 |
| A18Q2209 | 151-0476-03 |  |  | TRANSISTOR:POWER,W/LEAD FORM,TO-220SELECTED,TIP31C | 04713 | SJE389-001 |
| A18Q2210 | 151-0476-03 |  |  | TRANSISTOR:POWER,W/LEAD FORM,TO-220SELECTED,TIP31C | 04713 | SJE389-001 |
| A18Q2211 | 151-0188-00 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;40V,200MA, 250MHZ,AMPLIFIER | 04713 | 2N3906 |
| A18Q2212 | 151-0276-01 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP;50V,50MA,40MHZ, AMPLIFIER | OJR04 | S1423-TPE2 (AMM |
| A1802213 | 151-0276-01 |  |  | TRANSISTOR,SIG:BIPOLAR,PNP:50V,50MA,40MHZ, AMPLIFIER | OJR04 | S1423-TPE2 (AMM |
| A18Q2214 | 151-1197-00 |  |  | TRANSISTOR,PWR:MOS,N-CH;60V, 12A,0.25 OHM | 04713 | IRF533 W/LEADFO |
| A18R2201 | 308-0678-00 |  |  | RES,FXD,WW:0.1 OHM,5\%,2W | 91637 | CPF-2-R10J-T/R |
| A18R2203 | 301-0184-00 |  |  | RES,FXD,FILM:180K OHM,5\%,0.5W | 57668 | TR50J-E180K |
| A18R2204 | 301-0184-00 |  |  | RES,FXD,FILM:180K OHM, $5 \%, 0.5 \mathrm{~W}$ | 57668 | TR50J-E180K |
| A18R2205 | 313-1104-00 |  |  | RES,FXD,FILM:100K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10002J |
| A18R2206 | 313-1104-00 |  |  | RES,FXD,FILM:100K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10002J |
| A18R2207 | 322-3164-00 |  |  | RES,FXD,FILM:499 OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TOMI | 91637 | CCF501G499ROF |
| A18R2208 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000 J |
| A18R2209 | 313-1104-00 |  |  | RES,FXD,FILM:100K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10002 J |
| A18R2210 | 313-1513-00 |  |  | RES,FXD,CMPSN:51K OHM,5\%,0.2W | 91637 | CCF50-2-51001J |
| A18R2211 | 313-1332-00 |  |  | RES,FXD,FILM:3.3K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-33000 J |
| A18R2212 | 313-1822-00 |  |  | RES,FXD,FILM:8.2K,OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-82000」 |
| A18R2215 | 313-1272-00 | B100100 | B110219 | RES,FXD,FILM:2.7K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-27000J |
| A18R2215 | 313-1222-00 | B110220 |  | RES,FXD,FILM:2.2K OHM,5\%,0.2W (2246 MOD A,2Y) | 91637 | CCF50-2-22000 J |
| A18R2215 | 313-1272-00 | B700100 | B710142 | RES,FXD,FILM:2.7K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-27000 J |
| A18R2215 | 313-1222-00 | B710143 |  | RES,FXD,FILM:2.2K OHM,5\%,0.2W (2246 1Y) | 91637 | CCF50-2-22000 ${ }^{\text {J }}$ |
| A18R2216 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A18R2218 | 313-1101-00 |  |  | RES,FXD,FILM: 100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A18R2219 | 313-1105-00 |  |  | RES,FXD,FILM:1M ОНM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10003J |
| A18R2220 | 313-1105-00 |  |  | RES,FXD.FILM:1M OHM,5\%,0.2W | 91637 | CCF50-2-10003 |

Replaceable Electrical Parts 22461 Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial No. Effective Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A18R2221 | 313-1203-00 |  | RES, FXD,FILM:20K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-20001J |
| A18R2222 | 313-1103-00 |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A18R2223 | 313-1105-00 |  | RES,FXD,FILM:1M OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10003J |
| A18R2224 | 313-1101-00 |  | RES,FXD,FILM:100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A18R2225 | 313-1101-00 |  | RES,FXD,FILM:100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A18R2226 | 301-0274-00 |  | RES,FXD,FILM:270K OHM, $5 \%, 0.5 \mathrm{~W}$ | 19701 | 5053CX270KOJ |
| A18R2227 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A18R2228 | 313-1102-00 |  | RES,FXD,FILM:1K OHM,5\%,0.2W | 91637 | CCF50-2-10000J |
| A18R2229 | 301-0823-00 |  | RES,FXD,FILM:82K OHM, $5 \%, 0.5 \mathrm{~W}$ | TK1727 | SFR30 2322-182- |
| A18R2230 | 301-0823-00 |  | RES,FXD,FILM:82K OHM, $5 \%, 0.5 \mathrm{~W}$ | TK1727 | SFR30 2322-182- |
| A18R2231 | 315-0101-03 |  | RES,FXD,CMPSN: 100 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |
| A18R2232 | 313-1102-00 |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A18R2233 | 313-1103-00 |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A18R2236 | 313-1104-00 |  | RES,FXD,FILM: 100 K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10002J |
| A18R2237 | 313-1105-00 |  | RES,FXD,FILM:1M OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10003J |
| A18R2238 | 313-1753-00 |  | RES,FXD,FILM:75K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-75001J |
| A18R2239 | 313-1103-00 |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A18R2240 | 313-1204-00 |  | RES,FXD,FILM:200K,5\%,0.2W | 91637 | CCF50-2-20002J |
| A18R2241 | 313-1103-00 |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A18R2242 | 313-1027-00 |  | RES,FXD.FILM:2.7 OHM,5\%,0.2W | 57668 | TR20JE 02E7 |
| A18R2243 | 313-1027-00 |  | RES,FXD,FILM:2.7 OHM, $5 \%, 0.2 \mathrm{~W}$ | 57668 | TR20JE 02E7 |
| A18R2245 | 313-1103-00 |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A18R2246 | 313-1513-00 |  | RES,FXD,CMPSN:51K OHM,5\%,0.2W | 91637 | CCF50-2-51001J |
| A18R2247 | 322-3289-00 |  | RES, FXD,FILM:10K OHM, $1 \%, 0.2 \mathrm{~W}, \mathrm{TC}=$ TO | 91637 | CCF50G10001F |
| A18R2248 | 313-1513-00 |  | RES,FXD,CMPSN:51K OHM,5\%,0.2W | 91637 | CCF50-2-51001J |
| A18R2250 | 301-0106-00 |  | RES,FXD,FILM:10M OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB1065 |
| A18R2252 | 311-2270-00 |  | RES,VAR,NONWW:TRMR,10K OHM,20\%,0.5WLINEAR,MI | 32997 | ADVISE |
| A18R2253 | 313-1101-00 |  | RES,FXD,FILM:100 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-100ROJ |
| A18R2254 | 313-1051-00 |  | RES,FXD,FILM:5.1 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CT3-5R100J |
| A18R2255 | 313-1051-00 |  | RES,FXD,FILM:5.1 OHM,5\%,0.2W | 91637 | CT3-5R100」 |
| A18R2256 | 301-0274-00 |  | RES,FXD,FILM:270K OHM,5\%,0.5W | 19701 | 5053CX270к0J |
| A18R2257 | 301-0200-00 |  | RES,FXD,FILM:20 OHM,5\%,0.5W | 19701 | 5053CX20R00J |
| A18R2259 | 315-0472-03 |  | RES,FXD,CMPSN:4.7K OHM, 5\%,0.25W | 01121 | CB4725 |
| A18R2260 | 301-0560-00 |  | RES,FXD,FILM: 56 OHM,5\%,0.5W | 01121 | EB5605 |
| A18R2265 | 313-1101-00 |  | RES,FXD,FILM: 100 OHM,5\%,0.2W | 91637 | CCF50-2-100ROJ |
| A18R2266 | 315-0472-03 |  | RES, FXD,CMPSN:4.7K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4725 |
| A18R2267 | 307-0113-00 |  | RES, FXD,CMPSN:5.1 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB51G5 |
| A18R2268 | 313-1103-00 |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A18R2270 | 313-1103-00 |  | RES,FXD,FILM:10K OHM,5\%,0.2W | 91637 | CCF50-2-10001J |
| A18R2271 | 313-1512-00 |  | RES,FXD,FILM:5.1K OHM,5\%,0.2W | 91637 | CCF50-2-51000J |

Replaceable Electrical Parts 2246 1Y and 2246 MOD A Service

| Component Number | Tektronix <br> Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A18R2272 | 313-1051-00 |  |  | RES,FXD,FILM:5.1 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CT3-5R100J |
| A18R2273 | 313-1051-00 |  |  | RES,FXD,FILM:5.1 OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CT3-5R100J |
| A18R2274 | 313-1103-00 |  |  | RES,FXD,FILM:10K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10001J |
| A18R2275 | 301-0432-00 |  |  | RES,FXD,FILM:4.3K OHM, $5 \%, 0.5 \mathrm{~W}$ | TK1727 | SFR30 2322-182- |
| A18R2276 | 313-1102-00 |  |  | RES,FXD,FILM:1K OHM, $5 \%, 0.2 \mathrm{~W}$ | 91637 | CCF50-2-10000J |
| A18RT2201 | 307-0863-00 | B100100 | B113898 | RES,THERMAL:10 OHM,10\%,NTC (2246 MOD A. 2 Y) | 15454 | SG-13S |
| A18RT2201 | 307-1551-00 | B113899 |  | RES,THERMAL: 20 OHM,10\%,NTC. (2246 MOD A,2Y) | 91637 | TECHNO COMPONEN |
| A18RT2201 | 307-0863-00 | B700100 | B711093 | RES,THERMAL: 10 OHM, $10 \%$,NTC (2246 1Y) | 15454 | SG-13S |
| A18RT2201 | 307-1551-00 | B711094 |  | RES,THERMAL:20 OHM, $10 \%$, NTC, (2246 1Y) | 91637 | TECHNO COMPONEN |
| A18S2201 | 260-2443-00 |  |  | SWITCH,PUSH:POWER,DPST,6A,250VAC, | 31918 | 130238 |
| A18S2202 | 260-2318-00 |  |  | SWITCH,THRMSTC:NC, 105 DEG C OPEN, 80 DEG C CLOSED, 10A,240VAC | 14604 | 2455R-161-101 |
| A18T2203 | 120-1686-00 |  |  | TRANSFORMER,RF:COUPLED INDUCTOR, | 75498 | ORDER BY DESC |
| A18T2204 | 120-1685-01 |  |  | XFMR,PWR,STU:HIGH VOLTAGE, | 75498 | ORDER BY DESC |
| A18T2205 | 120-1347-00 |  |  | TRANSFORMER,RF:DRIVER SATURATING | 54583 | BDT-001 |
| A18T2206 | 120-1401-00 |  |  | XFMR,TRIGGER:LINE,1:1 TURNS RATIO, | 54937 | DM1 500-2044 |
| A18U2201 | 156-2395-00 |  |  | MICROCKT,LINEAR:BIPOLAR,PWM POWER SUPPLY,CONTROLLER | 04713 | MC34060AP |
| A18U2230 | 152-0926-00 |  |  | SEMICOND DVC,DI:HIGH VOLTAGE MULTIPLIER,INPUT VOLTAGE 5.7KVAC PEAK TOPEAK, OUTPUT VOLTAGE 13750 VDC | 51406 | MSL8523 |
| A18VR2201 | 152-0255-00 |  |  | DIODE,ZENER:;51V,5\%,0.4W | 04713 | 1N978BRL |
| A18VR2202 | 152-0166-00 |  |  | DIODE,ZENER:;6.2V,5\%,0.4W | 04713 | SZ11738RL |
| A18VR2203 | 152-0304-00 |  |  | DIODE,ZENER:;;20V,5\%,0.4W | 04713 | 1N968BRL |
| A18VR2204 | 307-0456-00 |  |  | RES,V SENSITIVE:250VAC,20W,METAL OXIDE | 34371 | MOV-V250LA20A |
| A18VR2205 | 152-0166-00 |  |  | DIODE,ZENER:;6.2V,5\%,0.4W | 04713 | SZ11738RL |
| A18VR2206 | 152-0282-00 |  |  | DIODE,ZENER:;30V,2\%,0.4W | 04713 | 1N972CRL |
| A18VR2207 | 152-0304-00 |  |  | DIODE,ZENER:;,20V,5\%,0.4W | 04713 | 1 N 968 BRL |
| A18W28 | 196-3093-00 |  |  | LEAD,ELECTRICAL:18 AWG,3.3 L.8-9, | 0J7N9 | PER TEK DRAWING |
| A18W29 | 196-3092-00 |  |  | LEAD,ELECTRICAL:18 AWG, 3.3 L, 8-0, | 0.J7N9 | PER TEK DRAWING |
| A18W31 | 196-3094-00 |  |  | LEAD,ELECTRICAL:26 AWG, 2.6 L,9-N | 0.J7N9 | PER TEK DRAWING |
| A18W32 | 196-3094-00 |  |  | LEAD,ELECTRICAL:26 AWG, 2.6 L,9-N | 0J7N9 | PER TEK DRAWING |
| A18W2201 | 131-0566-00 |  |  | BUS,CONDUCTOR:DUMMY RES,0.094 OD X 0.225L | 24546 | OMA 07 |

Replaceable Electrical Parts
2246 1Y and 2246 MOD A Service

| Component Number | Tektronix Part No. | Serial Effective | No. Dscont | Name \& Description | Mfr. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| B25 | 119-2063-00 | B700100 | B710985 | FAN,TUBEAXIAL:12V,130MA,19.4 CFM (2246 iY) | 61529 | A1F891003 |
| B25 | 119-3564-00 | B710986 |  | FAN,TUBEAXIAL:12VDC,2.6W,3200RPM,36CFM (2246 1Y) | TKOIH | MD1208PTS1 |
| B25 | 119-2063-00 | B100100 | B111718 | FAN,TUBEAXIAL:12V,130MA,19.4 CFM (2246 MOD A,2Y) | 61529 | A1F891003 |
| B25 | 119-3564-00 | B111719 |  | FAN,TUBEAXIAL:12VDC,2.6W,3200RPM,36CFM (2246 MOD A,2Y) | TKOIH | MD1208PTS1 |
| DL21 | 119-2118-01 |  |  | DELAY LINE,ELEC:2246,W/STRAIN RELIEF | 80009 | 119211801 |
| F2201 | 159-0023-00 |  |  | FUSE,CARTRIDGE:3AG,2A,250V,SLOW BLOW, | 71400 | MDX2 |
| FL2201 | 119-2055-00 |  |  | FILTER,RFI:3A, 115-230V,48-440HZ | S4307 | FN322-3/05 |
| J16 | 131-0955-00 |  |  | CONN,RF JACK:BNC,;50 OHM,FEMALE,STR,SLDR CUP/FRONT PNL,GOLD, $0.520 \mathrm{MLG} \times 0.490$ TAIL, 0.092 L SLDR CUP,0.375-32/D/1 FLAT | 24931 | 28.JR200-1 |
| V1 | 154-0905-00 |  |  | ELECTRON TUBE:CRT | 80009 | 154090500 |
| W30 | 195-3990-00 |  |  | LEAD,ELECTRICAL:18 AWG,4.5 L.5-4 | TK0032 | ORDER BY DESC |

## DIAGRAMS AND CIRCUIT BOARD ILLUSTRATIONS

## Symbols

Graphic symbols and class designation letters are based on ANSI Standard Y32.2-1975

Logic symbology is based on ANSI/IEEE 91-1984. Logic symbols depict the logic function performed and may
differ from the manufacturer's data.

The overline on a signal name indicates that the signal performs its intended function when it is in the LO state.

Abbreviations are based on ANSI Y1.1-1972.

Other ANSI standards that are used in the preparation of diagrams by Tektronix, Inc., are:

Y14.15-1966 Drafting Practices.
Y14.2M-1979 Line Conventions and Lettering Letter Symbols for Quantities Used in Electrical Science and Electrical Engineering

American National Standards Institute 1430 Broadway
New York, New York 10018
Component Values
Electrical components shown on the diagrams are in the following units unless noted otherwise:
$\begin{array}{ll}\text { Capacitors } & \begin{array}{l}\text { Values one or greater are in picofarads }(\mathrm{pF}) \text {. } \\ \text { Values less than one are in microfarads }(\mu \mathrm{F}) .\end{array} \\ & \end{array}$
Resistors Ohms ( $\Omega$ )

The information and special symbols below may appear in this manual.


Assembly Numbers and Grid Coordinates
Each assembly in the instrument is assigned an assembly number (e.g., A20). The assembly number
appears on the circuit board outline on the diagram, inthe appears on the circuit board outline on the diagram, in the
title for the circuit board component location illustration, and in the lookup table for the schematic diagram and corresponding component locator illustration. The Replaceable Electrical Parts list is arranged by assemblies in numerical sequence; the components are listed by component number *(see following illustration for constructing a component number)

The schematic diagram and circuit board component location illustration have grids. A lookup table with the grid coordinates is provided for ease of locating the component. Only the components illustrated on the facing diagram are listed in the lookup table. When more than a circuit board, the circuit board illustration may only appear opposite the first diagram on which it was illustrated; the lookup table will list the diagram number of other diagrams that the circuitry of the circuit board appears on.

Masion ciocaw
Strap or Link

Plug to E.C. Board
 $\underset{\substack{\text { Coaxial connector } \\ \text { male } \\ \text { female }}}{\text { and }}$ Iemale
Plug Index: signifies pin No. 1 Shiedting Selectied ralue, see Parts List
ancmannena
Selection Citeria Section tor

Decoupled or Filtered
Votitase
Votatage or fillered
Refer to Diagram Number Schemalic Name
and Number

(1)(2) and (3) -1 st, 2nd, and 3rd significant figures

(TC) -temperature coefficient


| COLOR | SIGNIFICANT <br> FIGURES | RESISTORS |  |  |
| :--- | :---: | :--- | :--- | :---: |
|  |  | MULTIPLIER | TOLERANCE |  |
| BLACK | 0 | 1 | --- |  |
| BROWN | 1 | 10 | $\pm 1 \%$ |  |
| RED | 2 | $10^{2}$ or 100 | $\pm 2 \%$ |  |
| ORANGE | 3 | $10^{3}$ or 1 K | $\pm 3 \%$ |  |
| YELLOW | 4 | $10^{4}$ or 10 K | $\pm 4 \%$ |  |
| GREEN | 5 | $10^{5}$ or 100 K | $\pm 1 / \%$ |  |
| BLUE | 6 | $10^{6}$ or 1 M | $\pm 1 / 2 \%$ |  |
| VIOLET | 7 | --- | $\pm 1 / 10 \%$ |  |
| GRAY | 8 | --- | --- |  |
| WHITE | 9 | --- | --- |  |
| GOLD | - | $10^{-1}$ or 0.1 | $\pm 5 \%$ |  |
| SILVER | - | $10^{-2}$ or 0.01 | $\pm 10 \%$ |  |
| NONE | - | --- | $\pm 20 \%$ |  |
|  |  | $(1861-2046081 \cdot 95$ |  |  |


(1)(2) and (3) -1 st, 2nd, and 3rd significant figures
(M) -multiplier
(T) -tolerance
(TC) -temperature coefficient
(P) -polarity and voltage rating

| COLOR | SIGNIFICANT <br> FIGURES | RESISTORS |  |  |
| :--- | :---: | :--- | :--- | :---: |
|  |  | MULTIPLIER | TOLERANCE |  |
| BLACK | 0 | 1 | --- |  |
| BROWN | 1 | 10 | $\pm 1 \%$ |  |
| RED | 2 | $10^{2}$ or 100 | $\pm 2 \%$ |  |
| ORANGE | 3 | $10^{3}$ or 1 K | $\pm 3 \%$ |  |
| YELLOW | 4 | $10^{4}$ or 10 K | $\pm 4 \%$ |  |
| GREEN | 5 | $10^{5}$ or 100 K | $\pm 1 / 2 \%$ |  |
| BLUE | 6 | $10^{6}$ or 1 M | $\pm / 2 \%$ |  |
| VIOLET | 7 | --- | $\pm 1 / 10 \%$ |  |
| GRAY | 8 | --- | --- |  |
| WHITE | 9 | --- | --- |  |
| GOLD | - | $10^{-1}$ or 0.1 | $\pm 5 \%$ |  |
| SILVER | - | $10^{-2}$ or 0.01 | $\pm 10 \%$ |  |
| NONE | - | --- | $\pm 20 \%$ |  |
|  |  | $(1861-20416081-95$ |  |  |




INTEGRATED CIRCUITS

LEAD CONFIGURATIONS AND CASE STYLES ARE TYPICAL, BUT MAY VARY DUE TO VENDOR CHANGES OR INSTRUMENT MODIFICATIONS.
a. Identify the Assembly Number of the circuit board that the component is mechanical parts exploded views at the rear of this manual.
To identify any component mounted on a circuit board and to locate that comp nent in the schematic diagram.
b. In the manual, locate the tabbed foldout page that corresponds with the Assembly Number of the circuit board. The circuit boord assembly num-
bers and names are printed on the back side of the tabs (facing the rear of the manual).
2. Determine the Circuit Number and Schematic Diagram.
a. Compare the circuit board with its illustration. Locate the component you are looking for by area and shape on the illustration to determine its Circuit Number.
Scan the lookup table next to the Circuit Board illustration to find the Circuit Number of the component.
c. Read the SCHEM NUMBER column next to the component's circuit number to find the Schematic Diagram number.
3. Locate the Component on the Schematic Diagram.
a. Locate the tabbed page that corresponds to the Schematic Diagram number. Schematic diagram numbers and names are printed on the front side of the tabs (facing the front of the manual).
Locate the Assembly Number in the Component Location lookup table next to the schematic diagram. Scan the CIRCUIT NUMBER colum of next to the schematic diagram. Scan the CIRCUIT NUMBER column of
that table to find the Circuit Number of the component you are looking for in the schematic.


1. Determine the Circuit Board Illustration and Component Location.
a. From the schematic diagram, determine the Assembly Number of the circuit board that the component is on. The Assembly Number and Name is boxed and located in a corner of the heavy line marking the circuit board outine in the schematic diagram.
b. Find the Component Location table for the Assembly Number found on the schematic. Scan the CIRCUIT NUMBER column to find the Circuit Num-
ber of the component.
c. Look in the BOARD LOCATION column next to the component number and read its circuit board grid coordinates.
2. Locate the Component on the Circuit Board.
a. In the manual, locate the tabbed page that corresponds to Assembly Number the component is on. Assembly numbers and names for circuit boards
are on the back side of the tabs.
b. Using the Circuit Number of the component and its given grid location, find the component in the Circuit Board illustration.

c. From the small circuit board location illustration shown next to the circur board, find the circuit board's location in the instrument.
d. Find the circuit board in the instrument. Compare it with the circuit board iliustration in the manual to locate the component on the circuit board itself. are looking for by area and shape on the illustration to detemmine its Circuit Number.
Scan the lookup table next to the Circuit Board illustration to find the Circuit Number of the component.

Read the SCHEM NUMBER column next to the component's circuit num-
ber to find the Schematic Diagram number. ber to find the Schematic Diagram number.
3. Locate the Component on the Schematic Diagram
a. Locate the tabbed page that corresponds to the Schematic Diagram num ber. Schematic diagram numbers and names are printed on the front side of the tabs (facing the front of the manual).
b. Locate the Assembly Number in the Component Location lookup table Locate the Assembly Number in the Component Location lookup table
next to the schematic diagram. Scan the CIRCUIT NUMBER column of next to the schematic diagram. Scan the CIRCDO
that table to find the Circuit Number of the component you are looking for in the schematic.
c. In the SCHEM LOCATION column next to the component, read the grid coordinates of the component in the schematic.
d. Using the grid coordinates given, find the component in the schematic diagram.


Locate the Component on the Circuit Board.
a. In the manual, locate the tabbed page that corresponds to Assembly Number the component is on. Assembly numbers and names for circuit boards are on the back side of the tabs.
Using the Circuit Number of the component and its given grid location, find the component in the Circuit Board illustration.
c. From the small circuit board location illustration shown next to the circuit board, find the circuit board's location in the instrumen.
d. Find the circuit board in the instrument. Compare it with the circuit board illustration in the manual to locate the component on the circuit board itself.




Table 9-1 (cont)

| SIGNAL NAME | ORIGINATES ${ }^{\text {a }}$ <br> DIAG/CIR\#(VIA) | GOES TO <br> DIAG/CIR\#(VIA) |
| :---: | :---: | :---: |
| A GATE | 4/U603-11 | 5/VR302 |
| $\overline{\text { A GATE }}$ | 4/U603-14 | 5/VR301 |
| $\overline{\text { A GATE }}$ T | 4/Q604 | 9/U2410-9 |
| A INTEN | 11/U2312-14(R2357) | 7/U1001-12 |
| A INTEN GATE | 4/U602-17 | 7/Q1001 |
| A RAMP | 5/Q312,Q328 | 6/U802-3 |
| A SLOPE | 4/U600-30 | 3/U421-8(R493) |
| A SWP END | 5/U316-15(R378) | 4/U602-8 |
| A TRIG | 3/U421-10(R411) | 4/U602-7 |
| A TRIG LVL | 11/U2304-8 | 3/U421-24(R448) |
| AD COMP | 11/U2306-7 | 8/U2515-12 (R2511) |
| ADDRO | 8/U2512-15(R2560) | 4/U501-1, U602-38(R619), U600-4 |
| ADDR1 | 8/U2512-6(R2561) | 4/U501-2,U602-39(R618), U600-5 |
| ADDR2 | 8/U2512-16(R2562) | 4/U501-3, U600-6 |
| ADDR3 | 8/U2512-5(R2563) | 4/U502-10, U600-7 |
| ATS 0 | 4/U600-31 | 3/U421-13(R490) |
| ATS 1 | 4/U600-32 | 3/U421-12 (R491) |
| ATS 2 | 4/U600-33 | 3/U421-9(R492) |
| AUX DATA | 5/U303-9 | 3/U1103-2(R1162) |
| B DELTA TRIG LVL | 11/U2305-1 | 3/U1106-5 |
| B GATE | 4/U603-9 | 5/VR304 |
| B GATE | 4/U603-15 | 5/VR303 |
| B INTEN | 11/U2312-4(R2356) | 7/U1001-5 |
| B INTEN GATE | 4/U602-18 | 7/Q1004 |
| B RAMP | 5/Q325, Q329 | 6/U802-5 |
| B REF TRIG LVL | 11/U2304-7 | 3/U1106-3 |
| B SLOPE | 4/U600-26 | 3/U431-8(R497) |
| B SWP END | 5/U316-2(R380) | 4/U602-37 |
| B TRIG | 3/U431-10(R415) | 4/U602-34(DL22+) |
| $\overline{\text { B TRIG }}$ | 3/U431-11(R416) | 4/DL22- |
| BEAM FIND | 4/U503-7 | 7/Q2706(R2705) ; 2/U701-21;6/U802-14 |
| BTS 0 | 4/U600-27 | 3/U431-13 (R494) |
| BTS 1 | 4/U600-28 | 3/U431-12(R495) |
| BTS 2 | 4/U600-29 | 3/U431-9 (R496) |
| BW LIMIT | 3/U1103-11 | 4/U502-4;2/U701-22 |
| CH 1 EN | 4/U600-39 | 2/U210-11(R213) |
| CH 1 POS | 11/U2310-14(R2354) | 2/U203-3 |
| CH 1 PRB | 1/R105;C111 | 7/U506-15 |
| CH 1 PREAMP 0 | 1/U172-4 | 2/U210-1 (CR201) |
| CH 1 PREAMP 1 | 1/U171-11 | 2/U210-2 (CR202) |
| CH 1 PREAMP $\mathrm{IN}^{+}$ | 1/U112-8 | 2/U210-7 |
| CH 1 TR + | 2/U210-20 | 3/U421-7,U431-7 |
| CH 1 VAR | 11/U2309-13(R2350) | 2/U210-10(R225) |
| CH 2 EN | 4/U600-38 | 2/U220-11 (R223) |
| CH 2 Invert | 1/U173-6 | 2/U220-12 |
| CH 2 POS | 11/U2310-4(R2353) | 2/U203-5 |
| CH2 PRB | 1/R106,C121 | 7/U506-12 |
| CH 2 PREAMP 0 | 1/U173-5 | 2/U220-1 |


| SIGNAL NAME | ORIGINATES ${ }^{\text {a }}$ <br> DIAG/CIR\#(VIA) | GOES TO <br> DIAG/CIR\#(VIA) |
| :---: | :---: | :---: |
| CH 2 PREAMP 1 | 1/U173-4 | 2/U220-2 |
| CH 2 PREAMP $\mathrm{IN}+$ | 1/U122-8 | 2/U220-7 |
| CH 2 TR + | 2/U220-20 | 3/U421-5,U431-5 |
| CH 2 VAR | 11/U2309-5(R2351) | 2/U220-10(R227) |
| CH 3 EN | 4/U600-37 | 2/U230-11(R233) |
| CH 3 POS | 11/U2311-4(R2327) | 2/U203-10 |
| CH 3 PRB | 1/R107,C131 | 7/U506-1 |
| CH 3 PREAMP 1 | 1/U173-7 | 2/U230-2 |
| CH 3 PREAMP IN + | 1/Q131A(R139), Q131B(R139) | 2/U230-7 |
| CH 3 TR + | 2/U230-20 | 3/U421-3,U431-3 |
| CH 4 EN | 4/U600-36 | 2/U240-11(R243) |
| CH 4 POS | 11/U2310-15(R2326) | 2/U203-12 |
| CH 4 PRB | 1/R108,C151 | 7/U506-5 |
| CH 4 PREAMP 1 | 1/U173-14 | 2/U240-2 |
| CH 4 PREAMP $\mathrm{IN}^{+}$ | 1/Q151A(R159),Q151B(R159) | 2/U240-7 |
| CH 4 TR + | 2/U240-20 | 3/U421-1,U431-1 |
| CLK 1K | 7/U930-7 (R933,R934) | 4/U600-2 |
| CLK 4M | 9/U2417-3 | 11/J2601-16 |
| CLK 8M | 8/U2501-56 | 9/U2409-1 |
| $\overline{\text { DAC INTR }}$ | 11/R2310 | 8/U2515-16(R2554) |
| $\overline{\text { DAC LSB CLK }}$ | 8/U2517-15 | 11/U2301-11 |
| DAC MSB CLK | 8/U2517-14 | 11/U2300-11 |
| DATA BUS | 8/U2514 | $\begin{aligned} & \text { 9/U2401,U2402; 10/U2523,U2524; } \\ & \text { 11/U2300,U2301,U2307,U2313 } \end{aligned}$ |
| DELTA CURSOR | 11/U2304-1 | 9/U2414-2, U2415-2 |
| DELTA DELAY | 11/U2305-7 | 5/U301-12(R330), U313-6(R330) |
| DIG HORIZ POS | 11/U2305-14 | 6/U301-3(R369);2/U702-3(R722) |
| DLY END 0 | 5/U315-15(R388) | 4/U602-36 |
| DLY END 1 | 5/U315-2 (R387) | 4/U602-35 |
| DLY SEL | 4/U600-25 | 5/U301-11;3/U1106-9 |
| FLIC WR | 8/U2518-11 | 4/U602-40 (R647) |
| HDO | 4/U600-24 | 6/U802-8 |
| HD1 | 4/U600-23 | 6/U802-11 |
| HOLDOFF | 11/U2308-5(R2343) | 4/Q600 (R636) |
| + HORIZONTAL OUTPUT | 6/Q805(R819), Q806(R819) | 7/V1-R |
| - HORIZONTAL OUTPUT | 6/Q801 (R802), Q802 (R802) | 7/V1-L |
| $\bar{Z}$ INTEN GATE | 4/U602-19 | 7/Q1003 |
| LED ANODE CLK | 8/U2501-27 | 10/U2523-11(R2528) |
| LED CATH CLK | 8/U2501-25 | 10/U2524-11 (R2529) |
| LINE TRIG | 12/T2206 | 3/U1106-2 |
| MAG | 3/U1103-6 | 6/U802-6 |
| MAIN BD MUX | 7/U506-3 (R503) | 11/U2309-12 (R2352) |
| $\overline{M B C N T L ~ W R ~}$ | 8/U2518-15(R2564) | 4/U501-4 |
| MB DATA | 8/U2515-11(R2555) | 4/U600-9,U602-12 (R620), U502-2 |
| MB RETURN | 4/U502-12 | 8/U2515-14(R2509) |
| POT5 | 11/U2313-5 | 7/U506-11(R508) |
| POT6 | 11/U2313-19 | 7/U506-10(R510) |
| POT7 | 11/U2313-2 | 7/U506-9(R512) |

SIGNAL NAME

POT MUX CLK REF CURSOR REF DELAY RO BLANK RO BUF RD RO BUF WR RO CH 1 POS RO CH 2 POS E RO CH 3 POS E RO CH 4 POS RO FREEZE RO HORIZ RO NTEN RO INTEN GATE
O
$\overline{R O}$ REQ
RO VERT
RO VERT
RO VERT EN
SLIC RD
$\frac{\text { SLIC WR }}{\text { SRO CLK }}$
$\frac{\text { SR1 CLK }}{\text { SR1 CKKTTL }}$
SR DATA
SW BD DATA
SW BD SR LOA
SW BD SR SHIF
SYS RESE
TB CAL
TIME VAR
TRACE SEP
trace sep en
TRIG CLK
VERT COMP
VERT COMP EN

+ VERTICAL O
- VERTICAL
volt cal
VOLT CAL 1
Volt cal 2
$\overline{X Y}$
X AXIS
ZERO HYST
- 

| SIGNAL NAME | ORIGINATES ${ }^{\text {a }}$ <br> DIAG/CIR\#(VIA) | GOES TO DIAG/CIR\#(VIA) |
| :---: | :---: | :---: |
| CH 2 PREAMP 1 | 1/U173-4 | 2/U220-2 |
| CH 2 PREAMP $\mathrm{IN}+$ | 1/U122-8 | 2/U220-7 |
| CH 2 TR + | 2/U220-20 | 3/U421-5,U431-5 |
| CH 2 VAR | 11/U2309-5(R2351) | 2/U220-10(R227) |
| CH 3 EN | 4/U600-37 | 2/U230-11(R233) |
| CH 3 POS | 11/U2311-4(R2327) | 2/U203-10 |
| CH 3 PRB | 1/R107,C131 | 7/U506-1 |
| CH 3 PREAMP 1 | 1/U173-7 | 2/U230-2 |
| CH 3 PREAMP $\mathrm{IN}^{+}$ | 1/Q131A(R139),Q131B(R139) | 2/U230-7 |
| CH 3 TR + | 2/U230-20 | 3/U421-3,U431-3 |
| CH 4 EN | 4/U600-36 | 2/U240-11(R243) |
| CH 4 POS | 11/U2310-15(R2326) | 2/U203-12 |
| CH 4 PRB | 1/R108,C151 | 7/U506-5 |
| CH 4 PREAMP 1 | 1/U173-14 | 2/U240-2 |
| CH 4 PREAMP $\mathrm{IN}+$ | 1/Q151A(R159),Q151B(R159) | 2/U240-7 |
| CH 4 TR + | 2/U240-20 | 3/U421-1, U431-1 |
| CLK 1 K | 7/U930-7(R933,R934) | 4/U600-2 |
| CLK 4M | 9/U2417-3 | 11/J2601-16 |
| CLK 8M | 8/U2501-56 | 9/U2409-1 |
| $\overline{\text { DAC INTR }}$ | 11/R2310 | 8/U2515-16(R2554) |
| $\overline{\text { DAC LSB CLK }}$ | 8/U2517-15 | 11/U2301-11 |
| DAC MSB CLK | 8/U2517-14 | 11/U2300-11 |
| dATA BUS | 8/U2514 | 9/U2401, U2402;10/U2523,U2524; <br> 11/U2300,U2301,U2307,U2313 |
| delta Cursor | 11/U2304-1 | 9/U2414-2, U2415-2 |
| DELTA DELAY | 11/U2305-7 | 5/U301-12(R330), U313-6(R330) |
| DIG HORIZ POS | 11/U2305-14 | 6/U301-3(R369);2/U702-3(R722) |
| DLY END 0 | 5/U315-15(R388) | 4/U602-36 |
| DLY END 1 | 5/U315-2 (R387) | 4/U602-35 |
| DLY SEL | 4/U600-25 | 5/U301-11;3/U1106-9 |
| FLIC WR | 8/U2518-11 | 4/U602-40 (R647) |
| HDO | 4/U600-24 | 6/U802-8 |
| HD1 | 4/U600-23 | 6/U802-11 |
| HOLDOFF | 11/U2308-5 (R2343) | 4/Q600 (R636) |
| + HORIZONTAL OUTPUT | 6/Q805(R819), Q806(R819) | 7/V1-R |
| - HORIZONTAL OUTPUT | 6/Q801 (R802), Q802 (R802) | 7/V1-L |
| 1 Z INTEN GATE | 4/U602-19 | 7/Q1003 |
| LED ANODE CLK | 8/U2501-27 | 10/U2523-11 (R2528) |
| LED CATH CLK | 8/U2501-25 | 10/U2524-11 (R2529) |
| LINE TRIG | 12/T2206 | 3/U1106-2 |
| MAG | 3/U1103-6 | 6/U802-6 |
| MAIN BD MUX | 7/U506-3(R503) | 11/U2309-12(R2352) |
| $\overline{M B C N T L ~ W R ~}$ | 8/U2518-15(R2564) | 4/U501-4 |
| MB DATA | 8/U2515-11 (R2555) | 4/U600-9, U602-12(R620), U502-2 |
| mb Return | 4/U502-12 | 8/U2515-14(R2509) |
| POT5 | 11/U2313-5 | 7/U506-11 (R508) |
| POT6 | 11/U2313-19 | 7/U506-10(R510) |
| POT7 | 11/U2313-2 | 7/U506-9(R512) |


| SIGNAL NAME | ORIGINATES ${ }^{\text {a }}$ <br> DIAG/CIR\# (VIA) | GOES TO DIAG/CIR\# (VIA) |
| :---: | :---: | :---: |
| $\overline{\text { POT MUX CLK }}$ | 8/U2517-13 | 11/U2313-11 |
| REF CURSOR | 11/U2304-14 | 9/U2414-5,U2415-5 |
| REF DELAY | 11/U2305-8 | 5/U301-13(R329) |
| $\overline{\text { RO BLANK }}$ | 9/U2410-16(R2419) | 4/U600-12 |
| RO BUF RD | 8/U2501-29(R2515) | 9/U2402-1 |
| $\overline{\text { RO BUF WR }}$ | 8/U2501-28(R2516) | 9/U2417-9, U2401-11 |
| RO CH 1 POS EN | 9/U2403-19 | 2/U202-10 |
| ROCH 2 POS EN | 9/U2403-2 | 2/U202-11 |
| ROCH 3 POS EN | 9/U2403-5 | 2/U201-9 |
| ROCH 4 POS EN | 9/U2403-6 | 2/U201-10 |
| RO FREEZE | 1/U173-11 | 4/U502-5,U503-3 |
| RO HORIZ | 9/U2416-8 | 6/U802-1 (R807,R823) |
| RO INTEN | 11/U2312-15(R2355) | 7/U1001-10 |
| RO INTEN GATE | 4/U602-20 | 7/Q1002 |
| RO INTR | 9/U2417-11 | 8/U2515-15(R2508) |
| $\overline{\text { RO REQ }}$ | 9/U2410-14(R2420) | 4/U503-2,U600-11 |
| RO TR SEP EN | 9/U2403-9 | 2/U201-11 |
| RO VERT | 9/U2416-14 | 2/U202-4(R207,R205) |
| RO VERT EN | 4/U503-4 | 2/U202-9(R215) |
| $\overline{\text { SLIC RD }}$ | 8/U2503-8 | 4/U600-8 |
| SLIC WR | 8/U2518-12 | 4/U600-3 |
| SRO CLK | 4/U606-12 | 1/U171-3,U172-3,Q171,U173-3(R176) |
| SR1 CLK | 4/U606-4 | 5/U302-3, U303-3 |
| SR1 CLK TTL | 4/U501-13 | 3/U1103-3 |
| SR DATA | 4/U606-6 | U171-2;5/U302-2 |
| SW BD DATA | 10/U2002-9 | 8/U2515-13(R2510) |
| SW BD SR LOAD | 8/U2518-14 | 10/U2001-1,U2002-1 |
| SW BD SR SHIFT | 8/U2518-13 | 10/U2001-2, U2002-2 |
| SYS RESET | 8/U2506-8 | 9/U2400-28 |
| TB CAL | 8/U2501-22 | 3/U1106-1 (R1170) |
| TIME VAR | 11/U2308-12 (R2344) | 5/U309-5 (R309,CR301) |
| TRACE SEP | 11/U2311-15(R2323) | 2/U801-5 |
| TRACE SEP EN | 4/U600-22 | 2/U201-11(R209) |
| TRIG CLK | 4/U600-19 | 3/U421-2(R498) |
| VERT COMP | 2/U702-7 | 4/U502-6 |
| VERT COMP EN | 3/U1103-7 | 2/Q703 |
| + VERTICAL OUTPUT | 2/Q701 (R731,L703) | 7/V1-TOP |
| - Vertical output | 2/Q702(R732,L704) | 7/V1-BOT |
| volt cal | 7/U931-3 | 1/R114,R124 |
| VOLT CAL 0 | 5/U303-11 | 7/U931-11(R924) |
| VOLT CAL 1 | 5/U303-12 | 7/U931-10(R923) |
| VOLT CAL 2 | 5/U303-13 | 7/U931-9(R922) |
| $\overline{X Y}$ | 3/U1103-12 | 6/U301-9 |
| X AXIS | 3/U421-19(R426,L426) | 6/U802-7 (R836,R827) |
| ZERO HYST | 1/U173-13 | 3/Q480 (R484) |

${ }^{\mathrm{a}}$ Signals that begin and end on the same diagram are not listed in this table.



## VOLTAGENAVEFORM SETUP CONDITIONS

## WAVEFORMS

Test waveforms are shown on a page just before the schematic diagram to which they apply. Normal control settings for the test oscilloscope are given in the readouts shown in each waveform illustration Unless otherwise indicated near the waveform, setup conditions for the oscilloscope under test are as follows:

1. Set up the 22461 Y or 2246 Mod A front-panel controls as follows:

VERTICAL MODE
CH 1 COUPLING
CH 1 VOLTS/DIV
VERTICAL POSITION Controls SCOPE BW
HORIZONTAL MODE
A/B SELECT
SEC/DIV
Trigger LEVEL
HOLDOFF
SLOPE
rigger MODE
Trigger SOURCE
Trigger COUPLING
MEASUREMENTS
MENU Displays
A INTEN
READOUT
FOCUS
SCALE ILLUM

CH 1 (other
channels off) chan
DC

2 o'clock On
A

A
A
0.1 ms

12 o'clock
min (CCW)
$\min$ (CCW)
-
auto level
VERT
DC
OFF
10 o'clock
$120^{\prime}$ 'clock
for well de-
fined display fully CCW
2. Connect the front panel PROBE ADJUST output to the channel 1 input connector.
3. For all waveforms, except those obtained from the Low-Voltage Power Supply, connect the test oscilloscope probe ground wire to the chassis When obtaining waveforms from the power
supply, first connect the power cord of the $22461 Y$ or 2246 Mod A under test through an isolation transformer, then connect probe ground wire to ground "P' (rear side of R2256) See Figure 9-12 to locate ground " $P$ "'

## WARNING

To avoid electric shock and instrument damage, always connect the power cord of the instrument under test through an isolation transformer when viewing waveforms or measuring voltages in the low-voltage power supply.

## DC VOLTAGES

Dc voltages shown the schematic diagrams are typi al of a normally operating instrument. Voltages are eferenced to chassis ground, except in the isolated portion of the Low Voltage Power Supply where they are referenced to ground "P" (at R2256 as shown in Figure 9-12). Make sure that the DMM leads are floating (isolated from chassis ground) when measuring voltages in this section.

## TEST EQUIPMENT

The following test equipment is recommended for obtaining waveforms and voltages from the 2246 1Y and 2246 Mod A Oscilloscopes. Other similar equipment types can also be used.

1. Test Oscilloscope with 10X probe(s)TEKTRONIX 2246 1Y or 2246 Mod A.
2. Digital Voltmeter-TEKTRONIX DM 501A
3. Power-Line Isolation Transformer-Tektronix Part No. 006-5953-00.

## 22461 Y and 2246 Mod A Service

|  |
| :---: |
|  |  |

## ( $)^{\text {Static Sensitive Devices }}$ Se Mindenarce Section



| OTHER PARTS |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT NUMBER | SCHEM NUMBER | Circuit NUMBER | SCHEM NUMER | Circuit nUMBER | $\underset{\text { NUUBEER }}{\text { SCHEM }}$ |
| ${ }^{\text {в25 }}$ <br> fl2201 | 12 12 | $\begin{gathered} 116 \\ { }_{32726} \end{gathered}$ P25 | $\begin{aligned} & 7 \\ & 12 \end{aligned}$ | $\begin{gathered} \mathrm{P}_{{ }_{P 27}} \end{gathered}$ $\mathrm{v}_{1}$ | $7$ |

[^11]\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline \multicolumn{12}{|c|}{A10 - MAIN BOARD (SN B715000 AND ABOVE, 2246 1Y SN B120000 AND ABOVE, 2246 MOD A)} \& \multicolumn{12}{|c|}{A10 - MAIN BOARD (cont) (SN B715000 AND ABOVE, 2246 1Y SN B120000 AND ABOVE, 2246 MOD A)} \\
\hline (tarcuit \& SCHEM
NUMEER \&  \& \(\underbrace{}_{\substack{\text { Circuit } \\ \text { NUMER }}}\) \& SCHEM \&  \&  \& SCHEM
NUMEER \& \({ }_{\substack{\text { Boaro } \\ \text { LOCATOO }}}\) \& \begin{tabular}{|c} 
CliRCUIT \\
NUMBER \\
\hline
\end{tabular} \& SCHEM \& \[
\begin{aligned}
\& \text { BOARD } \\
\& \text { BCATION }
\end{aligned}
\] \& CIRCUIT
NUMEER \& SCHEM
NUMER \& \({ }_{\text {L }}^{\text {BOARA }}\) \& \(\underset{\substack{\text { CiRCuIT } \\ \text { NUMEER }}}{\text { a }}\) \& SCHEM
NUMEER \& \(\xrightarrow{\text { BOARD }}\) \& \(\underset{\substack{\text { CiRCUIT } \\ \text { NUMER }}}{ }\) \& SCHEM
NUMEER \& \({ }_{\text {L }}^{\text {LOCARON }}\) \& \begin{tabular}{c} 
Clircuit \\
NUMEER \\
\hline
\end{tabular} \& SCHEM
NUMER \& \({ }_{\text {cosen }}^{\text {Boato }}\) \\
\hline \({ }_{\text {ati }}^{\text {AT117 }}\) \& ' \& \({ }_{48}^{68}\) \& \(\xrightarrow{\text { c223 }}\) \& \({ }_{13}^{2}\) \& \({ }_{30}^{4 E}\) \& C455
C462 \& \(3_{3}^{3}\) \& \({ }_{19}^{16}\) \& \(c880\) \& \({ }_{13}\) \& \(\stackrel{8}{88}\) \& CR802 \& \({ }^{6}\) \& 4 M \& \({ }^{\text {P9 }}\) \& \& \({ }^{8 N}\) \& 0802 \& \& \& \({ }^{\text {R156 }}\) \& \& \\
\hline \({ }^{\text {AT127 }}\) \& 1 \& \({ }^{48}\) \& \(\underset{\substack{\mathrm{c} 224 \\ \mathrm{c} 225}}{\substack{\text { a }}}\) \& 13
13 \& \({ }_{30}^{30}\) \& \({ }_{\substack{\text { Ca62 } \\ \text { C463 }}}^{\text {ces }}\) \& 3
3 \& \({ }_{\substack{1 / 8 \\ 1 / 8}}^{\text {c }}\) \&  \& 13
13 \& \({ }_{88}^{98}\) \&  \& \% \({ }_{6}^{6}\) \& \({ }_{8}^{81}\) \& ¢99 \& 7 \& ¢ \({ }_{\text {gn }}^{\text {gn }}\) \& \begin{tabular}{l} 
68023 \\
0803 \\
0804 \\
\hline
\end{tabular} \& 6 \& \({ }_{7}^{7,}\) \& \(\underset{\substack{\text { R156 } \\ \text { R157 } \\ \text { R158 }}}{ }\) \& \& 10
10
10 \\
\hline c1
Cl

212 \& 1 \& ${ }_{4 c}^{6 C}$ \& c228
$\substack{\text { c229 }}$
c 223 \& $\stackrel{13}{2}$ \& ${ }_{40}^{1006}$ \& C474
C475
cate \& 3
3
13 \& $\stackrel{\text { r }}{1 / 8}$ \& ( \& 13
13
13 \& ${ }_{98}^{89}$ \&  \& 7 \& ${ }_{7}^{81}$ \& ${ }_{\substack{\text { P17 } \\ \hline 18 \\ \hline 18}}$ \& ${ }^{6}$ \& ${ }_{8 K}{ }_{8}$ \& ${ }^{0804}$ \& ${ }_{6}^{6}$ \& ${ }_{8 K}^{7 J}$ \& ${ }_{\substack{\text { R1588 } \\ \text { R159 }}}$ \& ${ }_{1}^{2}$ \&  <br>
\hline c10 \& 1 \& ${ }_{68}^{48}$ \& ( \& ${ }_{2}^{13}$ \& ${ }_{\substack{4 \\ 20}}^{40}$ \&  \& ${ }_{3}^{13}$ \& ${ }_{\text {1/ }}^{1 / 2}$ \& cos \& ${ }_{7}^{13}$ \& ${ }_{108}^{98}$ \& ${ }_{\text {cher }}^{\text {CRa36 }}$ \& 7 \& ${ }_{51}^{7 \mathrm{~A}}$ \& P190 \& 2 \& $\stackrel{10 \mathrm{H}}{9}$ \& ${ }^{0806}$ \& ${ }_{6}$ \& ${ }_{88}^{8 K}$ \& Rentici \& ${ }_{1}^{2}$ \& 18 <br>
\hline ${ }_{\text {c }}^{\text {c20 }}$ \& 1 \& ${ }_{48}^{68}$ \& cere \& $\stackrel{1}{13}$ \& ${ }_{30}^{2 E}$ \& ${ }_{\substack{\text { ca77 } \\ 0478}}$ \& ${ }_{3}^{3}$ \& ${ }_{15}^{1 F}$ \&  \& 7 \& ${ }_{7}^{78}$ \& crer CR1020 \& 7 \& ${ }_{\substack{5 L \\ 8 L}}$ \&  \& ${ }_{7}^{13}$ \& 100
100

100 \& (08099 \& ${ }_{6}^{6}$ \& ${ }_{7 \%}^{8 \%}$ \&  \& 1 \& | 18 |
| :---: |
| 18 |
| 18 |
| 18 | <br>

\hline C21
C101
cid \& 1 \& ${ }_{68}^{58}$ \&  \& ${ }_{2}^{13}$ \& 20

106 \& | Cata |
| :--- |
| $\substack{\text { C480 } \\ \text { cas }}$ | \& 13

13
13 \& ${ }_{3}^{15}$ \& ( \& 7 \& ${ }_{8 \times}$ \& CRH103 \& 7 \& ${ }^{81}$ \& P2302
P2304
P2502 \& 7 \& ${ }_{1}^{100}$ \& ${ }^{0810}$ \& ${ }_{6}^{6}$ \& ${ }_{7}^{7 H}$ \& ${ }_{\substack{\text { R171 } \\ \text { R175 }}}$ \& 1 \& ${ }_{38}^{38}$ <br>
\hline c101
C102

0 \& 1 \& ${ }_{4}^{6 A}$ \& | C238 |
| :--- |
| $\substack{\text { c239 }}$ |
| 1829 | \& ${ }_{13}^{2}$ \& ${ }_{30}^{106}$ \& C481

$\substack{488 \\ \text { C48 }}$ \& 13

13 \& ${ }_{\substack{2 F \\ 2 F}}^{5(5)}$ \&  \& 7 \& ${ }_{7}^{8 \times}$ \& CR1005 \& 7 \& $\stackrel{3}{9}$ \& ${ }_{\text {P2502 }}$ \& ${ }_{4}^{13}$ \& \% \& | 0812 |
| :--- |
| 0905 |
| 095 | \& ${ }_{7}^{6}$ \& ${ }_{108}^{20}$ \& ${ }_{\substack{\text { R176 } \\ \text { R177 }}}$ \& 1 \& ${ }_{30}^{30}$ <br>

\hline  \& 1 \& ${ }_{1}^{3 A}$ \& (c242 \& ${ }_{2}^{2}$ \& ${ }^{1 \mathrm{E}}$ \& ${ }_{\text {Ca84 }}^{\text {Ca83 }}$ \& $3_{3}^{3}$ \& ${ }_{3 G}^{2 F}$ \&  \& 13
13 \& ${ }_{8 L}^{7 M}$ \&  \& 7 \& ${ }_{8 \times} \mathrm{MN}^{\text {a }}$ \& 01318 \& \& \& -0907 \& 7 \& ${ }_{1} 10 \mathrm{~A}$ \& ${ }_{\substack{\text { R178 } \\ R \\ R 178 \\ \hline 178 \\ \hline}}$ \& 1 \& ${ }_{3}^{3 C}$ <br>

\hline | C105 |
| :--- |
| $\substack{\text { c108 } \\ \text { C10 }}$ | \& 1 \& ${ }_{68}^{68}$ \&  \& 13

13
13 \& ${ }_{20}^{20}$ \&  \& 3
3

3 \& cois \& | c11006 |
| :--- |
| $\mathrm{Cl101}$ | \& 13

13

13 \& | 81 |
| :--- |
| 3 | \& CR2723 \& 7 \& ¢ ${ }_{\text {8N }}^{8 \mathrm{~N}}$ \& ${ }_{\substack{0131 A \\ 0 \\ 0 \\ 01318}}$ \& 1 \& ${ }_{28}^{28}$ \& ${ }_{\substack{0908 \\ 0.000}}$ \& 7 \& ${ }_{44}^{104}$ \& ${ }_{\substack{\text { R178 } \\ \text { R180 }}}^{\text {R17 }}$ \& 1 \&  <br>

\hline ${ }_{\text {ckior }}$ \& 1 \& ${ }_{5 B}^{6 A}$ \& - \& ${ }_{2}$ \& ${ }_{1}^{20}$ \& ${ }_{\substack{\text { C488 } \\ \text { C488 }}}^{\text {cid }}$ \& | 3 |
| :---: |
| 3 | \& ${ }_{26}^{3 F}$ \&  \& ${ }_{3}^{13}$ \& ${ }_{4}^{3 G}$ \& CR2705 \& 7 \& ${ }_{7 L}^{6 M}$ \&  \& 1 \& ${ }_{18}^{18}$ \& (0, $\begin{aligned} & 01002 \\ & 0 \\ & 0\end{aligned}$ \& 7 \& ${ }_{4}^{4 L}$ \& ¢, \& 13 \& ${ }_{60}^{18}$ <br>

\hline ciob
C111 \& 1 \& ${ }_{10 \mathrm{C}}^{4 \mathrm{C}}$ \& C249 \& ${ }_{2}^{13}$ \& ${ }_{46}^{20}$ \& ${ }_{\text {C489 }} \mathbf{C 4 9 9}$ \& ${ }^{3}$ \& $\underset{\substack{36 \\ \\ 2 \mathrm{~F}}}{ }$ \& C1104 \& ${ }_{3}^{13}$ \& ${ }_{4}^{4+}$ \& core \& 7 \& 111 \& ${ }^{0} 0$ \& 1 \& ${ }^{18}$ \& ${ }^{0}$ \& 7 \& ${ }_{4}^{4 L}$ \&  \& 1 \& $6{ }_{6}^{60}$ <br>
\hline  \& 1 \& 68
54 \&  \& ${ }_{2}^{13}$ \& ${ }_{\substack{\text { SF } \\ 96}}$ \& C4912
C492
0 \& 2 \& ${ }_{\substack{2 F \\ 2 F}}^{2 F}$ \& C1105
C1106
C10 \& 3 \& ${ }_{4}^{56}$ \& CR2714 \& 7 \& ${ }_{9}^{112}$ \& 0181
0182
0
0 \& \& ${ }_{40}^{60}$ \& -01005 \& 7 \& ${ }_{5}^{7}$ \& ¢, \& 1 \& 30
56 <br>
\hline ${ }_{C} 114$ \& 1 \& 50 \& C271 \& 2 \& ${ }_{4}^{96}$ \& Ca93 \& ${ }_{2}^{2}$ \& ${ }_{2 \sim}^{2 E}$ \&  \& ${ }_{13}$ \& ${ }_{49}^{46}$ \& CR2716 \& 7 \& ${ }_{9 \mathrm{~m}}^{9 \mathrm{M}}$ \& -0250 \& ${ }_{2}^{2}$ \& ${ }_{4 \mathrm{4E}}^{4 \mathrm{E}}$ \& 01102
01103
0 \& 3 \& ${ }_{51}^{50}$ \& ${ }_{\substack{\text { R2202 } \\ \text { R203 }}}$ \& 2 \& ${ }_{56}^{56}$ <br>
\hline C121
$\substack{122 \\ C 122}$ \& 1 \& ${ }_{5 A}^{100}$ \&  \& 2 \& ${ }_{48}^{4 F}$ \& case
case
casid \& 3
13
13 \& ${ }_{4}^{2 \mathrm{a}}$ \& cilit \& ${ }^{13}$ \& ${ }_{36}{ }^{46}$ \& ${ }_{\text {CR2718 }}$ \& 7 \& ${ }_{81}{ }^{\text {81 }}$ \& 0251
0252
0253
0253 \& 2 \& ${ }_{4 \times}^{4 F}$ \& 01103
01104
0

0 1 \& 3 \& ${ }_{5}^{50}$ \&  \& \& | 56 |
| :--- |
| 56 |
| 56 | <br>

\hline  \& 1 \& ${ }_{4}^{54}$ \&  \& 2 \& ${ }_{4}^{4 F}$ \& C501 \& 13

13 \& ${ }_{4}^{4 M}$ \& C1111 \& ${ }_{3}^{3}$ \& ${ }_{4 j}^{4 H}$ \& ${ }_{0} \mathrm{L21}$ \& 2 \& ${ }^{35}$ \& -0233 \& ${ }_{2}^{2}$ \& ${ }_{68}^{4 F}$ \& | 01105 |
| :--- |
| 0 |
| 01106 |
| 0 | \& ${ }_{3}^{3}$ \& 515 \& ${ }_{\substack{\text { R205 } \\ \text { R206 }}}$ \& 2 \& ${ }_{56}^{50}$ <br>

\hline | c124 |
| :--- |
| 1225 |
| $C 125$ | \& 1 \& ${ }^{4 C}$ \&  \& $\stackrel{2}{13}$ \& ${ }_{4}^{4 F}$ \& | C5503 |
| :--- |
| C505 | \& 13

13 \& $\stackrel{4 \mathrm{c}}{9 \mathrm{C}}$ \& C1130
C1143

C1/ \& | 3 |
| :---: |
| 3 |
| 3 | \& ${ }_{4}^{46}$ \& $c021022$ \& 3 \& 10 K

$2 H$ \& | 0285 |
| :--- |
| 0301 |
| 030 | \& 2 \& ${ }_{\text {ST }}^{56}$ \&  \& 7 \& ${ }_{7}{ }_{7}$ \& ${ }_{\substack{\text { R2007 } \\ \text { R208 } \\ \text { R20 }}}$ \& 2

2
13 \&  <br>

\hline | c126 |
| :--- |
| $\substack{123 \\ C 131 \\ \hline}$ | \& 1 \& 1100 \& (\%283 \& 13

13 \& ${ }_{6 F}^{6 F}$ \& | C5605 |
| :--- |
| C600 |
| 600 | \& ${ }_{4}^{13}$ \& ${ }_{3}^{9 \mathrm{Na}}$ \& C1143

C1154
C15 \& 3 \& ${ }_{46}^{46}$ \& ${ }^{0122}$ \& ${ }_{3}$ \& ${ }_{3 k}^{2 H}$ \& O301
0302
0303 \& 5
5
5 \& ${ }_{8 \times}^{7 c}$ \& O2702
0.703
0704 \& 7 \& ${ }_{7} 7 \mathrm{M}$ \& Rers \& ${ }_{2}^{13}$ \& ${ }_{19}^{114}$ <br>
\hline C132
$\substack{133 \\ C 133}$
Cra \& 1 \& ${ }_{28}^{2 \mathrm{~A}}$ \& c2388

$\substack{\text { c230 } \\ \text { c30 }}$ \& ${ }^{13}$ \& ${ }_{\substack{3 F \\ 8 ⿷ 匚}}^{\text {c }}$ \& ${ }_{6602}$ \& ${ }_{4}^{4}$ \& ${ }_{4 L}^{3 N}$ \& | C1155 |
| :--- |
| $\substack{\text { c1158 }}$ | \& $\stackrel{3}{13}$ \& ${ }_{5 L}^{4 \mathrm{G}}$ \& ${ }^{\text {DS901 }}$ \& 7 \& ${ }^{8 B}$ \& ${ }_{0}^{0303}$ \& 5 \& ${ }_{8 F}^{8 F}$ \& -02704 \& 7 \& ${ }_{7}^{6 N}$ \& ${ }_{\substack{\text { R210 } \\ R 211}}$ \& 2 \& ${ }_{6 E}^{6 E}$ <br>

\hline  \& 1 \& ${ }_{28}^{28}$ \& C301 \& 5 \& ${ }_{9 F}^{8 F}$ \& C6033
$C 604$
C60 \& ${ }_{13}^{4}$ \& ${ }_{3 \mathrm{c}}^{3 \mathrm{M}}$ \& C1159
$\substack{1150 \\ \text { C15 }}$ \& 13
13 \&  \& - ${ }_{\text {DS902 }}$ \& 7 \& ${ }_{118}^{98}$ \& -0305 \& 5 \& TE \& (02706 \& 7 \& ${ }_{6}^{7 M}$ \& ${ }_{\substack{\text { R2212 } \\ \text { R213 }}}$ \& 2 \& $2 L$
$2 \times$
$2 \times$ <br>
\hline C135
$\substack{\text { C136 } \\ \text { C136 }}$ \& ${ }_{13}^{13}$ \& ${ }_{28}^{28}$ \& C303 \& 5

13 \& ${ }_{70}^{80}$ \& | C604 |
| :---: |
| $\substack{6605 \\ \hline 609}$ | \& 13

13 \& | $3 \times$ |
| :--- |
| $2 \times$ | \& C1160

C11250
C125 \& ${ }_{2}^{13}$ \&  \& Sssor
DS5701
DS2702 \& 7 \& ${ }_{90}$ \& - 03007 \& 5 \& ${ }_{80}^{70}$ \&  \& 7 \& ${ }_{6}^{6 M}$ \& ${ }_{\substack{\text { R213 } \\ \text { R214 }}}$ \& ${ }_{2}^{2}$ \& ${ }_{6 \mathrm{H}}^{2 \mathrm{H}}$ <br>

\hline | C137 |
| :---: |
| $\substack{1338 \\ \mathrm{C} 138 \\ \hline}$ | \& \& ${ }_{2}^{20}$ \& (c309 \& ${ }_{5}$ \& 90 \& ${ }_{\substack{6606 \\ C 607}}^{604}$ \& ${ }_{4}^{13}$ \& ${ }_{3}^{3 L}$ \& C1251 \& ${ }_{13}$ \& $\stackrel{5}{7 \times}$ \& (iss720 \& 7. \& ${ }_{99} 9$ \& 0308

0309 \& 5
5 \& ${ }_{8 F}^{8 F}$ \& -02709 \& 7 \& ${ }_{10}^{6 L}$ \& ${ }_{\substack{\text { R215 } \\ \text { R216 }}}$ \& ${ }_{2}^{2}$ \& ${ }_{86}^{6 N}$ <br>

\hline  \& 2 \& ${ }_{20}^{2 C}$ \& ${ }_{\substack{\text { C306 } \\ \text { C307 }}}$ \& 5 \& ${ }_{7}^{7 C}$ \& cicor \& ${ }_{4}^{4}$ \& ${ }_{3}^{34}$ \& | c2701 |
| :--- |
| $\substack{\text { c2702 } \\ \\ \\ \hline 2303 \\ \hline}$ | \& 13

13 \&  \& ${ }^{\text {DS2704 }}$ \& 7 \& $9{ }_{9}$ \&  \& 5
5

5 \&  \& \begin{tabular}{l}
O2712 <br>
\hline 0713 <br>
02713

 \& 7 \& 100 \& ${ }_{\substack{\text { R2, } \\ \text { R217 } \\ \text { R218 }}}^{\text {R218 }}$ \& 2 \& 

86 <br>
$\substack{86 \\
\hline 5 \\
\hline 5 \\
\hline}$
\end{tabular} <br>

\hline  \& ${ }_{1}^{13}$ \& 20
100 \& C3078 \& 5 \& ${ }_{1}^{7}$ \& C6099
$\substack{660}$ \& ${ }_{13}^{13}$ \& ${ }_{2 L}^{2 J}$ \& c2703
c2704
cras \& 7 \& ${ }_{7}^{7 M}$ \& ${ }^{11}$ \& 1 \& ${ }_{5}^{6 A}$ \& ${ }_{\substack{0311 \\ 03108}}$ \& 5 \& ${ }_{\substack{8 E \\ 8 \%}}^{8 \times}$ \& - 02715 \& 7 \& ${ }_{6 M}^{114}$ \& ¢ $\begin{gathered}\text { R2188 } \\ \text { R219 }\end{gathered}$ \& 2 \& ${ }_{50}^{5 E}$ <br>

\hline | C152 |
| :--- |
| $\substack{\text { c152 } \\ \mathrm{C} 153 \\ \hline}$ | \& 1 \& 28

18 \&  \& | 5 |
| :--- |
|  | \& TE \& c611

66612 \& ${ }_{4}^{4}$ \& ${ }_{3 \mathrm{4K}}^{4 \mathrm{~K}}$ \& C2705
C 2706 \& 7 \& ${ }_{60} \mathrm{on}_{0}$ \& ${ }^{312}$ \& \& ${ }_{3 \text { 3A }}^{5 A}$ \& ${ }^{0312}$ \& 5
5 \& ${ }_{9}^{8 E}$ \& \& 1 \& \& ${ }_{\substack{\text { R220 } \\ \text { R221 }}}$ \& 2 \& ${ }_{4}^{4 \mathrm{E}}$ <br>
\hline c153
C154
0

0155 \& \& ${ }_{18}^{18}$ \& c399 \& ${ }^{13}$ \& ${ }_{\text {7 }}^{\text {7 }}$ \& c6is \& ${ }_{4}^{13}$ \& \begin{tabular}{l}
1/ <br>
\hline 1 M <br>
\hline

 \& 

cerer <br>
$\substack{\text { c2707 } \\
\mathrm{c} 2708 \\
\hline}$
\end{tabular} \& 7 \& $\stackrel{7}{7 N}$ \&  \& 1 \& ${ }_{7}^{1 / A}$ \& - 0315 \& 5 \& ${ }_{9}^{9 E}$ \& R12

R13
R22 \& 1 \& $\underset{\substack{60}}{5 c}$ \& ${ }_{\substack { \text { R222 } \\ \begin{subarray}{c}{\text { R222 } \\ \text { R223 }{ \text { R222 } \\ \begin{subarray} { c } { \text { R222 } \\ \text { R223 } } }\end{subarray}}$ \& 2 \&  <br>
\hline C155 \& 13
13 \& ${ }_{18}^{28}$ \& c311 \& 5
5 \& $\xrightarrow{7 \%}$ \& C614

$\substack{6636 \\ C 685}$ \& ${ }_{4}^{4}$ \& ${ }_{6}^{5 \mathrm{~N}}$ \& | c2708 |
| :--- |
| c2709 | \& 7 \& $\underset{7 M}{7 N}$ \& (3127 \& 7 \& 78

$\substack{118 \\ 50}$ \& O316
$\substack{0317 \\ 0318}$
0318 \& 5 \& $\underset{9}{9 \%}$ \& ${ }_{\text {R23 }}^{\text {R23 }}$ \& 1 \& ${ }_{4}^{5 C}$ \& ${ }_{\substack{\text { R223 } \\ \text { R224 }}}$ \& ${ }_{2}^{2}$ \& ${ }_{6}^{2 \mathrm{H}}$ <br>
\hline  \& 1 \& ${ }^{1 c}$ \& (c313 \& 5
5 \& ${ }_{\text {¢F }}^{\text {9E }}$ \& ${ }^{6666}$ \& ${ }_{13}^{4}$ \& 4 M
100 \&  \& 7 \& ${ }_{8 \times}^{7 N}$ \& \& 13 \& \& -0318 \& 5 \& ${ }_{9}^{10 \mathrm{E}}$ \& R101
R102 \& 1 \& ${ }_{7 A}^{7 A}$ \& ${ }_{\substack{\text { R2225 } \\ \text { R226 }}}$ \& ${ }_{13}$ \& ${ }_{6 \mathrm{c}}^{8 \mathrm{E}}$ <br>
\hline crisb
C159
$\mathrm{Cl171}$

C 171 \& ${ }_{13}^{2}$ \& \begin{tabular}{|c}
16 <br>
18 <br>
18 <br>
\hline 8

 \&  \& 

5 <br>
5 <br>
\hline 13
\end{tabular} \& ${ }_{\substack{\text { ¢ }}}^{7 \mathrm{FG}}$ \& C702

$\mathrm{C703}$ \& 13

13 \& ${ }_{10 \mathrm{~K}}^{9}$ \& | c2712 |
| :--- |
| $\substack{\text { c2712 } \\ \mathrm{c} 2713 \\ \hline}$ | \& 7 \& ${ }_{80}^{80}$ \& K100

k101 \& 1 \& ${ }_{5 \mathrm{sa}}^{6 \mathrm{~A}}$ \& ${ }_{\text {coser }}^{0322}$ \& 5
5 \& ${ }_{9 F}^{9 F}$ \&  \& 1 \& ${ }_{7 \mathrm{c}}^{7 \mathrm{~A}}$ \& ${ }_{\substack{\text { R227 } \\ \text { R228 }}}$ \& 2 2 \& ${ }_{4}^{86}$ <br>
\hline 1717
$\mathrm{Cl72}$
0 \& 13
13 \& ${ }_{3}^{28}$ \& C316 \& 13
13 \& ${ }_{8}^{8 E}$ \& C704
C705 \& 13
13
13 \& ¢\% \& c2213
c 2715
c 215 \& 7 \& ${ }^{6}$ \& K102
K 103 \& 1 \&  \& - ${ }_{\text {cis23a }}^{0323}$ \& 5 \& ${ }_{96}^{9 E}$ \& R1109
R106 \& 1 \& ${ }_{7 B}^{7 A}$ \& R228
R223
R230 \& 2 \& ${ }_{\substack{\text { S }}}^{\text {SE }}$ <br>
\hline c173
$\substack{1780 \\ C 180}$ \& 13 \& ${ }_{30}^{38}$ \& C318 \& ¢ ${ }^{13}$ \& ${ }_{7 \times}^{9 E}$ \& C705 \& ${ }^{13}$ \& $\stackrel{9}{100}$ \& c2716
c2717 \& 7 \& ${ }_{111}^{102}$ \& K1103
K100
K105 \& 1 \& ${ }_{50}$ \& ${ }_{\text {coich }}^{03238}$ \& 5 \& ${ }_{\text {P10 }}^{\text {Pex }}$ \& R106
R107
R10 \& 1 \& ${ }_{7 B}^{78}$ \& ${ }_{\substack{\text { R230 } \\ \text { R231 }}}^{2}$ \& ${ }_{2}^{2}$ \& ${ }_{3 \mathrm{le}}^{3 \mathrm{E}}$ <br>

\hline | C181 |
| :--- |
| $\substack{185 \\ \mathrm{Cl} \\ \hline}$ | \& ${ }_{1}^{13}$ \& 60 \& cere \& ${ }_{5}^{13}$ \& ${ }_{8 F}$ \& | C707 |
| :--- |
| 0708 |
| 108 | \& ${ }_{13}^{2}$ \& $\stackrel{91}{3}^{1}$ \& c2719

c2720
c 270 \& 7 \& ${ }_{80}^{109}$ \& ${ }_{\substack{\text { K105 } \\ \text { K107 }}}$ \& 1 \& ${ }_{48}^{60}$ \& - $\begin{aligned} & \text { 0326 } \\ & 0328 \\ & 0328\end{aligned}$ \& 5
5 \& ${ }_{8,}^{8 F}$ \& ${ }_{\substack{\text { R108 } \\ R 111}}^{181}$ \& 1 \& ${ }_{68}^{78}$ \& ${ }_{\text {R }}^{\substack{\text { R233 }}}$ \& ${ }_{2}^{2}$ \& 14
2
20 <br>
\hline ${ }^{1} 186$ \& 1 \& ${ }_{40}^{60}$ \&  \& 13
13 \& ${ }_{90}^{10 \%}$ \& ${ }_{C 711}$ \& ${ }_{2}$ \& 100 \& C2720
C2721
c723 \& 7 \& ${ }_{6 K}^{8 L}$ \& K108
K 109 \& 1 \& ${ }_{4 B}^{4 A}$ \& 0339
0330
038 \& 5
5 \&  \&  \& 1 \& ${ }_{5}^{5 A}$ \& ${ }_{\substack{\text { R233 }}}^{\text {R2323 }}$ \& 2 \& ${ }_{6}^{24}$ <br>
\hline  \& 1 \& ${ }_{20}^{60}$ \& ${ }_{\substack{\mathrm{C} 326 \\ \mathrm{C329}}}$ \& 5 \& $\xrightarrow{10 \mathrm{E}}$ \& C712
C801 \& $\stackrel{2}{13}$ \& ${ }_{8 k}^{101}$ \& (c2723 \& 7 \& $\stackrel{7}{710}$ \& K1990
K110
K111 \& 1 \& - ${ }_{5 B}^{4 B}$ \&  \& 5 \& ${ }_{\text {8F }}^{8 \%}$ \& R114
R115
R11 \& $\stackrel{1}{2}$ \& ${ }_{50}^{50}$ \& ${ }_{\substack{\text { R2335 } \\ R 238}}$ \& ${ }_{2}^{2}$ \& ${ }_{20}^{3 E}$ <br>
\hline ${ }_{\text {C121 }} \mathbf{C} 2101$ \& $\frac{1}{2}$ \& ${ }_{46}^{1 C}$ \& ¢ ${ }_{\text {c330 }}^{\text {c33 }}$ \& 5
13 \& 106

10 E \& c802 \& \& ${ }_{7 K}$ \& | C2759 |
| :--- |
| C 2783 | \& 7 \& ${ }_{8 N}^{7 \times 1}$ \& ${ }_{\text {k112 }}$ \& 1 \& ${ }_{40}^{4 D}$ \& ${ }^{0332}$ \& 5 \& ${ }_{88}{ }_{8}$ \& ${ }_{\substack{\text { R123 } \\ \text { R123 }}}^{122}$ \& 1 \& ${ }_{4}^{5 A}$ \& ${ }_{\substack{\text { R240 }}}^{\text {R240 }}$ \& ${ }_{2}$ \& ${ }_{2 \mathrm{LE}}^{2 \mathrm{E}}$ <br>

\hline C202 \& ${ }_{2}^{2}$ \& ${ }_{4}^{4 H}$ \& cock \& 13
13 \& 86
89 \& C8004 \& ¢ ${ }_{6}^{6}$ \& $\underset{\substack{8 \mathrm{~g} \\ \mathrm{gH}}}{\text { d }}$ \& c2784
$\substack{\text { c2785 }}$
c \& 7 \& ${ }_{60}^{6 N}$ \& 101 \& ${ }^{13}$ \& ${ }^{2 C}$ \& 0440
044
044 \& 3
3 \& ${ }_{3}^{2 H}$ \& ${ }_{\substack{\text { R124 } \\ \text { R125 }}}^{\text {R12 }}$ \& 1 \& ${ }_{40}^{3 A}$ \& ${ }_{\substack{\text { R242 } \\ \text { R243 }}}^{\text {R24 }}$ \& 2 \& 14
20 <br>
\hline C204 \& ${ }_{2}^{2}$ \& ${ }_{5}^{4 H}$ \& cent \& ${ }_{13}^{13}$ \& ${ }_{80}^{96}$ \& C8009
$\substack{8806 \\ C 807}$ \& ${ }^{13}$ \& ${ }_{8}^{9 H}$ \& ${ }^{\text {c2785 }}$ \& 7 \& ${ }_{8} 8$ \& ${ }_{\substack{\text { L102 } \\ \text { L130 }}}$ \& ${ }_{1}^{13}$ \& ${ }_{2 \mathrm{c}}^{2 \mathrm{C}}$ \& 0470
0474 \& 3 \& $\underset{\substack{14 \\ 2 \%}}{1+1}$ \& R131
$\substack{\text { R132 }}$
R12 \& 1 \& ${ }_{2}^{2 A}$ \& ${ }_{\substack{\text { R224 } \\ \text { R245 }}}^{\text {R24 }}$ \& 2 \& ${ }_{6}^{6+1}$ <br>
\hline C205 \& 13

13 \& ${ }_{1}^{110}$ \& Cole \& 3 \& ${ }_{3 G}^{3 G}$ \& ${ }_{\substack{\text { C808 } \\ \text { C808 }}}$ \& ${ }_{6}^{6}$ \& ${ }_{96}^{7 J}$ \& CR131 \& 1 \& ${ }_{18}^{28}$ \& | L1140 |
| :---: |
| $\substack{1201}$ |
| 101 | \& 13 \& 10

$6 E$
10 \& 04780
$\substack{080 \\ 060}$ \& 3
4
4 \& ${ }_{1 / 2}$ \&  \& 1 \& ${ }_{2}^{2 A}$ \& ${ }_{\substack{\text { R224 } \\ \text { R228 }}}^{\text {R293 }}$ \& ${ }_{2}^{13}$ \&  <br>
\hline C210
$\mathrm{C211}$ \& 2 \& ${ }_{5}^{6 E}$ \& ¢ \& 3

3 \& ${ }_{39} 36$ \& | C809 |
| :--- |
| C811 |
| 809 | \& ${ }_{2}^{6}$ \& ${ }_{75}^{89}$ \& CR171 \& $\frac{1}{2}$ \& 38

50 \& | 1201 |
| :--- |
| 2121 |
| 1217 | \& 13

13

13 \&  \& | 0600 |
| :--- |
| $\substack{660 \\ 0602}$ |
| 0.0 | \& 4

4
4 \& $\underset{\substack{3 N \\ 3 \mathrm{~N} \\ \\ \hline}}{ }$ \& R134
$\substack{\text { R13 } \\ \text { R13 }}$ \& 1 \& 28
28
28 \& (R294 \& 2 \& ${ }_{5}^{56}$ <br>

\hline | C 212 |
| :--- |
| $\mathrm{C213}$ |
| 213 | \& ${ }_{2}^{2}$ \& ${ }_{5 \text { SE }}^{56}$ \& ¢ \& 3 \& - ${ }_{3}^{26}$ \& C814 \& 6

13 \& ${ }_{7 H}^{8}$ \&  \& \& Sc

$\substack{50 \\ 45}$ \& | L217 |
| :--- |
| $\substack{127 \\ 1423 \\ \hline}$ | \& ${ }^{13}$ \& ${ }_{3}^{5 H}$ \& ${ }^{0602}$ \& 4 \& ${ }_{2}^{2 \mathrm{2K}}$ \& ${ }_{\substack{\text { R136 } \\ \text { R137 }}}$ \& 1 \& 20 \& ${ }_{\substack{\text { R225 } \\ \text { R25 }}}^{\text {20, }}$ \& ${ }_{2}^{2}$ \& ${ }_{68}^{5 F}$ <br>

\hline C214
C214
C215 \& 13
13
13 \& ${ }_{\substack{\text { SE } \\ \text { SE }}}^{\text {Se }}$ \&  \& 3
3
13 \& 2H
2

$2 H$ \& cict \& | 13 |
| :--- |
| ${ }_{6}^{13}$ |
| 6 | \& ${ }_{8}^{86}$ \& CRR260

$\substack{\text { CR261 } \\ \text { CR262 }}$ \& \&  \& ${ }_{\substack{\text { Li432 } \\ \text { L445 }}}$ \& ${ }^{3}$ \& ${ }_{3}^{2 \mathrm{a}}$ \& ${ }^{0604}$ \& ${ }_{4}^{4}$ \& ${ }_{3}^{3 \mathrm{sk}}$ \& R138
R139 \& ${ }_{1}^{2}$ \& 20
20
20 \& ${ }_{\substack{\text { R250 } \\ \text { R261 }}}^{\text {R23 }}$ \& 2
2
2 \& ${ }_{\substack{5 E \\ 56}}^{5}$ <br>
\hline ${ }_{0}^{216}$ \& 13 \& ${ }^{50}$ \& ${ }_{C} 444$ \& 3 \& ${ }_{2 F}$ \& ${ }^{6818}$ \& 13 \& ${ }_{80}^{60}$ \&  \& 2 \& ${ }_{4 \times}^{4 F}$ \& ${ }_{\text {L } 4685}^{1475}$ \& ${ }_{13}$ \& ${ }_{15}^{19}$ \& ${ }_{0}^{06089}$ \& ${ }_{4}^{4}$ \& ${ }_{2}^{2 K}$ \& $\substack{\text { R140 } \\ \text { R141 }}_{\text {R19 }}$ \& ${ }_{1}^{2}$ \& 20
28
28 \& ${ }_{\substack{\text { R2262 } \\ \text { R263 }}}^{\text {R280 }}$ \& 2 \& ${ }_{\substack{\text { sF }}}^{5 \times}$ <br>
\hline C217
C218
0218 \& ${ }^{13}$ \& 59
109

109 \& C445 \& $3_{3}^{13}$ \& ${ }_{2}{ }_{2 F}$ \& | C819 |
| :---: |
| $C 820$ |
| 80 | \& ${ }_{6}^{6}$ \& ${ }_{8,}^{7 J}$ \& CR301 \& ${ }_{3}^{5}$ \& ${ }_{26}^{70}$ \& 1701

1702 \& ${ }_{2}^{2}$ \& 101 \& (06088 \& 4 \& 22 \&  \& 1 \& ${ }_{28}^{28}$ \& ${ }_{\substack{\text { R283 } \\ \text { R265 }}}^{\text {R281 }}$ \& 2 \& ${ }_{\substack{\text { SF } \\ 5 \times \\ 5}}^{5}$ <br>
\hline C219 \& ${ }_{2}^{13}$ \& 50 \& C451 \& ${ }_{3}^{3}$ \& - ${ }_{26}{ }^{6}$ \& ${ }_{\substack{C 821 \\ C 822}}^{81}$ \& 13
13 \& ${ }_{88}^{88}$ \& CRA62 \& ${ }_{4}^{4}$ \& a
4 \& ${ }_{\substack{1703 \\ 1704}}$ \& 2 \& ${ }_{10 \mathrm{H}}^{9+1}$ \& 0702
0703

0703 \& 2 \& \% \& | R15 |
| :---: |
| $\substack{\text { R15 } \\ \text { R153 }}$ |
| 185 | \& 1 \& ${ }_{2}^{2 A}$ \& ${ }_{\substack{\text { R2266 } \\ \text { R267 }}}^{\text {R283 }}$ \& ${ }_{2}^{2}$ \& ${ }_{4}^{5 F}$ <br>

\hline C-221 \& ${ }_{2}^{2}$ \& ${ }_{4 \mathrm{4E}}^{4 \mathrm{E}}$ \& (cas \& | 3 |
| :--- |
| 3 | \& 26

26 \&  \& ${ }_{6}$ \& ${ }_{8}^{7 J}$ \& (encole \& | 4 |
| :--- |
| 4 |
| 4 | \&  \& \& 2

7 \& ${ }_{10 \mathrm{H}}$ \& 0703
0704
0801 \& ${ }_{2}^{2}$ \& ${ }_{11 \mathrm{H}}^{\text {a }}$ \& R153
R154 \& 1 \& ${ }_{2 A}^{1 A}$ \& ${ }_{\substack{\text { R2267 }}}^{\text {R268 }}$ \& 2 \& ${ }_{\substack{4 F \\ 5 F}}^{5}$ <br>
\hline \& \& \& \& \& \& \& \& \& \& \& \& \& 7 \& 10 N \& 0801 \& 6 \& TK \& R155 \& 1 \& 18 \& R269 \& 2 \& ${ }_{5}$ <br>
\hline
\end{tabular}



| A10－MAIN BOARD（cont） <br> （SN B715000 AND ABOVE， 2246 1Y <br> SN B120000 AND ABOVE， 2246 MOD A） |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Clincuit | SCHEM | ${ }_{\text {L }}^{\text {BOARD }}$ | CIRCUIT NUMBER | SCHEM | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT | SCHEM NUMBER | BOARD | CIRCUIT | SCHEM NUMBER | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ |
| св8о2 |  | 4 M | ${ }^{\text {P9 }}$ |  |  | 0802 |  |  | ${ }^{\text {R156 }}$ |  |  |
| ${ }_{\text {CR803 }}$ | 6 | ${ }^{8}$ | ${ }^{\text {P9 }}$ | 7 | ${ }_{9}{ }^{\text {N }}$ | ${ }^{8803}$ | ${ }_{6}$ | $7{ }^{7}$ | ${ }_{\text {R157 }}^{\text {R158 }}$ | 1 | ${ }^{16}$ |
| $\mathrm{c}_{\text {CR807 }}^{\text {CRa35 }}$ | ${ }_{7}^{6}$ | ${ }_{7 \text { A }}^{8}$ | ${ }_{\text {P18 }}^{\text {P17 }}$ | ${ }_{6}^{6}$ | ${ }_{88}^{76}$ | ${ }_{0}^{0804}$ | ${ }_{6}^{6}$ | ${ }_{8 k}^{7 J}$ | ¢ | ${ }_{1}$ | 10 10 10 |
|  | 7 | ${ }_{5 \text { ci }}^{7}$ | P19 <br> 190 | ${ }_{2}^{2}$ | （10H | － | ${ }_{6}^{6}$ | ${ }_{8}^{8 \times}$ | ${ }_{\substack{\text { R1，} \\ \text { R160 } \\ \text { R161 }}}$ | ${ }_{1}$ | ${ }_{18}^{18}$ |
| CRR100 CR102 Clin | 7 | 5L | ¢ ${ }_{\text {P20 }}^{\text {P2302 }}$ | ${ }^{2}$ | ¢ | － 08097 | ${ }^{6}$ | ${ }_{8}^{81}$ |  |  | $\underset{18}{18}$ |
| CR1003 <br> CR1004 | 7 | ${ }_{7}^{8 L}$ | ${ }_{\substack{\text { P2302 } \\ \text { P2304 }}}$ | 7 | 100 110 | －8810 | ${ }_{6}^{6}$ | ${ }_{7}^{7 \%}$ | ${ }_{\substack{\text { R171 } \\ \text { R175 }}}^{\text {R17 }}$ |  | ${ }_{38}^{38}$ |
|  | 7 | ${ }_{9}^{3 L}$ | ${ }_{\substack{\text { P2502 } \\ \text { P2522 }}}$ | ${ }_{4}^{13}$ | $1{ }_{1}^{12}$ | － 0812 | ${ }_{7}^{6}$ | ${ }_{108}^{81}$ | ${ }_{\substack{\text { R176 } \\ \text { R177 }}}$ |  | 3 c 3 c |
| CR2701 | 7 | ${ }_{9}^{9 M}$ | P2502 |  |  |  | 7 | ${ }_{1}^{10 A}$ | R117 R178 R179 |  | 36 30 30 |
| CR2703 | 7 | ${ }_{6 \mathrm{~N}}^{8 \mathrm{~N}}$ |  | 1 | ${ }_{2 B}^{28}$ | －0908 | 7 | ${ }_{44}^{10 A}$ | R179 R130 | 1 | ${ }_{28}^{36}$ |
|  | 7 | ${ }_{7}^{69}$ |  | 1 | ${ }^{18}$ | （01002 | 7 | ${ }_{4}^{4}$ | ¢， $\begin{gathered}\text { R1181 } \\ \text { R182 }\end{gathered}$ | 13 | 18 60 80 |
| CR27207 CR2713 | 7 | ${ }_{10}$ | ${ }^{\text {O／171 }}$ | 1 | ${ }_{28}^{18}$ | （1003 | 7 | ${ }_{4}^{4 L}$ | ${ }_{\substack{\text { R1822 } \\ \text { R．185 }}}^{\text {R18 }}$ | 13 | ${ }_{60}^{60}$ |
| ${ }_{\substack{\text { CR2714 } \\ \text { CR2715 }}}$ | 7 | ${ }_{96}^{112}$ | 0181 0 0182 0.1 | 1 | ${ }_{40}^{60}$ |  | 7 | \％ | Ris8 | $\frac{1}{2}$ | 30 50 |
| ${ }_{\substack{\text { CR22715 } \\ \text { CR2716 }}}$ | 7 | ${ }_{9}^{9 \mathrm{M}}$ | $c01820250$ | 2 | ${ }_{4 \mathrm{E}}^{4 \mathrm{E}}$ | 01101 01102 0 | ${ }_{3}^{3}$ | ${ }_{50}^{50}$ | ${ }_{\substack{\text { R201 } \\ \text { R202 }}}$ | ${ }_{2}^{2}$ | ${ }_{56}^{56}$ |
| ${ }_{\substack{\text { CR2717 } \\ \text { CR2718 }}}$ | 7 | ${ }_{9}^{9 M}$ | ${ }^{0251}$ | 2 | ${ }_{45}^{4 F}$ | 01103 | 3 | ${ }_{5}^{51}$ | R2033 | 2 | ${ }_{56}^{59}$ |
| CR2718 |  | ${ }^{8 L}$ | 0253 | ${ }_{2}^{2}$ | ${ }_{4 F}^{4 E}$ | 01104 <br> 01104 <br> 01105 <br> 180 | ${ }^{3}$ | 50， |  | ${ }_{2}^{2}$ | 56 6 H |
| ${ }_{0} \mathbf{1 2 1}$ | 2 | ${ }^{3} \mathrm{~F}$ | ${ }_{0}^{0284}$ | 2 | ${ }^{6 E}$ | O106 | 3 | ${ }_{5}$ | ${ }_{\text {R206 }}$ | 2 | 5 s |
| ${ }^{\mathrm{LL21}}$ | ${ }_{3}^{2}$ | ${ }_{2}^{10 \mathrm{~K}}$ | －0285 | $\stackrel{2}{5}$ | ${ }^{\text {SE }}$ | O2701 O2702 | 7 | ${ }_{7}^{7 N}$ | （r207 | ${ }_{13}^{2}$ | ${ }_{11 \mathrm{c}}^{96}$ |
| $0 \mathrm{L22}$ | 3 | ${ }^{3 k}$ | a3022 $\substack{0303 \\ 030}$ | 5 | ${ }_{8}^{86}$ | ${ }^{02703}$ | 7 | ${ }^{7 N}$ | ${ }_{\text {R209 }}$ | 2 | 1 M |
|  |  |  | －0303 | 5 | ${ }_{88}^{8 \mathrm{FF}}$ | －02704 | 7 | ${ }_{7 \times 1}{ }_{7}$ |  | 2 | ${ }_{6 \mathrm{c}}^{6 \mathrm{E}}$ |
| ${ }^{\text {05902 }}$ | 7 | ${ }_{98}$ | ${ }^{0305}$ | 5 | 7 F | ${ }^{02706}$ | 7 | ${ }^{7 M}$ | ${ }^{\text {R212 }}$ | 2 | 2 |
| 05903 DS52701 | 7 | ${ }_{90}^{118}$ | ${ }_{\substack{0306 \\ 0307}}^{\text {O30 }}$ | 5 | ${ }_{80}^{76}$ | （02707 | 7 | ${ }_{6 L}^{6 M}$ | R213 R214 | 2 | ${ }_{6}^{2 M}$ |
| ${ }^{\text {DS5272 }}$ |  | 9 M |  | 5 | ${ }_{8} 8$ | 02709 | 7 | 6 | R215 | 2 | ${ }_{6 N}$ |
|  | 7 | ${ }_{9}^{9 M}$ | －0399 | ${ }_{5}^{5}$ | ${ }_{88}^{8 F}$ | －02711 | 7 | ${ }_{10}^{102}$ | ${ }_{\substack{\text { R2216 } \\ R 217}}$ | ${ }_{2}^{2}$ | ${ }_{79}^{89}$ |
|  |  |  | ${ }_{\text {व3108 }}$ | 5 | ${ }_{86}{ }_{8}$ | ${ }^{02713}$ | 7 | 111 | ${ }_{\text {R218 }}$ | 2 | ${ }_{5 E}$ |
| ${ }^{311}$ | 1 | ${ }_{5 A}^{6 A}$ | －0311 | 5 | ${ }_{88}^{8 F}$ | 02715 | 7 | ${ }_{\text {¢ }}$ | R219 | ${ }_{2}^{2}$ | ${ }_{4 \times}^{50}$ |
| （13 | 1 | $3{ }^{3 A}$ 1 1 | － | 5 | ${ }_{96}^{9 F}$ | ${ }_{\text {R13 }}^{\text {R12 }}$ | 1 | ${ }_{60}^{60}$ | R221 R222 | 2 | ${ }_{\text {c }}^{4}$ |
| J15 | 7 | 7 A | ${ }_{\text {－}}$ | 5 | ${ }_{\text {OF }}^{\text {ge }}$ | ${ }_{\text {R22 }}^{122}$ | 1 | ${ }_{5}^{60}$ |  |  | ${ }_{2} 2 \mathrm{M}$ |
|  | 73 | ${ }_{50}^{118}$ | － | 5 | ${ }_{10 \mathrm{e}}^{\text {9F }}$ | R23 <br> R2101 | 1 | ${ }_{7 A}^{4 C}$ | R224 R225 | ${ }_{2}^{2}$ | ${ }_{80}^{64}$ |
|  |  |  | ${ }_{\substack{0320 \\ 0321}}^{0321}$ | 5 | ${ }_{96}^{9 E}$ | R．102 | 1 | ${ }^{7 \text { 7 }}$ | ${ }_{\substack{\text { R222 } \\ \text { R227 } \\ \text { R23 }}}$ | ${ }^{13}$ | ${ }_{6 \mathrm{Cb}}^{8 \mathrm{C}}$ |
| K101 | 1 | ${ }_{5 A}^{6 A}$ | ${ }^{\circ}$ | 5 | ${ }_{9}{ }^{\text {9F }}$ |  | 1 | ${ }_{7 A} 7$ | ${ }_{\text {R228 }}$ | 2 | ${ }_{4 E}$ |
| ${ }_{\substack{\text { K102 } \\ \text { K103 }}}$ | 1 | ${ }_{78}^{58}$ |  | 5 | ${ }_{96}^{9 E}$ | R， | 1 | ${ }_{7 B}^{7 A}$ | R229 R230 | 2 | S0 <br> 3 E |
| K103 K109 K105 | 1 | 50 60 60 | a $\substack{0323 \\ 0325 \\ 0326}$ 0 | 5 5 5 |  |  | 1 | ${ }_{78}^{78}$ |  | 2 |  |
| K107 | 1 | ${ }_{4}^{60}$ | － | 5 | ${ }_{8 E}^{8 F}$ | R108 R111 | 1 | ${ }_{68}^{78}$ | ${ }_{\substack{\text { R2323 } \\ \text { R233 }}}$ | ${ }_{2}$ | 2 m |
| K108 K 109 | 1 | ${ }_{4 B}^{4 A}$ | － | 5 | ${ }_{88}^{9 E}$ | R113 | 1 | ${ }_{5}^{5 A}$ | Re234 | 2 |  |
| K110 K111 K112 | 1 | ${ }_{5 B}^{5 B}$ | － | 5 | ${ }_{\text {¢ }}^{9 \mathrm{gF}}$ | ${ }^{\text {R115 }}$ | 2 | ${ }_{50}^{50}$ | ${ }_{\text {R238 }}^{\text {R238 }}$ |  | 20 |
| ${ }_{\text {K112 }}$ | 1 | ${ }_{40}^{4 D}$ | － | 5 | ${ }_{8 \times}^{9 F}$ | ${ }_{\substack{\text { R121 } \\ \text { R123 }}}$ | 1 | ${ }_{4}^{5 A}$ | ${ }_{\substack{\text { R240 } \\ \text { R241 }}}^{208}$ | ${ }_{2}^{2}$ | ${ }_{2 \mathrm{~L}}^{2 \mathrm{E}}$ |
|  |  |  | O440 | 3 | ${ }_{3 F}^{2 H}$ | Rind | 1 | ${ }^{3 A}$ | R242 | 2 | ${ }_{2}^{14}$ |
| L102 | ${ }_{13}^{13}$ | $2 \mathrm{2C}$ |  | 3 |  | ${ }_{\text {R131 }}$ | 1 | ${ }_{2} 2 \mathrm{~A}$ |  | 2 | ${ }_{6}$ |
| L130 | 1 | 120 |  | ${ }_{3}^{3}$ | ${ }_{1 F}^{2 F}$ |  | 1 | ${ }_{2 A}^{2 A}$ | ¢R245 | ${ }_{2}^{13}$ | $\underset{\substack{30 \\ 20}}{ }$ |
| 1201 1216 1218 | 13 <br> 13 | ${ }_{4}^{6 \mathrm{E}}$ | O600 | ${ }_{4}^{4}$ | ${ }_{3}^{3 N}$ |  | 1 | ${ }^{2 B}$ | R249 R254 | 2 | ${ }_{\substack{56 \\ 56}}^{26}$ |
| 4217 | ${ }^{13}$ | ${ }_{5}^{5 H}$ | ${ }^{0602}$ | 4 | ${ }^{2 \mathrm{~K}}$ | ${ }_{\text {R136 }}^{\text {R13 }}$ | 1 | ${ }^{20}$ | ${ }_{\substack{\text { R225 } \\ \text { R25 } \\ \hline}}$ | 2 | ${ }_{5}^{5 F}$ |
|  | ${ }_{3}^{3}$ | ${ }_{26}^{3 H}$ | （0603 | ${ }_{4}^{4}$ | ${ }_{3 K}^{2 K}$ | ${ }_{\substack{\text { Rel38 } \\ \text { R138 }}}^{\text {R13 }}$ | $\frac{1}{2}$ | ${ }_{2 C}^{2 C}$ | ${ }_{\substack{\text { R2260 } \\ \text { R280 }}}$ | 2 |  |
| L445 | ${ }_{3}^{13}$ | 3 | （0605 | ${ }_{4}^{4}$ | － | ${ }_{\substack{\text { R139 } \\ \text { R140 }}}^{\text {R140 }}$ | $\frac{1}{2}$ | ${ }_{20}^{20}$ | ${ }_{\text {R281 }}^{\text {R2623 }}$ | 2 | ${ }_{\substack{\text { 5E }}}^{5 E_{5}}$ |
| ${ }^{14729}$ | ${ }^{13}$ | $1{ }^{15}$ | （0607 | 4 | ${ }_{2 K}^{2 K}$ | ${ }_{\substack{\text { R／141 }}}^{\text {R14，}}$ | $\stackrel{1}{1}$ | ${ }_{2 B}^{2 C}$ | ${ }_{\substack{\text { R2823 } \\ \text { R283 }}}$ | ${ }_{2}^{2}$ | ${ }_{5}^{5 F}$ |
| $\stackrel{1701}{1702}$ | ${ }_{2}^{2}$ | ${ }_{101}^{901}$ | 0608 0701 | ${ }_{2}^{4}$ | ${ }_{9}^{2 L}$ | ${ }_{\substack{\text { R142 } \\ \text { R151 }}}^{\text {R10 }}$ | 1 | ${ }_{28}^{28}$ | ${ }_{\substack{\text { R2264 } \\ \text { R265 }}}^{2}$ | ${ }_{2}^{2}$ | ${ }_{55}^{5 F}$ |
| 17203 $L 1704$ | 2 | ${ }_{10 \mathrm{H}}^{9}$ | （0702 | 2 | 101 96 | ${ }_{\substack{\text { R．152 } \\ \text { R153 }}}^{\text {R15 }}$ | 1 | ${ }_{2 A}^{2 A}$ | $\substack{\text { R226 } \\ \text { R262 }}_{\text {R28，}}$ | 2 | ${ }_{4}^{4 F}$ |
| P8 |  | 10 N | ${ }^{0} 704$ | 2 | 11 H | ${ }_{\text {R }}$ | 1 | ${ }_{2}^{2 A}$ | ${ }_{\text {R2288 }}^{\text {R28 }}$ | ${ }_{2}$ | ${ }_{5}^{4 F}$ |
|  |  |  | 0801 |  | ik | ${ }^{\text {R155 }}$ |  |  | R269 |  |  |


| A10－MAIN BOARD（cont） （SN B715000 AND ABOVE， 2246 1Y SN B120000 AND ABOVE， 2246 MOD A） |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| （circuit | SCHEM | ${ }_{\text {L }}^{\text {BOARD }}$ | ${ }_{\substack{\text { CiRCUIT } \\ \text { NUMBER }}}$ | SCHEM | ${ }_{\substack{\text { Boabd } \\ \text { LOCATON }}}$ | （circuit | SCHEM NUMER | ${ }_{\substack{\text { BOARD } \\ \text { LOCATON }}}$ | CiRcut | SCHEM | ${ }_{\text {b }}^{\text {boado }}$ Locaton |
| R270 | ${ }^{2}$ | 4 | ${ }^{\text {R347 }}$ | 5 | ${ }^{8 E}$ | R448 | ${ }^{3}$ | ${ }^{2 F}$ | R628 | 4 | ${ }^{2 k}$ |
| ${ }_{8}^{\mathrm{R} 272}$ | 2 | ${ }_{4}^{45}$ | ${ }_{\text {R348 }}^{\text {R34 }}$ | ${ }_{5}^{5}$ | ${ }_{74}^{8 H}$ | Ra49 | 3 | ${ }_{1 / 8}^{3 F}$ | ${ }_{\substack{\text { R630 } \\ \text { R331 }}}$ | ${ }_{4}^{4}$ | ${ }_{31}^{2 N}$ |
| ${ }_{\substack{\text { R272 } \\ \text { R273 }}}$ | 2 | ${ }_{4 G}^{4 F}$ | ${ }_{\substack{\text { R339 } \\ \text { R350 }}}^{\text {Re3 }}$ | 5 | ${ }_{7}^{7 H}$ | ${ }_{\substack{\text { R455 } \\ \text { R451 }}}^{\text {Rat }}$ | ${ }_{3}^{3}$ | ${ }_{16}^{1 / 16}$ | ${ }_{\substack{\text { R6331 } \\ \text { R．36 }}}^{\text {Re9 }}$ | ${ }_{4}^{4}$ | ${ }_{6 \mathrm{SN}}^{3 L}$ |
| ${ }_{\text {R274 }}$ | 2 | ${ }^{3 E}$ | ${ }^{\text {R3352 }}$ | 5 | ${ }^{7 H}$ | ${ }^{\text {R4452 }}$ | 3 | 1 l | ${ }^{\text {R637 }}$ | 4 | ${ }_{3 N}^{3 N}$ |
| R275 R276 | ${ }_{2}^{2}$ | ${ }_{4}^{4 F}$ | ${ }_{\text {R }}^{\text {R354 }}$ | 5 5 | ${ }_{96}^{90}$ | ${ }_{\substack{\text { Ras53 } \\ \text { R454 }}}^{\text {R4，}}$ | ${ }_{3}^{3}$ | ${ }_{26}^{26}$ | ${ }_{\substack{\text { R6338 } \\ \text { R639 }}}^{\text {¢ }}$ | $\stackrel{4}{4}$ | ${ }_{3}^{3 \mathrm{~N}}$ |
| ${ }^{\text {R227 }}$ | 2 | ${ }^{4 F}$ | ${ }^{\text {R335 }}$ | 5 | 9 P | ${ }^{\text {R4455 }}$ | 3 | ${ }^{2 H}$ | ${ }_{\text {P640 }}$ | 4 | ${ }_{3}^{3 L}$ |
| R278 R279 | 2 | ${ }_{3 F}^{3 F}$ | $\underbrace{\text { R35 }}_{\text {R3357 }}$ | 5 | ${ }_{88}^{9 F}$ | ${ }_{\substack{\text { Ras6 } \\ \text { R460 }}}$ | ${ }_{3}^{3}$ | ${ }_{16}^{19}$ | ${ }_{\substack{\text { R6442 }}}^{\text {R64 }}$ | ${ }_{4}^{4}$ | ${ }_{4}^{4 M}$ |
| ${ }_{\text {R280 }}$ | 2 | 3 S | ${ }_{\text {R }}^{\text {R358 }}$ | ${ }_{5}^{6}$ | ${ }_{98}^{90}$ | R4661 | 3 | 16 | ${ }_{\text {R643 }}$ | 4 | 3 M |
| ${ }_{\substack{\text { R2828 } \\ \text { R28 }}}$ | ${ }_{13}$ | ${ }_{4}^{3 F}$ | ${ }_{\text {R }}^{\text {R3360 }}$ | 5 | ${ }_{9 F}^{8 F}$ |  | ${ }_{3}^{3}$ | ${ }_{1 /}^{16}$ |  | ${ }_{4}^{4}$ | ${ }_{3}^{3 M}$ |
| ${ }_{\text {R283 }}$ | ${ }^{13}$ | ${ }_{6}^{66}$ | ${ }_{\substack{\text { R3361 } \\ \text { R392 }}}$ | ${ }_{5}^{5}$ | ${ }_{86}^{8 G}$ | R470 | 3 | 1 G | R646 | 4 | ${ }^{3 \mathrm{M}}$ |
| ${ }_{\substack{\text { R284 } \\ \text { R285 }}}$ | 2 | ${ }_{6}^{68}$ | ${ }_{\text {cher }}^{\substack{\text { R3362 }}}$ | 5 | ${ }_{9 F}^{8 F}$ | ${ }_{\text {R4772 }}^{\text {R47 }}$ | 3 | $\underset{\text { 1H }}{1 \text { H }}$ | $\substack{\text { R647 } \\ \text { R648 }}_{\text {Ref }}$ | ${ }_{4}^{4}$ | ${ }_{3 \mathrm{3k}}^{1 \mathrm{~J}}$ |
| ${ }^{\text {R286 }}$ | 2 | ${ }_{5}^{56}$ | ${ }_{\text {R364 }}$ | 5 | 9 ga | R473 | 3 | ＋ | ${ }_{\text {R649 }}^{\text {R649 }}$ | ${ }_{4}^{4}$ | ${ }_{3 K}$ |
|  | ${ }_{2}^{2}$ | ${ }_{6}^{5 F}$ | ${ }_{\text {R }}^{\text {R3665 }}$ | 5 | ${ }_{80}^{105}$ | ${ }_{\substack{\text { R474 } \\ \text { R475 }}}^{\text {a }}$ | ${ }_{3}^{3}$ | $\underset{\text { 1／}}{1 /}$ | ${ }_{\substack{\text { R650 } \\ \text { R651 }}}^{\text {Ref }}$ | ${ }_{4}^{4}$ | ${ }_{4 M}^{3 M}$ |
| R289 R290 | 2 | ${ }_{65}^{5 F}$ | $\underbrace{}_{\substack{\text { R3367 } \\ \text { R369 }}}$ | ${ }_{6}^{5}$ | ${ }_{80}^{90}$ | ${ }_{\text {R477 }}$ | 3 | 1 F | ${ }_{\text {R }}^{\text {R652 }}$ | 4 | ${ }_{4}{ }^{\text {M }}$ |
| ${ }_{\text {R }}^{\text {R290 }}$ | ${ }_{2}^{2}$ | ${ }_{5}^{6 F}$ | ${ }_{\substack{\text { R3390 } \\ \text { R370 }}}^{\text {are }}$ | ${ }_{5}^{6}$ | ${ }_{96}^{80}$ | ${ }_{\substack{\text { R477 } \\ \text { R47 }}}$ | ${ }_{3}^{3}$ | ${ }_{1 /}^{1 / 2}$ | ${ }_{\substack{\text { R6553 } \\ \text { R654 }}}$ | $\stackrel{4}{4}$ | ${ }_{3}^{3 \mathrm{3L}}$ |
| ${ }_{\text {R2922 }}$ | ${ }_{2}^{2}$ | ${ }_{\substack{5 F \\ 55}}^{5}$ | ${ }_{\text {R }}^{\text {R331 }}$ | 5 | ${ }_{96}^{96}$ | R479 | 3 | 1 F | R655 | 4 | ॐ |
| ${ }_{\substack{\text { R293 } \\ \text { R293 }}}^{\text {Red }}$ | ${ }_{2}^{2}$ | ${ }_{56}^{5 F}$ | ${ }_{\text {cher }}^{\substack{\text { R372 }}}$ | 5 | ${ }_{109}^{90}$ | R4800 | 13 | ${ }_{1}^{4 F}$ | ${ }_{\substack{\text { R656 } \\ \text { R657 }}}$ | ${ }_{4}^{4}$ | ${ }_{4}^{4 K}$ |
| ${ }^{\text {R295 }}$ | 2 | ${ }_{5}^{56}$ | ${ }_{\text {R }}$ | 13 | 10 E | ${ }_{\substack{\text { R483 } \\ \text { R48 }}}$ | ${ }_{3}$ | ${ }_{3 F}$ | ${ }_{\text {Re58 }}$ | ${ }_{4}^{4}$ | ${ }_{4 \mathrm{~K}}$ |
| ${ }_{\text {R297 }}^{\text {R296 }}$ | ${ }_{13}^{2}$ | ${ }_{68}^{68}$ | ${ }_{\text {R3376 }}^{\text {R375 }}$ | 5 | 隹 10 E | R4889 | 3 3 3 | 1E | ${ }_{\text {R659 }}$ | 4 | 4 M |
|  | ${ }^{13}$ |  | ${ }_{\substack{\text { R337 } \\ \text { R378 }}}^{\text {R37 }}$ | 5 5 5 | ${ }_{8}^{8 \mathrm{~g}}$ |  | ${ }_{3}^{3}$ | 揖 | （f663 | ${ }_{4}^{4}$ | ${ }_{3 K}^{2 L}$ |
| ${ }_{\text {R }}^{\text {R290 }}$ | 2 | ${ }_{9 \times}^{4 F}$ | ${ }_{\substack{\text { R3378 } \\ \text { R379 }}}^{\text {Re9 }}$ | 5 5 | ${ }_{76}^{61}$ | R4887 <br> R490 | 3 3 | $\stackrel{1 E}{15}$ | （R664 | ${ }_{4}^{4}$ | ${ }_{4 \times}^{3 K}$ |
| ${ }_{\text {R }}^{\text {R302 }}$ | 5 | ${ }_{8 \times}^{7 \%}$ | ${ }_{\substack{\text { R3300 } \\ \text { R381 }}}$ | 5 5 | ${ }_{10}^{60}$ | ${ }_{\text {R4991 }}$ | 3 | 3 | ${ }_{\text {Re66 }}^{\text {R685 }}$ | 4 | ${ }_{4}$ |
|  | 5 | ${ }_{8}^{8 F}$ | ${ }_{\substack{\text { R3323 }}}^{\text {R3902 }}$ | 5 | ${ }^{76}$ | R． | ${ }_{3}^{3}$ | ${ }_{2 J}^{31}$ |  | ${ }_{4}^{4}$ | ${ }_{4 K}^{3 L}$ |
| ${ }_{\substack{\text { R3305 } \\ \text { R306 }}}^{\text {Re3 }}$ | 5 | ${ }_{70}^{86}$ | ${ }_{\substack{\text { R3384 } \\ \text { R338 }}}^{\text {Re }}$ | 5 | ${ }_{80}^{96}$ | ¢ | ${ }_{3}^{3}$ | ${ }_{2}^{2 J}$ | ${ }_{\substack{\text { Re67 } \\ \text { R672 }}}$ | ${ }_{4}^{4}$ | ${ }_{3}^{3 M}$ |
| 良3307 | ${ }_{5}^{5}$ | ${ }_{7 c}^{70}$ | ${ }_{\substack{\text { R3365 } \\ \text { R386 }}}$ | 5 | ${ }_{96}^{9 F}$ | R4995 R996 | ${ }_{3}$ | ${ }_{2 J}^{2 J}$ | ${ }_{\text {cher }}^{\substack{\text { R672 } \\ \text { R673 }}}$ | ${ }_{4}^{4}$ | ${ }_{4}$ |
| ${ }_{\text {R }}^{\substack{\text { R309 }}}$ | 5 | $9{ }^{9}$ | ${ }_{\text {R }}^{\text {R387 }}$ | 5 | ${ }_{8}^{8 F}$ | ${ }_{\text {R498 }}^{\text {R497 }}$ | 3 3 | ${ }_{3}^{2 J}$ | R874 R701 | ${ }_{13}^{4}$ | $\stackrel{49}{9}$ |
| ${ }_{\substack{\text { R3311 } \\ \text { R311 }}}^{\text {and }}$ | 5 | ${ }_{80}^{7 c}$ |  | 13 <br> 1 | ${ }_{8 F}^{8 F}$ | ${ }_{\substack{\text { R5501 } \\ \text { Re5 }}}$ | 4 | L | R772 R703 | 2 | $\stackrel{9}{111}$ |
| R3312 R313 | 58 | 70 |  | ${ }_{5}^{13}$ | $\xrightarrow{7 c}$ | ${ }_{\substack{\text { R } \\ \text { R503 } \\ \hline}}$ | ${ }^{7}$ | 5N0 100 | R7733 R706 | ${ }_{2}$ |  |
| ${ }_{\substack{\text { R3314 } \\ \text { R312 }}}$ | 5 | 7 c |  | 5 | 退 |  | ${ }_{7}^{13}$ | 909 ${ }_{9}^{90}$ | R7707 R708 | 2 | 10 K <br> 10 H |
| ${ }_{\substack{\text { R3315 } \\ \text { R316 }}}^{\text {Rel }}$ | 5 5 | ${ }_{7 \times}^{10 \mathrm{~F}}$ | ${ }_{\substack{\text { R3935 } \\ \text { R396 }}}$ | 5 | ${ }_{10 \mathrm{E}}^{8 \mathrm{E}}$ |  | 7 | ${ }_{9}^{96}$ | R7709 <br> R779 <br> 871 | ${ }^{2}$ | ${ }_{9}^{10 H}$ |
| ${ }_{\substack{\text { R3317 } \\ \text { R318 }}}^{\text {R18 }}$ | 5 | ${ }_{\substack{8 E \\ 8 E}}$ | R4001 R402 | 3 3 3 |  |  | 7 | ${ }_{100}^{96}$ | ${ }_{\text {R710 }}^{\text {R711 }}$ | ${ }_{2}$ | $\underset{101}{10 \mathrm{~K}}$ |
|  | 5 5 5 | ${ }_{8}^{8 \times}$ |  | 3 | 3 | R5510 R512 Ref | 7 | 100 100 100 | R712 R715 | 2 | 101 <br> 10 K <br> 1 |
| ${ }_{\substack{\text { R332 }}}^{\text {R320 }}$ | 5 5 | ${ }_{80}^{8 E}$ |  | ${ }_{3}^{3}$ | ${ }_{36}^{3 G}$ |  | 4 | ${ }^{100}$ | R7715 <br> R716 <br> 777 |  | ${ }_{1}^{10 \mathrm{~K}}$ |
| ${ }_{\substack{\text { R3322 } \\ \text { R323 }}}^{\text {Re3 }}$ | 5 5 5 | ${ }_{7}^{7 \mathrm{Fb}}$ | R4412 R413 Rat | 3 3 3 | －36 | ${ }_{\text {R6003 }}^{\text {R602 }}$ | 4 | ${ }_{1}^{2 N}$ | R717 R718 | ${ }_{2}^{2}$ | ${ }_{9 K}$ |
| ${ }_{\substack{\text { R332 } \\ \text { R322 }}}^{\text {R323 }}$ | 5 | 70 | R4414 R414 Rat | 3 | ${ }_{3}^{36}$ |  | 4 | in | R7719 R720 | 2 | ${ }_{1}^{10 \mathrm{H}}$ |
| ${ }_{\text {R3327 }}^{\text {R336 }}$ | 5 | ${ }_{9 F}^{70}$ |  | ${ }_{3}^{3}$ | ${ }_{26}^{26}$ |  | ${ }_{4}^{4}$ | ${ }_{3 N}^{1 N}$ | ${ }_{\substack{\text { Rr720 } \\ \text { R721 }}}$ | ${ }_{2}^{2}$ | ${ }_{96}^{10 \mathrm{H}}$ |
| ${ }_{\substack{\text { Re328 } \\ \text { R329 } \\ \text { R323 }}}$ | 5 5 5 | ${ }_{\substack{\text { BF } \\ \text { OE }}}^{\text {ge }}$ | Ratio $\substack{\text { R447 } \\ \text { Ra20 }}$ | 3 | ${ }_{2 H}^{2 \mathrm{~L}}$ | R．6999 R610 | ${ }_{4}^{4}$ | $\xrightarrow{2 \mathrm{~K}}$ | ¢ $\begin{gathered}\text { R7222 } \\ \text { R723 }\end{gathered}$ | ${ }_{1}^{2}$ | ${ }_{90}$ |
| ${ }_{\text {R }}^{\substack{\text { R3330 }}}$ | 5 | ${ }_{9 E}^{96}$ | ${ }_{\substack{\text { Ra420 } \\ \text { R221 }}}$ | ${ }_{3}$ | ${ }_{36}{ }^{3 H}$ | ${ }_{8611}^{8691}$ | 4 | ${ }_{3}^{2 K}$ | ${ }_{\text {R }}$ | 2 | 9 g |
| ${ }_{\substack{\text { R3332 }}}^{\text {R33 }}$ | 5 | ${ }_{76}^{8 E}$ |  | ${ }_{3}^{3}$ | ${ }_{3}^{3 G}$ |  | $\stackrel{4}{4}$ | $\stackrel{3}{3 \mathrm{M}}$ | R725 R726 | ${ }_{2}^{2}$ | ${ }_{11}^{9 K}$ |
| （R332 | 5 5 5 | ${ }_{86}^{86}$ |  | 3 | 36 <br> 36 | ¢R614 <br> R615 | ${ }_{4}^{4}$ | ${ }_{4}^{3 L}$ | R727 R278 | 2 | $\substack{10 \mathrm{~K} \\ 10 \mathrm{c} \\ 10 \mathrm{H}}$ |
| ${ }_{\text {R }}^{\substack{\text { R334 } \\ \text { R33 }}}$ | 5 | ${ }_{\substack{86 \\ 96}}$ |  | 3 | ${ }_{3}^{36}$ | ${ }_{\substack{\text { R6616 }}}^{\text {R6，}}$ | ${ }_{4}^{4}$ | ${ }_{3 k}$ | ${ }_{\text {R729 }}$ | 2 | 10H |
| ${ }_{\substack{\text { R336 } \\ \text { R337 }}}^{\text {R33 }}$ | 5 | ${ }_{88}^{9 F}$ | $\substack{\text { Ras30 } \\ \text { R431 }}_{\text {Ras }}$ | 3 3 | $\underset{\substack{26 \\{ }_{2 F} \\ \hline}}{ }$ | ${ }_{\substack{\text { R6618 } \\ \text { R618 }}}$ | ${ }_{4}^{4}$ | ${ }_{3}^{3 \mathrm{3K}}$ | R730 | 2 | ${ }_{9+}$ |
|  | 5 <br> 5 | ${ }_{\text {¢ }}^{96}$ |  | 3 | ${ }_{26}^{26}$ | ${ }_{\substack{\text { R619 } \\ \hline 8620}}$ | 4 | ${ }_{3}^{3 M}$ | ${ }_{\text {R732 }}$ | 2 | ${ }^{104}$ |
| ${ }_{\text {R }}^{\substack{\text { R339 }}}$ | ${ }_{5}^{13}$ | ${ }^{96}$ | Re440 | ${ }_{3}^{3}$ | ${ }_{3 H}^{2 \mathrm{ab}}$ | ¢ | ${ }_{4}^{4}$ | ${ }_{4 L}^{4 M}$ | ${ }_{\text {R734 }}^{\text {R733 }}$ | 13 13 | $\underset{\substack{10 \mathrm{~K} \\ \text { 1OH }}}{ }$ |
| （R341 | 5 | ${ }_{96}^{9 \mathrm{~Pa}}$ | （R4422 | 3 3 3 | $\stackrel{3}{3+}$ |  | ${ }_{4}^{4}$ | ${ }_{\substack{3 K \\ 3 K}}$ | R8801 R802 | 6 6 | ${ }_{7}^{88}$ |
| Re342 | 5 5 | ${ }_{9}^{9 \mathrm{em}}$ | （eatis | 3 | ${ }_{3}^{2 H}$ | R623 <br> $\substack{\text { R62 }}$ | ${ }_{4}^{4}$ | ${ }_{2}^{3 K}$ | ${ }_{\substack{\text { R8802 } \\ \text { R80 }}}^{\text {R }}$ | ${ }_{6}^{6}$ | ${ }_{8 K} 8 \times$ |
| ${ }_{\substack{\text { R334 } \\ \text { R34 }}}^{\text {and }}$ | 5 13 | ${ }_{8 \times}^{8 E}$ | Re445 | ${ }_{3}^{3}$ | ${ }_{3}^{2 F}$ | $\substack{\text { R625 } \\ \text { R626 }}_{\text {R22 }}$ | ${ }_{4}^{4}$ | ${ }_{4}^{2 L}$ | ${ }_{\substack{\text { R8804 } \\ \text { R80 }}}$ | ${ }_{6}^{6}$ | ${ }_{80}^{8 K}$ |
| ${ }_{\text {R346 }}$ | ${ }_{5}$ | ${ }_{8 E}$ | ${ }_{\text {R447 }}$ | 3 | 2 F | R627 | 4 | 3 | R806 |  |  |


| A10 - MAIN BOARD (cont) <br> (SN B715000 AND ABOVE, 2246 1Y <br> SN B120000 AND ABOVE, 2246 MOD A) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (1) | SCHEM |  | CIRCUIT NUMBER | SCHEM NUMEER | BOARD | circuit | SCHEM NUMER | ${ }_{\text {coser }}^{\text {BOARD }}$ | (circuit | SCHEM NUMEER |  |
| R807 | 6 | ${ }_{9}$ | ${ }^{\text {R1010 }}$ | 7 | $\pi$ | R2711 | 7 | ${ }^{7 N}$ | 4260 | ${ }^{13}$ | ${ }^{5}$ |
| R8808 | ${ }_{6}^{6}$ | ${ }_{84}^{7 \%}$ | 号1020 | 7 | 7 | R2712 R2713 | 7 | ${ }_{7}^{7 M}$ | 4280 <br> 301 <br> 201 | 2 | ${ }_{9}^{55}$ |
| ${ }_{\substack{\text { R8899 } \\ \text { R810 }}}$ | ${ }_{6}^{6}$ | ${ }_{8}^{8 H}$ | ${ }_{\text {R1022 }}$ | 7 | $\pi$ |  | 7 | 7 M | U301 |  | 90 |
| ${ }_{\substack{\text { R8811 } \\ \text { R812 }}}$ | ${ }_{6}^{6}$ | ${ }_{84}^{73}$ | Rince | 7 | 7 | ${ }_{\text {Rer }}^{\text {R2715 }}$ | 7 | $\underset{7 M}{7 M}$ | U302 | ${ }^{5}$ | ${ }_{8 C}^{8 C}$ |
| ${ }_{\text {Rex }}^{\text {R812 }}$ | ${ }_{6}^{6}$ | ${ }_{8}^{88}$ | ${ }_{\text {RR1025 }}^{\text {R1024 }}$ | 7 | ${ }_{8}$ |  | 7 | ${ }_{7 \times}$ | U303 | ${ }_{5}^{13}$ | ${ }_{90}^{80}$ |
| ${ }^{\text {R812 }}$ | ${ }_{6}$ | ${ }^{8 /}$ | ${ }^{\text {R1022 }}$ | ${ }_{13}^{13}$ | ${ }^{89}$ | ${ }^{\text {R2718 }}$ | 7 | 7 M | 4303 | ${ }^{13}$ | ${ }_{9}$ |
| ${ }_{\substack{\text { R820 } \\ \text { R821 }}}$ | ${ }_{6}^{6}$ | ${ }_{7 J}^{8 /}$ | ¢, | ${ }^{13}$ | ${ }_{4 L}^{8 L}$ |  | 7 | ${ }_{8 N}^{8 N}$ | U304 | 5 13 | ${ }_{80}^{80}$ |
|  | ${ }_{6}^{6}$ | ${ }_{8}^{7}$ | ${ }_{\substack{\text { R11103 } \\ \text { R104 }}}$ | 3 | ${ }_{4 G}^{4 G}$ | ${ }_{\substack{\text { R27221 } \\ \text { R2722 }}}$ | 7 | ${ }_{9}^{9 \times 1}$ | ¢307 | 5 | 70 |
| ${ }_{\substack{\text { R825 } \\ \text { R823 }}}^{\text {Rer }}$ | ${ }_{6}$ | ${ }_{8}^{8 \mathrm{H}}$ | ${ }_{\text {R11105 }}$ | 3 | ${ }_{6 k}$ | ${ }_{\text {R2723 }}$ | 7 | ${ }_{9 \times}$ | U308 | ${ }_{5}$ | 70 |
| ${ }_{\substack{\text { R8826 } \\ \text { R827 }}}$ | ${ }_{6}^{6}$ |  | ${ }_{\substack{\text { R11106 } \\ \text { R.1188 }}}$ | 3 13 | ${ }_{4 G}^{6 K}$ |  | 7 | ${ }_{6}^{9 \times}$ | 4308 | ${ }_{5}^{13}$ | 70 |
| ${ }_{\text {Rex }}$ | ${ }_{6}$ | ${ }_{7}$ | ${ }_{\text {R11110 }}$ | ${ }^{13}$ | ${ }_{4 \mathrm{4}}^{4 \mathrm{G}}$ | ${ }_{\substack{\text { R22726 } \\ \text { R2727 }}}$ | 7 | ${ }_{7}^{6 L}$ | - 3309 | 5 13 | ${ }_{7 c}^{7 c}$ |
| ${ }^{\text {R8829 }}$ | ${ }_{6}$ | ${ }^{7 k}$ | ${ }^{\text {R11111 }}$ | 3 | ${ }_{5 K}^{5 k}$ | ${ }^{\text {R27228 }}$ | 7 | ${ }_{6}^{6 L}$ | 4310 | 5 | 100 |
| Re30 | 6 | ${ }_{8}^{8 K}$ | ${ }_{\text {R }}^{\substack{\text { R11112 }}}$ | ${ }_{3}^{3}$ | ${ }_{5 K}^{4 K}$ | ¢ | 7 | ${ }_{9}^{7 L}$ | U310 | ${ }_{5}^{13}$ | 100 100 |
| Re36 | ${ }_{1}^{6}$ | ${ }_{9 \times}^{9+}$ | R R1114 | 3 | $\stackrel{5 k}{41}$ | ${ }_{\substack{\text { R2734 } \\ \text { R2735 }}}$ | 7 | ${ }^{101}$ | 4311 | ${ }^{13}$ | ${ }^{100}$ |
| $\substack{\text { R883 } \\ \text { R83 }}_{\text {R8, }}$ | ${ }_{6}^{13}$ | ${ }_{8 \text { 8H }}^{9 \%}$ | ${ }_{\text {R }}^{\substack{\text { R11115 } \\ \text { R116 }}}$ | 3 3 | ${ }_{4}^{4 J}$ | ${ }_{\text {R2736 }}^{\text {R273 }}$ | 7 | , 10 K | U313 | 5 | ${ }_{8}^{8 E}$ |
| ${ }^{\text {R8555 }}$ | 6 | ${ }_{7}$ | ${ }^{\text {R1117 }}$ |  | 3 | ${ }_{\text {R2737 }}$ | 7 | 10k | U315 | 13 | ${ }_{8 F}$ |
| ${ }_{\substack{\text { R855 }}}^{\text {R85 }}$ | ${ }_{6}^{6}$ | ${ }_{8}^{8 \mathrm{8H}}$ | ¢ $\begin{gathered}\text { R1118 } \\ \text { R.120 }\end{gathered}$ | $3_{3}^{3}$ | ${ }_{5 K}^{4 J}$ | ${ }_{\substack{\text { R2738 } \\ \text { R2739 }}}$ | 7 | ${ }^{111}$ | 4316 <br> 316 | 5 | ${ }_{7}^{7 H}$ |
| ${ }_{\text {R880 }}$ | 6 | ${ }_{8}^{8 H}$ | ${ }_{\text {R11121 }}^{\text {R1120 }}$ | ${ }_{3}$ | ${ }_{5}^{5 k}$ | ${ }_{\substack{\text { R22739 } \\ \text { R270 }}}$ | 7 | 1114 | U421 |  | ${ }_{3 F}^{7 H}$ |
| ${ }_{\text {R8861 }}^{886}$ | ${ }_{6}^{6}$ | ${ }_{7}^{81}$ | ${ }_{\substack{\text { R21122 }}}^{\text {R1123 }}$ | 3 | $\stackrel{5}{5 \times}$ | ${ }_{\text {R2744 }}$ | 7 | ${ }_{8}^{89}$ | ${ }_{4} 4231$ | ${ }^{13}$ | ${ }^{3 F}$ |
| ${ }_{\substack{88662}}^{\text {R883 }}$ | ${ }_{6}^{6}$ | ${ }_{7 J}$ | ${ }_{\substack{\text { Rel123 } \\ \text { R.112 }}}^{\text {R12 }}$ | 3 | ${ }_{5}^{5 K}$ | ¢ | 7 | ${ }_{8 L}^{8 L}$ | ${ }_{\substack{\text { U4331 } \\ \text { U431 }}}^{4}$ | 13 <br> 13 | ${ }_{2 F}^{2 F}$ |
| ${ }^{\text {R877 }}$ | 6 | ${ }^{8}$ | ${ }^{\text {R1125 }}$ | 3 | 5 | ${ }^{\text {R2745 }}$ | 7 | 7N | U441 |  | $1{ }^{\text {+ }}$ |
| ${ }_{\substack{\text { R8772 } \\ \text { R873 }}}^{\text {Reg }}$ | ${ }_{6}^{6}$ | $\stackrel{9}{9}$ |  | ${ }_{3}^{3}$ | [51 | ${ }_{\text {Rer }}^{\substack{\text { R2750 } \\ \text { R251 }}}$ | 7 | ${ }_{9}^{8 M}$ | U441 | ${ }_{3}^{13}$ | ${ }_{3 H}^{1+}$ |
| R891 | 6 | ${ }_{8}$ | ${ }_{121128}$ | 3 | 5 | ${ }_{\text {R2758 }}$ | 7 | 9 | U442 | ${ }_{13}$ | ${ }_{3}$ |
| ${ }_{\substack{\text { R8932 }}}^{\text {R893 }}$ | ${ }_{6}^{6}$ | ${ }_{\text {8K }}^{8 K}$ | Rentilis | ${ }_{3}^{3}$ | ${ }_{46}^{4 G}$ | ${ }_{\text {Reler }}^{\text {R2760 }}$ | 7 | ${ }_{7 \mathrm{M}}^{10 \mathrm{M}}$ | U5011 | ${ }_{13}^{4}$ | ( ${ }_{\substack{5 M \\ 5 M}}$ |
| ${ }_{\text {R }}^{\text {R894 }}$ | 6 | ${ }_{8}^{81}$ | ${ }_{\text {R11133 }}$ | 3 | ${ }_{4}^{46}$ | ${ }_{\substack{\text { R2275 } \\ \text { R2733 }}}$ | 13 | 7 N | U502 | ${ }_{4}^{13}$ | ${ }_{4 N}^{5 M}$ |
| ${ }_{\substack{\text { R8896 } \\ \text { R897 }}}$ | ${ }_{6}^{6}$ | ${ }_{8 k}^{9}$ | ${ }_{\substack{\text { R1134 } \\ \text { R1135 }}}$ | 3 | ${ }_{4}^{4 H}$ | ${ }_{\substack{\text { R2784 } \\ \text { R2785 }}}$ | 7 | ${ }_{8 N}^{7 N}$ | U502 | ${ }_{4}^{13}$ | ${ }_{5}^{4 N}$ |
| ${ }^{\text {R898 }}$ | 6 | 7 K | ${ }^{\text {R1136 }}$ | 3 | $4{ }_{4}$ | ${ }_{\text {R2786 }}$ | 7 | ${ }_{60}$ | U503 | 13 | ${ }_{5 \mathrm{SN}}$ |
| ${ }_{\substack{\text { Ra0\% } \\ \text { R907 }}}^{\text {Reg }}$ | 7 | ${ }_{108}^{108}$ | ${ }_{\substack{\text { R } \\ \text { R11142 } \\ \text { R14 }}}$ | ${ }_{3}^{3}$ | ${ }_{4}^{4 H}$ | ${ }_{\substack{\text { R2787 } \\ \text { R2788 }}}$ | 7 | ${ }_{7 N}^{6 N}$ | U506 | ${ }_{13}$ | 100 100 100 |
| Reos | 7 | ${ }_{88}^{109}$ | ¢ | 3 3 3 | ${ }_{4}^{4 H}$ | ${ }_{\text {R2789 }}$ | 7 | ${ }_{80}$ | U600 | 4 | 2 m |
| ${ }_{\substack{\text { R909 } \\ \text { R90 }}}^{\text {R }}$ | 7 | ${ }_{108}^{88}$ |  | ${ }_{3}^{3}$ | ${ }_{3 G}^{4 H}$ | ${ }_{\text {RR2796 }}^{\text {R2795 }}$ | 7 | $7 / 4$ $6 \times$ | U600 | ${ }_{4}^{13}$ | ${ }_{1 \times}^{2 \mathrm{~m}}$ |
|  | ${ }_{13}$ | ${ }_{88}^{88}$ |  | 3 3 3 | ${ }_{46}^{46}$ |  |  |  | U601 | 13 | 1 N |
| ${ }_{\text {R996 }}$ | ${ }_{13}$ | ${ }_{9 A}$ | ${ }_{\text {R11158 }}^{\text {RR115 }}$ | ${ }_{13}$ | ${ }_{4}^{4 \mathrm{~L}}$ | R1201 |  |  | U6002 | ${ }_{13}^{4}$ | ${ }_{4 \mathrm{4K}}^{4 \mathrm{~K}}$ |
| ${ }_{\substack{\text { R9220 } \\ \text { R921 }}}^{\text {ari }}$ | 7 | ${ }_{88}^{88}$ |  | 13 13 | 50 | U112 | 13 | ${ }_{60}^{60}$ | U6603 | ${ }_{13}^{4}$ | ${ }_{2}^{2 K}$ |
|  | 7 | ${ }_{90}^{96}$ | ${ }_{\substack{\text { R11162 } \\ \hline 8.1163}}^{\text {R1, }}$ | 3 | ${ }_{\substack{6 K \\ 5}}^{5}$ | ${ }_{4}^{4122}$ | 1 | ${ }_{46}$ | U604 | 4 | ${ }_{2}^{21}$ |
| ${ }_{\substack{\text { Reg23 } \\ \text { R924 }}}^{\text {Re2 }}$ | 7 | ${ }_{96}^{90}$ |  | 3 3 | ${ }_{45}^{5 K}$ | U171 | ${ }_{1}^{13}$ | ${ }_{3 B}^{4 C}$ | U604 | ${ }_{4}^{13}$ | ${ }_{3 \mathrm{l}}^{2 \mathrm{~L}}$ |
| ${ }_{\text {R923 }}$ | 7 | ${ }_{88}^{98}$ | ${ }^{\text {R1210 }}$ | 2 | ${ }_{5}^{56}$ | 4171 | 13 | ${ }^{38}$ | U606 | 13 | зм |
| ${ }_{\text {R9931 }}$ | 7 | ${ }_{7 A}^{8 B}$ | ${ }_{\text {R }}^{\substack{\text { R21220 }}}$ | 2 | ${ }_{4 E}^{5 E}$ | 4172 <br> 172 <br> 172 | 13 | ${ }_{38}^{38}$ | U701 | ${ }_{13}$ | 101 |
| ${ }_{\substack{\text { R9932 } \\ \text { R933 }}}^{\text {and }}$ | 7 | ${ }_{78}^{78}$ |  | 2 | ${ }_{\substack{4 E \\ 3 E}}$ | $\underset{\substack{4173 \\ \hline 173}}{173}$ | 1 | ${ }^{36}$ | 4702 | 2 | ${ }_{9 K}$ |
| ${ }_{\substack{\text { R933 } \\ \text { R93 }}}^{\text {R939 }}$ | 7 | ${ }_{78}^{78}$ | $\substack{\text { R1230 } \\ R 12123}_{\text {R123 }}$ | ${ }_{2}$ | ${ }_{3}^{3 E}$ | ${ }_{4}{ }^{1773}$ | ${ }^{13}$ | ${ }_{3 A}^{3 C}$ | U802 | ${ }_{2}^{13}$ | ${ }_{7}^{\text {\% }}$ |
| ${ }_{\substack{\text { R933 } \\ \text { R936 }}}^{\text {a }}$ | 7 | ${ }_{78}^{78}$ |  | ${ }_{2}^{2}$ | ${ }_{\substack{2 E}}^{2 E}$ | 4175 <br> 1201 | 1 |  | U801 | ${ }_{13}$ | ${ }_{7}^{7 \%}$ |
|  | 7 | ${ }_{7}^{7 A}$ |  | 2 | ${ }_{\substack{\text { SF } \\ 5 \\ 5 \\ \hline}}$ | 4201 <br> 4201 <br> 202 | ${ }^{2}$ | $\underset{\substack{\text { 5H } \\ 5 \\ 5 \\ \hline \\ \hline}}{ }$ | U802 | ${ }^{13}$ | ${ }_{8}^{7 \%}$ |
| ${ }_{\substack{\text { R9938 } \\ \text { R993 }}}$ | 7 | ${ }_{7} 7$ | ${ }_{\substack{\text { R1251 } \\ \text { R1250 }}}^{\text {R125 }}$ | ${ }_{13}^{2}$ | ${ }_{50}^{55}$ | U202 | $\stackrel{2}{13}$ | ¢69 | U802 | 13 13 | ${ }_{98}^{8 H}$ |
| ${ }_{\substack{\text { R990 } \\ \text { R941 }}}^{\text {asi }}$ | 7 | ${ }_{8 \text { 8A }}^{7}$ | $\substack{\text { R1284 } \\ \text { R1285 }}_{\text {R128 }}$ | 2 | ${ }_{\substack{5 E \\ 5 E}}^{5}$ | 2203 <br> 203 <br> 203 | ${ }_{13}$ | 106 106 | บ930 | 7 | ${ }_{88}^{88}$ |
|  | 7 | ${ }_{5}^{4}$ | ${ }_{\substack{\text { R2701 } \\ \text { R2702 }}}$ | 7 | ${ }_{6 L}^{6 L}$ | U210 <br> U210 <br> 200 | ${ }_{13}$ | $\stackrel{\text { SE }}{5 \times}$ | บ931 | 7 | ${ }_{98}^{98}$ |
| R1003 R.1004 | 7 | ${ }_{4 L}^{4 M}$ | ${ }_{\substack{\text { Re2703 } \\ \text { R2704 }}}^{\text {R2, }}$ | 7 | ${ }_{7}^{7}$ | U220 <br> 220 <br> 220 | 2 13 1 |  | ¢ 4 ¢932 | ${ }_{1}^{13}$ | ${ }_{88}^{98}$ |
|  | 7 | ${ }_{8}^{8 L}$ | ${ }_{\substack{\text { Rer } \\ \text { R2704 } \\ \text { R2706 }}}$ | 7 | 7 | ciccue | ${ }_{2}^{13}$ | ${ }_{\text {3E }}^{4 E}$ | U1001 | 13 | 7 |
|  | 7 | ${ }_{8 L}$ | ${ }_{\substack{\text { R2706 } \\ \text { R2708 }}}$ | 7 | ${ }_{7}^{7 M}$ | U230 | ${ }_{2}^{13}$ | ${ }_{2 \times}^{3 E}$ | U1101 | ${ }_{7}$ | ${ }_{4 G}^{4 G}$ |
|  | 7 | ${ }_{7}^{8 L}$ | (R2709 | 7 | ¢ 7 | U240 <br> U260 <br> 23 | ${ }_{13}$ | $\substack{\begin{subarray}{c}{2 E \\ \text { SF }} }} \\{\hline} \end{subarray}$ | (1102 | ${ }_{3}^{13}$ |  |


| A10 - MAIN BOARD (cont) <br> (SN B715000 AND ABOVE, 2246 1Y <br> SN B120000 AND ABOVE, 2246 MOD A) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Clincuit | SCHEM NUMBER | ${ }_{\text {coser }}^{\text {BOARD }}$ | Clircuit NUMEER | SCHEM | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | clircuit <br> NUMBER | SCHEM NUMBER | BOARD LOCATION | circhit | SCHEM NUMEER | ${ }_{\text {coser }}^{\text {Boabd }}$ |
| 41102 | 13 | ${ }^{46}$ | w100 |  | ${ }^{50}$ | w505 | 4 | ${ }^{3 N}$ | W1203 | ${ }^{13}$ | J |
| $\mathrm{Cl}_{\substack{\text { U1103 } \\ \mathrm{U} \\ \hline 103}}$ | 3 13 13 |  | W101 | 1 | ${ }_{38}^{56}$ | W506 | ${ }_{4}^{4}$ | 3 c <br> 5 N | W1204 <br> W1205 | 13 13 | ${ }_{20}^{79}$ |
| ${ }^{41104}$ | 3 | ${ }_{4}$ | W103 | 13 | ${ }_{36}$ | W510 | 4 | ${ }_{6 M}$ | ${ }_{W}^{W} 1209$ | ${ }_{13}$ | ${ }_{4 j}^{2 J}$ |
| ¢ | ${ }_{3}^{13}$ | ${ }_{5 L}^{4 \mathrm{H}}$ | W200 | 2 | ${ }_{66}^{66}$ | W603 | 4 | ${ }_{4 L}^{4 M}$ | W1210 | ${ }^{13}$ | ${ }^{2 J}$ |
| ${ }^{4} \mathrm{U} 1106$ | ${ }_{13}$ | ${ }_{5}^{5 L}$ | $\underset{\substack{\text { W203 } \\ \text { W203 }}}{ }$ | 2 | ${ }_{6}^{66}$ | W600 | ${ }_{4}^{4}$ | ${ }_{6}{ }^{4}$ |  | 13 13 | ${ }_{68}^{96}$ |
|  |  |  | W204 | 2 | ${ }^{6 H}$ | W606 | ${ }^{13}$ | ${ }^{2 \mathrm{~K}}$ | W 1218 | 13 | ${ }_{6}{ }^{\text {H }}$ |
| VR301 | 5 | ${ }_{6 F}^{75}$ | ¢W205 <br> W206 | ${ }_{2}^{2}$ | ${ }_{66}^{66}$ | ${ }_{\text {W6ior }}^{\text {W607 }}$ | ${ }_{4}^{13}$ | ${ }_{3 \mathrm{~s}}{ }^{\text {m }}$ | W.1221 wi22 | 13 13 | ${ }_{60}^{2 J}$ |
| v®303 | 5 | ${ }_{96}^{96}$ | W207 | 2 | ${ }_{69}^{66}$ | W611 | ${ }_{4}^{4}$ | ${ }^{3 N}$ | ${ }_{\text {W }}{ }^{1222}$ | 13 | ${ }_{70}$ |
| V¢9304 | 5 5 | ${ }_{9 F}^{96}$ | ¢W208 <br> W209 <br> 102 | ${ }_{2}^{2}$ | ${ }_{66}^{69}$ | ¢ $\begin{gathered}\text { W802 } \\ \text { woos }\end{gathered}$ | 5 6 | ${ }_{\substack{86 \\ 69}}$ | ${ }_{\text {wi231 }}{ }^{1237}$ | 13 | ${ }_{8 K}^{8 K}$ |
| vR309 | 5 | ${ }^{79}$ | W210 |  | ${ }^{106}$ | W806 | 6 | ${ }^{7}$ | W1237 | 13 13 | ${ }_{70}^{7 k}$ |
| VR310 | ${ }_{5}^{5}$ | ${ }_{8 E}^{96}$ | W223 | ${ }_{2}^{2}$ | ${ }_{2 \mathrm{c}}^{50}$ | W807 | ${ }_{6}^{6}$ | ${ }_{7 \mathrm{~K}}^{69}$ | ${ }_{\text {W }}^{\mathbf{W} 248}$ | 13 | ${ }_{6} 6$ |
|  | 5 | ¢ | ¢W232 <br> W304 | 2 2 5 | ${ }_{\substack{2 E \\ 86}}^{\substack{26}}$ | We8 $\begin{aligned} & \text { W880 } \\ & \text { W811 }\end{aligned}$ | 6 | $\substack{64 \\ 54 \\ \hline 6 \\ \hline}$ | W1299 W1250 | 13 13 | ${ }_{8 K}^{6 H}$ |
| ${ }_{\text {l }}^{\text {VR8201 }}$ VR2701 | ${ }_{7}^{6}$ | ${ }_{69}^{81}$ | W304 | ${ }^{5}$ | ${ }_{10 \mathrm{c}}^{80}$ | ¢8811 | ${ }_{2}^{6}$ | ${ }_{86}^{5 H}$ | W1251 | 13 | ${ }_{6}{ }^{\text {H }}$ |
|  |  |  | W4001 | 3 | ${ }_{3}^{4 H}$ | ${ }_{\text {w900 }}$ | 7 | ${ }_{94}^{98}$ | W1252 | 13 <br> 13 | ${ }_{6 \mathrm{~F}}^{6 \mathrm{~F}}$ |
| ${ }_{\text {w9 }}^{\text {w9 }}$ | 7 | ${ }_{10 \mathrm{~m}}^{80}$ | W4006 | 3 | $3_{3}^{3 H}$ | W1000 W1010 | 7 | ${ }_{6 K}^{4 L}$ | ${ }_{\text {w }}^{\mathbf{W} 1277}$ | 13 | ${ }_{8 \times}$ |
| w $\begin{aligned} & \text { w } 11 \\ & \text { w12 }\end{aligned}$ | 1 | ${ }_{54}^{64}$ | W414 W415 | 2 | $\stackrel{3}{35}$ | w102 w1103 wion | 3 3 3 | ${ }_{6 K}^{31}$ | $W_{1288} 128$ <br> W2302 | 7 | ${ }_{100}^{60}$ |
|  | 1 | ${ }_{3}^{5 A}$ | W4ita | ${ }_{2}^{2}$ |  | W1103 | 3 13 13 | $\stackrel{3 K}{3 \%}_{3}$ | W2302 | 13 | 100 110 100 |
| W14 | $\frac{1}{7}$ | ${ }_{7}^{2 \mathrm{~K}}$ | W41168 | ${ }_{2}^{2}$ | ${ }_{3 \mathrm{l}}^{3 \mathrm{E}}$ | W1105 | ${ }_{3}^{13}$ | ${ }_{6 L}^{4}$ | ${ }_{\substack{\text { We304 } \\ \text { W2502 }}}$ | 7 | ${ }_{11}^{110}$ |
| w 17 W18 | ${ }_{6}^{6}$ | ${ }_{8 K}^{7 k}$ | W501 W502 | ${ }_{4}^{13}$ | 1/ 3 3 | w1108 $\mathbf{W 1 2 0 0}$ | 3 <br> 13 <br> 1 | ${ }_{\substack{56 \\ 65}}$ | W2502 | ${ }_{7}^{13}$ | 11 $6 \times$ |
| ${ }_{\text {w }}^{\text {w } 19}$ | ${ }_{2}$ | $\substack{\text { 10H } \\ \text { OH }}$ | $\underset{\substack{\text { W5032 } \\ \text { W504 }}}{ }$ | 4 | ( $\begin{aligned} & \text { 3M } \\ & 3 M\end{aligned}$ | w 1200 <br> w1202 | 13 13 13 | ${ }_{\substack{\text { 9\% } \\ 9}}$ | Y600 |  |  |
|  |  |  |  |  |  |  |  |  | Y600 | 4 | 3 |





246 1），and SN B110100 to B119999（2246 MOD A）．

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\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline \multicolumn{12}{|c|}{\begin{tabular}{l}
A10－MAIN BOARD（cont） \\
（SN B710100 to B714999， 2246 1Y \\
SN B110100 to B119999， 2246 MOD A）
\end{tabular}} \\
\hline CiRCUİ \& SCHEM \&  \&  \& SCHEM \&  \& cilicuit \& SCHEM \& \({ }_{\text {cte }}^{\text {BOAAD }}\) \& （circuit \& SCHEM \& （tocation \\
\hline R277 \& \({ }^{2}\) \& 4 \& \({ }^{\text {R347 }}\) \& 5 \& \({ }^{8 E}\) \& \({ }^{\text {R443 }}\) \& \({ }^{3}\) \& \({ }^{2 H}\) \& \({ }^{\text {R } 628}\) \& 4 \& \({ }^{2 \mathrm{~K}}\) \\
\hline （R278 \& 2 \& \({ }_{3 F}^{3 F}\) \&  \& 5 \& \({ }_{7}^{8}\) \& \({ }_{\substack{\text { R444 } \\ \text { R45 }}}^{\text {den }}\) \& \(3_{3}^{3}\) \& \({ }_{2}^{3 \mathrm{~F}}\) \& ¢ \& \({ }_{4}^{4}\) \& \({ }_{31}^{2 N}\) \\
\hline \({ }^{\text {R27280 }}\) \& \({ }^{2}\) \& \({ }_{3 F}\) \& \({ }_{\text {R3350 }}\) \& 5 \& \({ }_{7} 7\) \& \({ }_{\text {R446 }}\) \& \& \({ }^{3 F}\) \& \({ }_{\substack{\text { R6636 } \\ \text { R63 }}}\) \& \({ }_{4}^{4}\) \& 5N \\
\hline \({ }_{\substack{\text { R2828 } \\ \text { R282 }}}^{\text {R23 }}\) \& \({ }_{13}^{2}\) \& \({ }_{3 F}^{3 F}\) \& \({ }_{\substack{\text { Ra3508 } \\ \text { Rasoc }}}\) \& 5 \& \({ }_{7}^{7 H}\) \& \({ }_{\text {R448 }}^{\text {R447 }}\) \& 3
3 \& \({ }_{2 F}^{2 F}\) \& \({ }_{\substack{\text { Re638 } \\ \text { R63 }}}^{\text {Re }}\) \& \({ }_{4}^{4}\) \& \({ }_{3}^{3 N}\) \\
\hline \({ }_{\text {R283 }}\) \& 13 \& \({ }^{69}\) \& \({ }_{\text {R3505 }}\) \& 5 \& \({ }^{7}\) \& R4449
R494 \& 3 \& \({ }^{2 F}\) \& \({ }_{\text {R639 }}\) \& 4 \& \({ }^{3 L}\) \\
\hline \({ }_{\text {R }}^{\text {R284 }}\) \& \({ }_{2}^{2}\) \& \({ }_{65}^{65}\) \& \({ }_{\substack{\text { R3522 } \\ \text { R353 }}}\) \& \({ }_{6}^{5}\) \& \({ }_{90}^{7 \%}\) \&  \& \({ }_{3}^{3}\) \&  \&  \& \({ }_{4}^{4}\) \& \({ }_{4}^{3 L}\) \\
\hline \({ }_{\text {R }}^{\substack{\text { R288 } \\ \text { R288 }}}\) \& \({ }_{2}\) \& \({ }_{5}^{56}\) \& \({ }_{\text {R }}^{\text {R335 }}\) \& \({ }_{5}^{5}\) \& \({ }_{96}\) \& \({ }_{\substack{\text { Ras } \\ \text { Ras } \\ \text { Ras }}}\) \& 3 \& \({ }^{16}\) \&  \& \({ }_{4}^{4}\) \& \({ }_{3}^{3 M}\) \\
\hline \(\underbrace{\text { R287 }}_{\text {R288 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {¢F }}^{5 F}\) \& \({ }_{\substack{\text { R3355 } \\ \text { R35 }}}^{\text {Re3 }}\) \& 5 \& \({ }_{96}^{96}\) \& \({ }_{\text {R }}^{\substack{\text { Ras4 } \\ \text { R43 }}}\) \& \({ }_{3}\) \& \({ }_{26}^{26}\) \& \({ }_{\text {R664 }}\) \& 4 \& \({ }_{3}{ }^{\text {m }}\) \\
\hline \({ }_{\text {R289 }}\) \& 2 \& \({ }_{5}^{5 F}\) \& \({ }^{\text {R3578 }}\) \& 5 \& \({ }_{8}^{8 F}\) \& \({ }^{\text {R4455 }}\) \& 3 \& \({ }^{1+1}\) \& Re64 \& \({ }_{4}^{4}\) \& \({ }_{3}^{3 M}\) \\
\hline \({ }_{\substack{\text { R2900 } \\ \text { R291 }}}^{\text {a }}\) \& \({ }_{2}^{2}\) \&  \& \({ }_{\text {Rex }}^{\text {R3577 }}\) \& 5 \& \({ }_{88}^{8 F}\) \& \({ }_{\substack{\text { Ras6 } \\ \text { R460 }}}^{\text {a }}\) \& \({ }_{3}^{3}\) \& 1980 \&  \& \({ }_{4}^{4}\) \& \({ }_{1}^{3 M}\) \\
\hline \({ }_{\text {R }}^{\substack{\text { R2929 } \\ \text { R293 }}}\) \& 2 \& \({ }^{56}\) \& \({ }_{\text {cher }}^{\text {R3578 }}\) \& 5 \& \({ }_{8}^{8 F}\) \&  \& 3 \& \({ }^{16}\) \&  \& \({ }_{4}^{4}\) \& \({ }_{3}^{3 K}\) \\
\hline \({ }_{\text {Rer }}^{\text {R293 }}\) \& 2 \& \({ }_{56}\) \& \({ }_{\substack{\text { R338 } \\ \text { R359 }}}\) \& \({ }_{5}\) \& \({ }_{86}\) \& \({ }_{\substack{\text { R463 }}}^{\text {R462 }}\) \& \({ }_{3}\) \& \({ }_{1}\) \& \({ }_{\text {R650 }}\) \& 4 \& 2 M \\
\hline \({ }_{\substack{\text { R225 } \\ \text { R296 }}}^{29}\) \& \({ }_{2}^{2}\) \&  \& \({ }_{\substack{\text { R3360 } \\ \text { R361 }}}\) \& \({ }_{5}^{5}\) \& \({ }_{86}^{96}\) \& \({ }_{\text {RR471 }}^{\text {R470 }}\) \& \({ }_{3}^{3}\) \& \({ }_{1}^{16}\) \&  \& \({ }_{4}^{4}\) \& \({ }_{4}^{4 M}\) \\
\hline  \& 13
13
13 \&  \&  \& 5 \& \({ }_{\substack{8 \% \\ 8 \%}}^{80}\) \& \({ }_{\substack{\text { R4472 } \\ \text { R473 }}}^{\text {and }}\) \& 3
3 \& \(\underset{1}{1+}\) \& \({ }_{\substack{\text { R6533 } \\ \text { Re54 }}}^{\text {Res }}\) \& \(\stackrel{4}{4}\) \& \({ }_{3 \mathrm{c}}^{3 \mathrm{~K}}\) \\
\hline  \& \({ }_{1}^{13}\) \& \({ }_{3 E}^{3 F}\) \& \(\substack{\text { R．363 } \\ \text { R364 }}_{\text {P3，}}\) \& 5 \& \({ }_{96}^{9 F}\) \& \({ }_{\substack{\text { R4473 }}}^{\text {R47 }}\) \& \({ }_{3}\) \& \(\underset{1}{1+1}\) \& \({ }_{\text {R655 }}\) \& 4 \& \({ }_{31}\) \\
\hline R3301
R302 \& \({ }_{5}^{5}\) \& \({ }_{76}^{96}\) \& \({ }_{\text {R }}^{\text {R365 }}\) \& 5 \& \({ }_{8 C}^{108}\) \&  \& 3 \& \({ }_{15}^{15}\) \& \({ }_{\substack{\text { R655 } \\ \text { R65 }}}^{\text {Res }}\) \& \({ }_{4}^{4}\) \& \({ }_{4}^{4 K}\) \\
\hline  \& \({ }_{5}^{5}\) \& \({ }_{88}^{8{ }_{8}}\) \& \({ }_{\substack{\text { ค } \\ \text { R367 }}}^{\text {ase }}\) \& 5 \& \({ }_{80}^{80}\) \& \({ }_{8}^{84778}\) \& 3 \& \({ }_{\text {1F }}^{1 / 5}\) \&  \& \({ }_{4}^{4}\) \& \({ }_{4 \times}^{4 \mathrm{~K}}\) \\
\hline  \& 5
5 \& \({ }_{86}^{8 F}\) \&  \& \({ }^{6}\) \& \({ }^{80}\) \&  \& 3
3 \& \({ }_{1}^{1 / 5}\) \& \({ }_{\substack{\text { R662 }}}^{\text {R6939 }}\) \& \({ }_{4}^{4}\) \& \({ }_{2}{ }_{2}\) \\
\hline  \& \({ }_{5}^{5}\) \& \({ }_{7}^{70}\) \& \({ }_{\text {R3371 }}\) \& \({ }_{5}^{5}\) \& \({ }_{96}^{96}\) \& R4880 \& \({ }^{13}\) \& \({ }_{4}^{4 F}\) \& \({ }_{\substack{\text { R6663 } \\ \text { R66 }}}^{\text {Re }}\) \& \({ }_{4}^{4}\) \& \({ }_{3}^{3 K}\) \\
\hline \({ }_{\text {¢ }}^{\text {R308 }}\) \& \({ }_{5}^{5}\) \& \(7{ }^{7}\) \& \({ }_{\text {R373 }}\) \& 5 \& \({ }^{106}\) \& \({ }_{\text {R483 }}^{\text {Ras }}\) \& \({ }^{3}\) \& \({ }^{3 F}\) \& \({ }_{\substack{\text { Re665 } \\ \text { R668 }}}\) \& \({ }_{4}^{4}\) \& \({ }_{4}^{4 \mathrm{~K}}\) \\
\hline  \& 5 \& \({ }_{70}^{90}\) \& \({ }_{\substack{\text { R3375 }}}^{\text {R374 }}\) \& －\({ }_{5}^{13}\) \& － \&  \& 3
3 \& \(\underset{\sim}{11}\) \& \({ }_{\substack{\text { Re69 }}}^{\text {R66 }}\) \& \({ }_{4}^{4}\) \& \({ }_{31}^{4 K}\) \\
\hline \({ }_{\text {R311 }}\) \& \({ }_{5}^{5}\) \& \({ }_{70}^{80}\) \& \({ }_{\text {R3376 }}^{\text {R337 }}\) \& 5 \& \({ }_{8}^{10 \mathrm{E}}\) \& Ras6 \& 3 \& 1E \& \({ }_{\substack{\text { R } \\ \text { R677 }}}^{\text {¢67 }}\) \& \(\stackrel{4}{4}\) \& \({ }_{3}^{4 \mathrm{M}}\) \\
\hline \({ }_{\text {a }}^{\text {R3313 }}\) \& 5 \& 70 \& \({ }_{\text {R }}^{\text {R378 }}\) \& 5 \& \(\stackrel{\text { ® }}{ }\) \& \({ }_{\text {R490 }}\) \& 3 \& \(2{ }^{2 J}\) \& \(\substack{\text { R6672 } \\ \text { R673 }}_{\text {and }}\) \& \({ }_{4}^{4}\) \& \({ }_{4}^{2 M}\) \\
\hline \(\underbrace{\text { R3138 }}_{\text {R313C }}\) \& 5
5 \& 70 \& \({ }_{\substack{\text { R3379 }}}^{\text {R37 }}\) \& 5 \& \({ }_{\text {TH }}\) \& \({ }_{\substack{\text { Ra992 } \\ \text { R492 }}}^{\text {Ren }}\) \& 3
3 \& \(3_{3}^{3}\) \&  \& \({ }_{4}^{4}\) \& \({ }_{4 M}^{4 M}\) \\
\hline \({ }_{\text {R314 }}^{\text {R330 }}\) \& \({ }_{5}^{5}\) \& \({ }_{70}^{70}\) \& \({ }_{\substack{\text { R3381 } \\ \text { R382 }}}\) \& 5 \& \({ }_{76}^{10 \%}\) \& \({ }_{\substack{\text { R4933 } \\ \text { R994 }}}\) \& \(3_{3}^{3}\) \& \({ }_{21}^{2 J}\) \& \(\substack{\text { Re88 } \\ \text { R68 }}_{\text {¢ }}\) \& \(\stackrel{4}{4}\) \& \({ }_{3}^{3 L}\) \\
\hline \({ }_{\text {R3315 }}\) \& 5
5
5 \& \({ }^{105}\) \& \(\substack{\text { Ra33 } \\ \text { R304 }}_{\text {R392 }}\) \& 5
5
5 \& 9 ga \& \({ }_{\substack{\text { R495 }}}^{\text {Ra99 }}\) \& 3 \& 2 \&  \& 4 \& \({ }_{\substack{3 K \\ 2 k}}\) \\
\hline \(\substack{\text { R336 } \\ \text { R317 }}_{\text {ald }}\) \& \({ }^{5}\) \& \({ }_{8 \times} 7\) \& \({ }_{\substack{\text { R3384 } \\ \text { R339 }}}\) \& 5 \& \({ }_{96}^{80}\) \& \({ }_{\substack{\text { Ra99 } \\ \text { R999 }}}^{\text {Res }}\) \& 3 \& \({ }_{2}^{2 J}\) \& \({ }_{\substack{\text { R6899 } \\ \text { R690 }}}\) \& \({ }_{4}^{4}\) \& \({ }_{2}^{2 K}\) \\
\hline  \& 5 \& \({ }_{8 \mathrm{l}}^{8 \mathrm{EF}}\) \& \({ }_{\text {R387 }}^{\text {R336 }}\) \& 5 \& \({ }_{86}^{96}\) \&  \& \({ }_{4}^{3}\) \& \({ }_{12}^{3}\) \& \({ }_{\substack{\text { R6691 } \\ \text { R692 }}}^{\text {Re9 }}\) \& 4 \& \({ }_{2 L}^{2 L}\) \\
\hline \({ }_{\substack{\text { R3320 } \\ \text { R32 }}}\) \& 5 \& \({ }_{80}^{8 E}\) \& \({ }_{\substack{\text { R3888 } \\ \text { R30 }}}^{\text {asi }}\) \& \(\stackrel{5}{13}\) \& \({ }_{88}^{86}\) \& \({ }_{\substack{\text { R5022 } \\ \text { R503 }}}^{\text {Rem }}\) \& \({ }_{7}^{4}\) \& 50
100
100 \& \({ }_{\substack{\text { R6933 } \\ \text { R69 }}}^{\text {and }}\) \& \({ }_{4}^{4}\) \& \({ }_{22}^{2 L}\) \\
\hline \({ }_{\substack{\text { Ra3218 } \\ \text { R321C }}}^{\text {R23，}}\) \& 5 \& \({ }_{80}^{80}\) \&  \& （13 \& \({ }_{7}^{70}\) \&  \& \({ }_{7}^{13}\) \& 90

100 \&  \& ${ }_{2}^{13}$ \& $\stackrel{9}{9}$ <br>
\hline ${ }_{\substack{\text { R33210 }}}^{\text {R3312 }}$ \& 5 \& ${ }_{80}^{80}$ \& ${ }_{\substack{\text { R3394 } \\ \text { R339 }}}$ \& 5 \& ${ }_{1} 10 \mathrm{~F}$ \& ${ }_{\substack{\text { R5510 } \\ \text { R510 }}}^{\text {R }}$ \& 7 \& 100
100 \& ${ }_{\substack{87703 \\ \hline 8709}}$ \& 2 \& 100 <br>
\hline ${ }_{\substack{\text { R322F } \\ \text { R321F }}}^{\text {R3，}}$ \& $\stackrel{5}{5}$ \& ${ }_{80}^{80}$ \& ${ }_{\substack{\text { Ra39 } \\ \text { R396 }}}^{\text {Rem }}$ \& 5 \& ${ }_{\text {8E }}^{8 \mathrm{EE}}$ \& ${ }_{\substack{\text { R } \\ \text { R6012 }}}^{\text {and }}$ \& 7 \& ${ }_{3 N}^{100}$ \& R706
R707 \& ${ }_{2}^{2}$ \& ${ }_{10 \mathrm{~K}}^{9}$ <br>
\hline ${ }_{\substack{\text { R3322 } \\ \text { R323 }}}$ \& 5 \& ${ }_{7}^{7 \%}$ \& R4a01
R402 \& 3
3
3 \& ${ }_{\substack{3 F \\ 3 F}}$ \&  \& 4 \&  \& R7088
R709 \& 2
13 \& $\stackrel{10 H}{90}$ <br>
\hline  \& 5 \& ${ }_{70}^{76}$ \& ${ }_{\substack{\text { R4002 } \\ \text { R40 }}}$ \& ${ }^{3}$ \& ${ }_{3}^{3 F}$ \& ${ }_{\substack{\text { R．603 } \\ \text { R604 }}}$ \& ${ }_{4}^{4}$ \& ${ }^{\text {in }}$ \& ${ }_{\text {R7710 }}$ \& 2 \& ${ }^{10 \mathrm{~K}}$ <br>
\hline ${ }_{\text {R }}^{\text {R327 }}$ \& － \& 70
$9 F$ \& R4411 \& ${ }_{3}^{3}$ \& ${ }_{3 G}^{3 G}$ \& ${ }_{\substack{\text { R6005 } \\ \text { R60 }}}^{\text {R }}$ \& ${ }_{4}^{4}$ \& ${ }_{2}^{1 N}$ \& R711
$\mathrm{R7712}$ \& 2 \& ${ }_{10}^{20}$ <br>
\hline ${ }_{\substack{\text { Ra328 } \\ \text { R329 }}}^{\text {are }}$ \& 5
5 \& ${ }_{96}^{8 E}$ \& ${ }_{\substack{\text { R4112 } \\ \text { R413 }}}^{\text {and }}$ \& ${ }_{3}^{3}$ \& －${ }_{36}{ }^{3 G}$ \&  \& 4 \& $\stackrel{2 \mathrm{2K}}{2 \mathrm{~K}}$ \& R715
$\mathrm{R716}$ \& ${ }_{2}^{2}$ \& ${ }_{1}^{10 \mathrm{~K}}$ <br>
\hline  \& 5
5
5 \& ${ }_{88}^{9 E}$ \&  \& 3
3
3 \& $\stackrel{3}{36}$ \& $\substack{\text { R6611 } \\ \text { R612 }}_{\text {R69 }}$ \& 4 \& 3 3
3
3 \& ${ }_{\substack{\text { R7717 } \\ \text { R718 }}}$ \& 2 \& ${ }_{9 K}^{90}$ <br>
\hline  \& 5
5
5 \& ${ }_{\substack{76 \\ 86 \\ 89}}$ \& ¢ \& 3
3
3 \& 26
26
26 \&  \& 4 \& － \& ${ }_{\substack{\text { R719 } \\ \text { R720 }}}^{\text {R717 }}$ \& 2 \& － <br>
\hline ${ }_{\substack{\text { R333 } \\ \text { R334 }}}^{\text {R3，}}$ \& ${ }_{5}^{5}$ \& ${ }_{86}^{86}$ \& ${ }_{\text {Rata }}^{\text {Ral7 }}$ \& ${ }_{3}^{3}$ \& ${ }_{2}^{2 \mathrm{Ca}}$ \& ${ }_{\substack{\text { R614 } \\ \text { R615 }}}^{\text {R }}$ \& ${ }_{4}^{4}$ \& ${ }_{4 K}^{3 L}$ \& R720
R 721 \& ${ }_{2}^{2}$ \& ${ }_{96}^{10 \mathrm{H}}$ <br>
\hline  \& 5 \& ${ }_{9 \sim}^{9 F}$ \& ${ }_{\substack{\text { R4421 } \\ \text { Rat2 }}}^{\text {a }}$ \& ${ }_{3}^{3}$ \& 26
29 \& ${ }_{\substack{\text { R6616 } \\ \text { R667 }}}^{\text {R61 }}$ \& 4 \& $3 \mathrm{3k}$
3 K \& $\underset{\substack{\text { R722 } \\ \text { R723 }}}{\text { R2，}}$ \& ${ }_{13}^{2}$ \& $\stackrel{\text { 9K }}{\text { g\％}}$ <br>
\hline  \& ${ }_{5}^{5}$ \& ${ }_{8}^{8 F}$ \& ${ }_{\substack{\text { R4223 } \\ \text { R24 }}}$ \& 3
3 \& ${ }^{3 G}$ \& ${ }^{\text {R618 }}$ \& 4 \& ${ }^{3 \times}$ \& ${ }_{\text {R724 }}^{\text {R72 }}$ \& 2 \& ${ }_{9}$ <br>
\hline  \& ${ }^{5}$ \& ${ }_{\text {ge }}^{8 \mathrm{EE}}$ \& ${ }_{\substack{\text { R4424 } \\ \text { R422 }}}^{\text {and }}$ \& ${ }_{3}^{3}$ \& －${ }^{36}$ \& ${ }_{\text {R620 }}^{\text {R619 }}$ \& ${ }_{4}^{4}$ \& － \& ${ }_{\substack{\text { R722 }}}^{\text {R722 }}$ \& 2 \& ${ }_{10}^{90}$ <br>
\hline  \& 5 \& ${ }_{\text {gF }}^{7 \mathrm{ga}}$ \& ¢ \& ${ }_{3}^{3}$ \& ${ }_{2}^{2 H}$ \& ${ }_{\substack{\text { R6821 } \\ \text { R62 }}}^{\text {R }}$ \& ${ }_{4}^{4}$ \& ${ }_{3}^{4}$ \& ${ }_{\substack{\text { R727 } \\ \text { R728 }}}^{\text {R }}$ \& 2 \& $\underset{\substack{10 \mathrm{~K} \\ 10 \mathrm{H}}}{ }$ <br>
\hline  \& 5
5
5 \& ${ }_{\text {9F }}^{\text {9F }}$ \& R4331
R432
R43 \& 3
3

3 \& \begin{tabular}{l}
2 l <br>
2 Ca <br>
<br>
\hline

 \& ¢ \& ${ }_{4}^{4}$ \& ${ }^{3 \mathrm{3K}}$ \& 

R729 <br>
R730 <br>
\hline
\end{tabular} \& 2 \& lor

$\substack{10 \mathrm{H} \\ \text { Ok }}$ <br>
\hline  \& 5
5

5 \& ${ }_{\substack{\text { be } \\ 8 \times 5}}^{\text {ge }}$ \&  \& ${ }_{3}^{3}$ \& ${ }_{2}^{26}$ \& \begin{tabular}{c}
R624 <br>
R622 <br>
\hline

 \& ${ }_{4}^{4}$ \& ${ }_{24}^{24}$ \& 

R730 <br>
$\substack{\text { R773 } \\
\hline \\
\hline 733}$
\end{tabular} \& 2 \& ${ }_{9}^{9}$ <br>

\hline （ $\begin{gathered}\text { R345 } \\ \text { R346 }\end{gathered}$ \& －${ }_{5}^{13}$ \& ${ }_{8 \times}^{8 E}$ \& （R4441 \& ${ }_{3}^{3}$ \& ${ }_{2}^{2 H}$ \& ${ }_{\text {R }}^{\text {R627 }}$ \& ${ }_{4}^{4}$ \& ${ }_{3}^{4 L}$ \& ${ }_{\text {R }}^{\text {R733 }}$ \& $\stackrel{2}{13}$ \& ${ }_{10 \mathrm{~K}}^{10 \mathrm{H}}$ <br>
\hline
\end{tabular}



| A10－MAIN BOARD（cont） <br> （SN B710100 to B714999， 2246 1Y <br> SN B110100 to B1 19999， 2246 MOD A） |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| （lircuit | SCHEM NUMEER |  | $\begin{aligned} & \text { CIRCUIT } \\ & \text { NUMBER } \end{aligned}$ | SCHEM NUMER | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { CIRCUTUT } \\ & \hline \end{aligned}$ | $\begin{aligned} & \text { SCHEM } \\ & \text { NUMBER } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | $\begin{gathered} \text { CIRCUTT } \\ \text { NUMBER } \end{gathered}$ | SCHEM NUMBER |  |
| R277 | ${ }^{2}$ | ${ }^{4}$ | R347 | 5 | ${ }_{8 E}$ | ${ }^{\text {R443 }}$ |  | ${ }^{2 H}$ | ${ }^{\text {R6228 }}$ | 4 | ${ }^{2 k}$ |
| ${ }_{\text {R278 }}^{\text {R27 }}$ | 2 | ${ }_{3}^{3 F}$ | ${ }^{\text {R3388 }}$ | 5 | ${ }_{8}^{84}$ | ${ }^{\text {R } 2449}$ | $3_{3}^{3}$ | ${ }^{3 \mathrm{H}}$ | ${ }_{\substack{\text { R6330 }}}^{\text {R63 }}$ | 4 | ${ }_{2}^{2 N}$ |
| ${ }_{\text {R }}^{\text {R278 }}$ | 2 | ${ }_{3 F}^{3 F}$ | ${ }_{\substack{\text { R3399 } \\ \text { R350 }}}^{\text {R }}$ | $\stackrel{5}{5}$ | ${ }_{7}^{7 H}$ |  | ${ }_{3}^{3}$ | ${ }_{3 F}^{2 F}$ | ${ }_{\substack{\text { R633 } \\ \text { R63 }}}^{\text {Re3 }}$ | ${ }_{4}^{4}$ | ${ }_{5}^{3 L}$ |
| ${ }_{\text {R282 }}^{\text {R281 }}$ | ${ }_{13}^{2}$ | ${ }_{3}^{3 F}$ |  | 5 | ${ }_{7 \%}^{7 H}$ | $\substack{\text { R447 } \\ \text { R488 }}^{\text {R }}$ | $3_{3}^{3}$ | ${ }_{2 F}^{2 F}$ | ${ }_{\substack{\text { Re33 }}}^{\text {R637 }}$ | $\stackrel{4}{4}$ | ${ }_{3}^{3 N}$ |
| ${ }_{\text {R283 }}$ | 13 | ${ }_{69}$ | ${ }_{\text {R }}^{\substack{\text { R350E }}}$ | 5 | $7{ }_{7}$ | ${ }_{\substack{\text { R449 } \\ \text { R448 }}}^{\text {R4，}}$ | 3 | ${ }_{3 F}^{2 F}$ | ${ }_{\substack{\text { Re63 } \\ \text { Res }}}^{\text {R63 }}$ | ${ }_{4}^{4}$ | ${ }_{31}{ }_{3}$ |
| R284 | 2 | ${ }_{65}^{65}$ | ${ }_{\substack{\text { R352 } \\ \\ 8.535 \\ \hline}}$ | 5 | ${ }^{7 \%}$ | ${ }^{\text {Ras5 }}$ | 3 | 16 | ${ }_{\text {R }}^{\text {R640 }}$ | 4 | ${ }^{3 L}$ |
| ${ }_{\text {R286 }}^{\text {R285 }}$ | ${ }_{2}^{2}$ | ${ }_{\text {ck }}^{6 \mathrm{EE}}$ | ${ }_{\substack{\text { R3353 } \\ \text { R354 }}}^{\text {Res }}$ | ${ }_{5}^{6}$ | ${ }_{96}^{90}$ | ${ }_{\substack{\text { Rasi } \\ \text { R452 }}}$ | 3 3 | ${ }_{16}^{16}$ | ${ }_{\substack{\text { R64 } \\ \text { R64 }}}^{\text {Re9 }}$ | ${ }_{4}^{4}$ | ${ }_{3 \mathrm{Mm}}^{4}$ |
| ${ }_{\text {R287 }}^{\text {R288 }}$ | 2 | ${ }_{5}^{55}$ | ${ }^{\text {R3355 }}$ | 5 | ${ }^{96}$ | ${ }^{\text {Ra453 }}$ | ${ }^{3}$ | ${ }^{26}$ | ${ }_{\text {R } 843}$ | 4 | ${ }^{3 M}$ |
| $\underbrace{\text { R288 }}_{\text {R288 }}$ | ${ }_{2}^{2}$ | ${ }_{5}^{6 F}$ | ${ }_{\substack{\text { R} \\ \text { R3567 } \\ \hline 354}}$ | 5 5 | ${ }_{8 F}^{9 F}$ | ${ }_{\substack{\text { Ras5 } \\ \text { R45 }}}^{\text {Res }}$ | ${ }_{3}^{3}$ | ${ }_{1}^{29}$ | ¢ | $\stackrel{4}{4}$ | 3M ${ }_{\text {3M }}$ |
| R290 | 2 | ${ }^{6 F}$ | R357C | 5 | ${ }_{8 F}$ | R456 | 3 | 16 | R646 | 4 | зм |
| ${ }_{\text {R2929 }}^{\text {R2920 }}$ | ${ }_{2}^{2}$ | ${ }_{5}^{56}$ |  | ${ }_{5}^{5}$ | ${ }_{88}^{8 F}$ | ${ }_{\text {R4600 }}$ | 3 | 16 | Ri647 | ${ }_{4}^{4}$ | ${ }_{3 K}^{1 J}$ |
| ${ }_{\text {R293 }}$ |  | ${ }_{56}$ | ${ }_{\text {R358 }}$ | ${ }_{6}$ | ${ }_{90}$ | ${ }_{\text {R462 }}$ | 3 | 19 | R6699 | 4 |  |
| ${ }_{\text {R }}^{\text {R294 }}$ | ${ }_{2}^{2}$ | ${ }_{56}^{56}$ | R3599 | ${ }_{5}^{5}$ | ${ }^{8 G}$ | ${ }^{\text {R4633 }}$ | ${ }_{3}^{3}$ | ${ }^{1 / 8}$ | ${ }^{\text {R6550 }}$ | 4 | ${ }^{2 M}$ |
| ${ }_{\text {R }}$ | ${ }_{2}^{2}$ | ${ }_{5}^{56}$ | $\substack{\text { R．360 } \\ \text { R361 }}_{\text {R39 }}$ | 5 | ${ }_{86}^{96}$ | ${ }_{\substack{\text { R4771 }}}^{\text {R470 }}$ | 3 3 | ${ }_{1}^{16}$ | ${ }_{\substack{\text { R6552 }}}^{\text {R65 }}$ | ${ }_{4}^{4}$ | ${ }_{4}^{4 M}$ |
| ${ }_{\substack{\text { R297 } \\ \text { R298 }}}^{\text {Reg }}$ | 13 13 | ${ }_{3 F}^{6 F}$ | ${ }_{\substack{\text { R3362 } \\ \text { R363 }}}^{\text {and }}$ | 5 | ${ }_{\text {gF }}^{8 \mathrm{~F}}$ | ${ }_{\substack{\text { R4772 } \\ \text { R473 }}}$ | 3 3 3 | $\underset{1}{1+1}$ | ${ }_{\substack{\text { R6533 } \\ \text { R654 }}}^{\text {Res }}$ | ${ }_{4}^{4}$ | ${ }_{3}^{3 K}$ |
| R299 |  | ${ }_{3 E}$ | ${ }_{\text {R336 }}$ | 5 | 96 | ${ }_{\text {R4774 }}$ | 3 | ${ }_{1}$ | R665 | 4 | ${ }_{3}$ |
| R3301 R302 | ${ }_{5}^{5}$ | ${ }^{96}$ | ${ }_{\text {R }}$ | 5 | ${ }^{105}$ | R475 | ${ }_{3}^{3}$ | 1F | ${ }_{\text {R656 }}^{\text {R65 }}$ | 4 | ${ }_{4}$ |
| ${ }_{\text {R303 }}^{\text {R302 }}$ | 5 | ${ }_{8 F}$ | ${ }_{\substack{\text { R3366 } \\ \text { R367 }}}$ | 5 | ${ }_{80}^{8 C}$ | ${ }_{\substack{\text { R4477 } \\ \text { R47 }}}^{\text {dit }}$ | 3 <br> 3 | ${ }_{1 F}^{1 / 2}$ | ${ }_{\substack{\text { R．658 } \\ \text { R68 }}}$ | ${ }_{4}^{4}$ | ${ }_{4 \mathrm{4K}}^{4 \mathrm{~K}}$ |
| Re304 ${ }_{\text {R305 }}^{\text {R30 }}$ | ${ }_{5}^{5}$ | ${ }_{8 G}^{8 F}$ |  | \％ | ${ }_{96}^{80}$ | ${ }_{\substack{\text { R4778 } \\ \text { R479 }}}^{\text {R }}$ | $3_{3}^{3}$ | ${ }_{15}^{1 / 2}$ | Re659 | $\stackrel{4}{4}$ | ${ }_{2}^{4 M}$ |
|  | 5 5 5 | ${ }^{80}$ |  | 5 | ${ }_{96}^{96}$ | ${ }_{\text {Raso }}$ | ${ }^{13}$ | ${ }_{4}$ | ${ }_{\text {R }}$ | 4 | ${ }_{3}^{3 K}$ |
| ${ }_{\text {R308 }}^{\text {R307 }}$ | 5 | ${ }_{70}$ | ${ }_{\substack{\text { R3372 } \\ \text { R373 }}}^{\text {Ren }}$ | 5 | ${ }_{106}^{96}$ | ${ }_{\substack{\text { R4883 } \\ \text { R483 }}}^{\text {Res }}$ | ${ }_{3}^{13}$ | ${ }_{3 F}^{1 J}$ | ${ }_{\substack{\text { R．664 } \\ \text { R665 }}}^{\text {Re3 }}$ | ${ }_{4}^{4}$ |  |
| ${ }_{\text {R } 399}$ | ${ }_{5}^{5}$ | ${ }^{90}$ | ${ }_{\text {R374 }}^{\text {R375 }}$ | ${ }^{13}$ | ${ }^{10 E}$ | ${ }_{\text {R484 }}$ | ${ }^{3}$ | 1 E | ${ }^{\text {R6666 }}$ | 4 | ${ }^{4 K}$ |
| ${ }_{\text {R3311 }}$ | 5 | ${ }_{80}$ | ${ }_{\substack{\text { R3376 }}}^{\text {R37 }}$ | 5 | ¢ | ${ }_{\substack{\text { Ra88 } \\ \text { R485 }}}^{\text {Res }}$ | ${ }_{3}^{3}$ | ${ }_{1 \times}^{1 / 2}$ | ${ }_{\substack{\text { R6670 }}}^{\text {R69\％}}$ | ${ }_{4}^{4}$ | ${ }_{4 K}^{3 L}$ |
| ${ }_{\substack{\text { R3312 } \\ \text { R313A }}}^{\text {R }}$ | 5 ${ }^{13}$ | 70 | ${ }_{\substack{\text { R3378 }}}^{\text {R37 }}$ | 5 | ${ }_{\text {8H }}^{8 \text { ¢ }}$ | ${ }_{\substack{\text { Rasi } \\ \text { R490 }}}$ | 3 3 3 | ${ }^{1 / 1}$ | ${ }_{\substack{\text { R6671 } \\ \text { R672 }}}$ | 4 | ${ }^{3 \times 1}$ |
|  | 5 | 70 | ${ }_{\substack{\text { R379 }}}^{\text {R37 }}$ | 5 | ${ }_{7}$ | ${ }_{\text {R }}$ | 3 | ${ }_{3}^{2 J}$ | ${ }_{\text {R673 }}$ | 4 | ${ }_{4 \mathrm{M}}$ |
|  | ${ }_{5}^{5}$ | 78 | ${ }_{\substack{\text { R3380 }}}^{\text {R331 }}$ | ${ }_{5}^{5}$ | ${ }_{10}^{60}$ |  | 3 | 3 |  | ${ }_{4}^{4}$ | ${ }_{31}^{4 M}$ |
| ${ }_{\text {R }}^{\text {R314 }}$ | ${ }_{5}^{5}$ | ${ }_{7} 7$ | ${ }_{\text {R3382 }}$ | ${ }_{5}^{5}$ | 76 | R494 | 3 | ${ }_{2}$ | ${ }_{\text {R }}^{\text {P687 }}$ | 4 | ${ }_{3}$ |
| ${ }_{\text {R3316 }}$ | 5 | ${ }_{7 \%}$ | ${ }_{\substack{\text { R．383 } \\ \text { R384 }}}^{\text {R3，}}$ | 5 | ${ }_{80}^{96}$ | ${ }_{\text {R4996 }}^{\text {Ra95 }}$ | ${ }_{3}^{3}$ | 2J |  | ${ }_{4}^{4}$ | ${ }_{\substack{3 K \\ 2 K}}$ |
| ${ }_{\substack{\text { R} \\ \text { R317 }}}^{1818}$ | 5 5 | ${ }_{\substack{8 E \\ 8 E}}^{\text {8E }}$ | ${ }_{\substack{\text { R} \\ \text { R386 }}}^{\text {R38 }}$ | ${ }_{5}^{5}$ | ${ }_{96}^{96}$ | ${ }_{\substack{\text { R4997 } \\ \text { Ras }}}$ | 3 | ${ }_{3}^{2 J}$ | ${ }_{\text {R6901 }}^{\text {R690 }}$ | ${ }_{4}^{4}$ | ${ }_{21}^{2 K}$ |
| R3319 R320 | 5 | ${ }_{88}^{88}$ | ${ }_{\substack{\text { R3387 } \\ \text { R388 }}}^{\text {Ref }}$ | 5 | ${ }_{86}^{86}$ |  | ${ }_{4}^{4}$ | 1／ | Re982 | ${ }_{4}^{4}$ | 24 24 |
|  | 5 | ${ }_{80}^{80}$ | 哭3890 | 13 13 13 | ${ }_{8}^{88}$ |  | ${ }_{1}^{7}$ | $\substack{100 \\ 90}$ | ¢ Reses | ${ }_{1}^{4}$ | $\stackrel{24}{20}$ |
| ${ }_{\text {a }}^{\text {R3210 }}$ | 5 | 80 | ${ }_{\text {R }}$ | 5 | 10 F |  | 7 | ${ }^{90}$ | ${ }_{\text {R702 }}$ | ${ }_{2}$ | 9 |
|  | 5 5 | ${ }_{88}^{80}$ | ${ }_{\substack{\text { R} \\ \text { R399 }}}^{\text {R39 }}$ | 5 5 | ${ }_{8 \times}^{10 \mathrm{~F}}$ |  | 7 | ${ }_{100}^{100}$ | Rr706 | 2 | ${ }_{9}^{101}$ |
| ${ }^{\text {R321F }}$ | 5 | ${ }_{80}$ | R396 | 5 | 10 E | R601 | 4 | ${ }_{3 N}$ | R7707 | 2 | 10к |
|  | 5 5 | ${ }_{7 \text { 76 }}^{7 \text { \％}}$ | Rent | ${ }_{3}^{3}$ | ${ }_{3 F}^{3 F}$ | ${ }_{\substack{\text { R6002 } \\ \text { R603 }}}$ | ${ }_{4}^{4}$ | ${ }_{1}^{2 N}$ | R720 R77 | ${ }_{1}^{2}$ | 10 H |
| R3325 <br> R326 | ${ }_{5}^{5}$ | 70 | ${ }_{\text {R403 }}$ | 3 | ${ }^{3}$ | ${ }_{\text {R }}$ | ${ }_{4}^{4}$ | in | ${ }_{\substack{\text { R7710 }}}^{\text {R720 }}$ | ${ }_{2}$ | ${ }_{10 \mathrm{~K}}$ |
| ${ }_{\substack{\text { R336 } \\ \text { R327 }}}^{\text {Ren }}$ | ${ }_{5}^{5}$ | ${ }_{9 F}$ | ${ }_{\text {R4411 }}^{\text {R410 }}$ | ${ }_{3}^{3}$ | ${ }_{3 G}^{3 G}$ |  | ${ }_{4}^{4}$ | ${ }_{2}^{1 N}$ | ${ }_{\text {R }}^{\text {R711 }}$ | ${ }_{2}^{2}$ | $\stackrel{9}{10}$ |
| R328 R329 | ${ }_{5}^{5}$ | ${ }_{\substack{8 E \\ \text { ge }}}$ |  | 3 3 3 | －3a | （R6099 | $\stackrel{4}{4}$ | $\xrightarrow{2 \mathrm{2k}}$ | R715 R716 | 2 | ${ }^{10 \mathrm{~K}}$ |
| R330 | 5 | ${ }_{9 E}$ | ${ }_{\text {R414 }}$ | 3 | ${ }_{3}^{36}$ | ${ }_{\substack{\text { R6610 } \\ \text { R611 }}}$ | ${ }_{4}^{4}$ | ${ }_{3 \mathrm{k}}^{2 \mathrm{~K}}$ | ${ }_{\text {R7717 }}$ | 2 |  |
|  | ${ }_{5}^{5}$ | ${ }^{86}$ | R415 | 3 | ${ }^{2 G}$ | ${ }^{\text {R612 }}$ | 4 | ${ }^{3 \times}$ | ${ }^{\text {R7718 }}$ | 2 | ${ }^{\text {gK }}$ |
| ${ }^{\text {R3333 }}$ | 5 | ${ }_{86}$ | R447 | 3 | ${ }_{2}^{2 \mathrm{H}}$ | ${ }_{\substack{\text { R6613 } \\ \text { R614 }}}^{\text {Rel }}$ | ${ }_{4}^{4}$ | ${ }_{31}^{2 K}$ | ${ }_{\text {R720 }}$ | ${ }_{2}$ | ¢ |
| ${ }_{\substack{\text { R } \\ \text { R334 } \\ \text { R33 }}}$ | ${ }_{5}^{5}$ | ${ }_{9}^{86}$ |  | 3 | － 26 | R615 R6616 | ${ }_{4}^{4}$ | ${ }_{3}^{4 \mathrm{~K}}$ | ${ }_{\substack{\text { R722 } \\ \text { R722 }}}$ | ${ }_{2}^{2}$ | ${ }_{9 K}^{9 K}$ |
| ${ }_{\substack{\text { R3366 } \\ \text { R337 }}}^{\text {R3，}}$ | 5 | ${ }_{88}^{9 F}$ |  | 3 | ${ }^{26}$ | ${ }_{\text {R617 }}$ | 4 | ${ }^{3 \mathrm{3K}}$ |  | ${ }^{13}$ | 9 |
| ${ }_{\text {R338 }}$ | 5 | ${ }_{88}^{8 F}$ |  | ${ }^{3}$ | －${ }_{36}^{3 G}$ | ${ }_{\substack{\text { R6618 } \\ \text { R619 }}}^{\text {Re9 }}$ | ${ }_{4}^{4}$ | 3M | R724 R725 | 2 | ${ }_{96}^{9 k}$ |
|  | ${ }_{5}^{13}$ | ${ }_{76}^{9 E}$ |  | 3 | ${ }_{2}^{2 \mathrm{ab}}$ |  | 4 | ${ }_{4}{ }^{\text {m }}$ | R7226 R27 | 2 | 100 |
| ¢ | 5 5 | ${ }_{9}$ |  | 3 | ${ }_{2}^{24}$ | ${ }_{\substack{\text { R662 } \\ \text { R622 }}}^{18}$ | ${ }_{4}^{4}$ | ${ }_{3 \mathrm{4}}^{4 \mathrm{~L}}$ | ${ }_{\text {R }}$ | 2 | $\underset{\substack{\text { 10\％} \\ \text { 10\％}}}{ }$ |
| R342 R343 | 5 | ${ }_{96}^{9 E}$ | ${ }_{\substack{\text { R4332 } \\ \text { Ra32 }}}^{\text {Ren }}$ | ${ }_{3}^{3}$ | ${ }_{26}^{2 F}$ | ${ }_{\substack{\text { R623 } \\ \text { R62 }}}^{\text {Re2 }}$ | ${ }_{4}^{4}$ | ${ }_{2}^{3 K}$ | R729 R730 | 2 | ${ }_{9}^{10 \mathrm{H}}$ |
| － $\begin{gathered}\text { R344 } \\ \text { R345 }\end{gathered}$ | 5 <br> 13 | ${ }_{8 \times}^{8 E}$ |  | 3 3 3 | 26 2 CH 2 H | （ex | ${ }_{4}^{4}$ | $2 L$ 4 4 | R7731 <br> R 72 | 2 | ${ }_{\substack{9 \% \\ 10 \mathrm{O}}}^{\text {90，}}$ |
| R346 | 5 | ${ }_{8 E}$ | RA42 | 3 | 2 H | ${ }^{8627}$ | 4 | 3 | R733 | 13 | 10k |


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| A10 - MAIN BOARD (cont) <br> (SN B710100 to B714999, 2246 1Y <br> SN B110100 to B119999, 2246 MOD A) |  |  |  |  |  |  |  |  |  |  |  |
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| Clircuit | SCHEM | ${ }_{\text {cose }}^{\text {BOAAD }}$ | Clircuit NUMBER | SCHEM NUMEER | ${ }_{\text {cose }}^{\text {BOAAD }}$ | CIRCUIT NUMBER | SCHEM NUMEER | ${ }_{\text {cta }}^{\text {BOARD }}$ | ${ }_{\text {Cincuit }}^{\text {Cumbr }}$ | SCHEM | ${ }_{\text {cte }}^{\text {BOARD }}$ |
| 4702 | ${ }^{13}$ | ${ }^{9 K}$ | va308 | 5 | ${ }^{9}$ | w305 | 6 | 10 E | ${ }^{W} 1106$ | 3 | ${ }^{51}$ |
|  | ${ }_{13}^{2}$ | ${ }_{7}^{7 H}$ | Va309 | ${ }_{5}^{5}$ | ${ }^{76}$ | W401 | 3 | ${ }_{3}^{4 H}$ | W1108 | ${ }^{3}$ | ${ }_{\substack{5 L \\ \text { SF }}}$ |
| $\triangle 802$ | ${ }^{13}$ | ${ }^{8 H}$ | va311 | 5 | ${ }_{8 E}$ | w413 |  | 3 | W 1201 | 13 | ${ }_{9}$ |
| ${ }_{\text {U }}^{40018}$ | -13 | ${ }_{9 A}^{9 A}$ | Vn312 | ${ }_{5}^{5}$ | ${ }_{8,}^{108}$ | $\mathrm{W}_{\text {W414 }}$ | 2 | ${ }_{5 \times}^{3}$ | Wince | 13 13 | ${ }_{10}^{9 H}$ |
| บ901 | ${ }_{13}^{13}$ | ${ }_{98}^{9 A}$ | VR2701 | 6 | ${ }_{6 M}$ |  | 2 | ${ }_{3}^{5 \mathrm{sF}}$ | $\underset{W}{\text { W1204 }}$ | ${ }_{13}^{13}$ | 76 |
|  | 7 | ${ }_{88}^{88}$ |  |  |  | W416A | 2 |  | W1205 W1209 | 13 <br> 13 | ${ }_{4}^{2 J}$ |
| $\underset{\substack{\text { U930 } \\ \text { G91 }}}{\text { cese }}$ | 13 | ${ }_{88}^{88}$ | ${ }_{\text {w9 }}$ | 7 | ${ }^{10}$ | w501 | 13 | $1{ }^{19}$ | ${ }_{\text {witer }} \mathbf{1 2 0}$ | ${ }^{13}$ | $2{ }^{20}$ |
| $\underset{\text { ¢932 }}{\substack{\text { ¢93 }}}$ | 13 13 | ${ }_{88}^{98}$ | ${ }_{\text {w }}^{\text {w }}$ ¢ 11 | ${ }_{1}^{7}$ | ${ }_{6 A}^{100}$ | ¢ ${ }_{\substack{\text { W502 } \\ \text { W503 }}}$ | $\stackrel{4}{4}$ | $3 M$ $3 M$ | W1216 | 13 13 | ${ }_{80}^{80}$ |
| -931 | 7 | ${ }_{9 B}^{98}$ | w12 | 1 | ${ }_{5}{ }^{6 A}$ |  | 4 | ${ }_{3}^{3 M}$ | ${ }_{W}^{W} 1218$ | ${ }_{13}$ | ${ }_{5}$ |
| ¢ 11001 A | 7 | 7 | W13 | 1 | ${ }^{3 A}$ | W505 | $4_{4}^{4}$ | ${ }_{3}^{3 N}$ | W.1221 W1222 | 13 <br> 13 | ${ }_{60}^{10}$ |
| ¢U10001c <br> U10010 | 7 | 7 | W16 W16 w17 | 7 | ${ }_{7 K}^{2 A}$ | West W507 W510 | ${ }_{4}^{4}$ | ( |  | 13 13 13 13 | ${ }_{80}^{70}$ |
| 41001 | ${ }^{13}$ | $\pi$ | W18 | ${ }_{6}^{6}$ | ${ }_{8 K}$ | ${ }_{\text {W603 }}$ |  | ${ }_{4}{ }^{\text {m }}$ | ${ }_{W}{ }_{W} 1237$ | ${ }_{13}^{13}$ | 7 |
| ¢ ${ }_{\text {U1010 }}$ | ${ }_{3}^{3}$ | ${ }_{46}^{4 G}$ | ${ }_{\text {W }}^{\text {W20 }}$ | ${ }_{2}^{2}$ | $\xrightarrow{10 \mathrm{H}}$ | W604 | ${ }_{4}^{4}$ | ${ }^{31}$ | ${ }_{\text {W }}^{\mathbf{W}} \mathbf{1 2 4 7}$ | 13 <br> 13 | ${ }_{6} 7$ |
| 41101 | 13 | $4 \mathrm{4a}$ | ${ }_{W} \mathbf{1 0 0}$ | 2 | ${ }_{5}^{9}$ | $\underset{\substack{\text { W605 } \\ \text { w606 }}}{ }$ | ${ }_{13}^{4}$ | ¢ | $\underset{\substack{\text { W1248 } \\ \mathbf{W} 1249 \\ \hline 129}}{ }$ | 13 <br> 13 | ${ }_{5}^{6 \mathrm{H}}$ |
|  | ${ }_{3}^{3}$ | ${ }_{4 G}^{4 G}$ | w101 w102 | 1 | ${ }_{38}^{5 C}$ | Weor W610 W6ig | ${ }_{4}^{13}$ | -5M | wisiso $\mathbf{W} 1251$ $\mathbf{W}$ | 13 13 13 | ${ }_{\substack{8 K \\ 84}}$ |
| ${ }^{41102}$ | ${ }^{13}$ | 4a | w 103 | 13 | ${ }_{30}^{38}$ | W6611 | ${ }_{4}^{4}$ | ${ }_{3}{ }^{3 N}$ |  | ${ }_{13}^{13}$ | ${ }_{5}^{6 \mathrm{H}}$ |
| U1103 | ${ }_{13}^{13}$ | ${ }_{5}^{5 \mathrm{SK}}$ | W200 W201 | 2 |  | ¢ $\begin{gathered}\text { wooz } \\ \text { W805 }\end{gathered}$ | ${ }_{6}^{5}$ | ${ }_{\substack{86 \\ 69}}$ | W1255 W1277 | 13 13 13 | ${ }_{8 K}^{6 F}$ |
| 41104 A | 3 | ${ }_{4}^{4}$ | W202 | ${ }_{2}$ | ${ }_{56}$ | W8006 | ${ }_{6}^{6}$ | ${ }_{7}{ }_{7}$ | $\underset{\substack{* 1288 \\ \text { Wi28 }}}{ }$ | ${ }_{7}^{13}$ | $\stackrel{86}{8 \times}$ |
|  | ${ }_{3}^{3}$ | ${ }_{4}^{4 H}$ | W203 | ${ }_{2}^{2}$ | ${ }_{66}^{68}$ | W807 | ¢ ${ }_{6}^{6}$ | ${ }_{7 \times}^{6 M}$ | W2302 <br> W2302 | 7 | 100 |
| ${ }^{4} 1104$ | ${ }^{13}$ | ${ }_{4}^{4 H}$ | ${ }_{\text {W206 }}$ |  | ${ }_{66}^{66}$ | ${ }_{\text {W810 }}$ | 6 | ${ }_{6} 6$ | ${ }_{\text {W2020 }}$ | 13 | 100 |
| U11068 | ${ }_{3}^{3}$ | ${ }_{5}^{5 L}$ | W207 ${ }_{\text {W208 }}$ | ${ }_{2}^{2}$ | ¢68 | W811 | ${ }_{2}^{6}$ | ${ }_{8 G}^{5 H}$ | W2304 W2502 | 7 | ${ }_{11}^{100}$ |
| U11068 | 3 13 | ${ }_{5 L}^{5 L}$ | W209 W210 | 2 | c6, 10 H | W820 | ¢ ${ }_{6}^{6}$ | 5K 4 4 M | W2022 W2701 W202 | 4 | 号 |
|  |  |  | ${ }_{\text {W223 }}{ }^{293}$ | 2 | SE | wovo | ${ }^{13}$ | ${ }_{98}^{48}$ |  |  |  |
| Vereno | 5 | ${ }_{66}^{66}$ | ${ }_{\text {W }}{ }_{\text {W232 }} \mathbf{2 3 2}$ | ${ }_{2}^{2}$ |  | W100 $\mathbf{w 1 1 0 1}$ $w 1102$ | 7 | ${ }_{5 K}^{3 L}$ | Y600 | 4 | зм |
| Vex ve303 | 5 | ${ }_{86}^{86}$ | ${ }_{\text {W304 }}^{\text {w235 }}$ | ${ }_{5}^{13}$ | ${ }_{\text {SG }}^{\text {SE }}$ | ${ }_{\text {W }}^{\text {W1102 }}$ | ${ }_{3}^{3}$ | $\underset{5 k}{35}$ |  |  |  |



\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline \multicolumn{12}{|c|}{\begin{tabular}{l}
A10-MAIN BOARD \\
(SN B710099 AND BELOW, 2246 1Y \\
SN B110099 AND BELOW, 2246 MOD A)
\end{tabular}} \& \multicolumn{12}{|c|}{A10 - MAIN BOARD (cont) (SN B710099 AND BELOW, 2246 1Y SN B110099 AND BELOW, 2246 MOD A)} \\
\hline (1nccut \& SCHEM \& \(\underbrace{\substack{\text { cimen }}}_{\text {Circuit }}\) \& SCHEM \& Circuit \& \[
\begin{gathered}
\text { SCHEM } \\
\text { NUMBER }
\end{gathered}
\] \& Clincuit \& SCHEM \& \[
\begin{gathered}
\begin{array}{c}
\text { CIRCUIT } \\
\text { NUMBER }
\end{array} \\
\hline \hline
\end{gathered}
\] \& \[
\begin{gathered}
\begin{array}{c}
\text { SCHEM } \\
\text { NUMBER }
\end{array} \\
\hline
\end{gathered}
\] \& \[
\begin{gathered}
\text { CIRCUIT } \\
\text { NUMBER }
\end{gathered}
\] \& \[
\begin{aligned}
\& \text { SCHEM } \\
\& \text { NUMBER }
\end{aligned}
\] \& \begin{tabular}{l}
CIRCUIT \\
NUMBE
\end{tabular} \& \[
\begin{gathered}
\text { SCHEM } \\
\text { NUMBER }
\end{gathered}
\] \& CIRCUIT NUMBER \& SCHEM \& CIRCUIT \& \[
\begin{gathered}
\text { SCHEM } \\
\text { NUMBER }
\end{gathered}
\] \& \begin{tabular}{l}
CIRCUIT \\
NUMBER
\end{tabular} \& SCHEM \& \(\xrightarrow{\substack{\text { cincuit } \\ \text { NUMBER }}}\) \& SCHEM \& Clincuit \& SCHEM \\
\hline \({ }^{\text {ATT117 }}\) \& 1 \& \({ }^{\text {c235 }}\) \& \({ }_{2}^{13}\) \& \({ }^{\text {C493 }}\) \& 2 \& \({ }^{\text {c2708 }}\) \& 13 \& \({ }^{\text {k105 }}\) \& , \& 9600 \& \({ }_{4}^{4}\) \& \({ }^{\text {R140 }}\) \& \({ }^{2}\) \& \({ }^{\text {R270 }}\) \& \({ }_{2}\) \& \({ }_{\text {R352 }}\) \& 5 \& \({ }^{\text {R4600 }}\) \& \({ }^{3}\) \& \({ }^{\text {R649 }}\) \& 4 \& \({ }^{\text {R842 }}\) \& 6 \\
\hline \& 1 \&  \& \({ }_{13}^{2}\) \&  \& \(\stackrel{2}{13}\) \& C2709 \& \(\stackrel{13}{7}\) \& \({ }_{\substack{\text { K107 } \\ \text { k108 }}}\) \& 1 \& \({ }_{\text {a }}^{0601}\) \& \({ }_{4}^{4}\) \& \({ }_{\text {R142 }}^{\text {R14 }}\) \& 1 \& \({ }_{\text {R272 }}^{\text {R271 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R3554 }}^{\text {R333 }}\) \& \({ }_{5}^{6}\) \& \({ }_{\text {R462 }}^{\text {R461 }}\) \& \({ }_{3}^{3}\) \& R650 \& \(4_{4}^{4}\) \& \({ }_{\substack{\text { R843 } \\ \text { R844 }}}\) \& \({ }_{6}^{6}\) \\
\hline C1
\(C 2\) \& 1 \& C242
c243
c243 \& 2
2
2 \& ( \(\begin{aligned} \& \text { C5022 } \\ \& \text { C503 }\end{aligned}\) \& 13
13
13 \& cer 2711
c2712 \& 7 \& K109
K110
110 \& 1 \& - \({ }_{\text {a }}^{\text {a063 }}\) \& \({ }_{4}^{4}\) \& \({ }_{\substack{\text { R151 } \\ \text { R152 }}}\) \& 1 \& \({ }_{\substack{\text { R2723 } \\ \text { R274 }}}^{\text {Ren }}\) \& 2 \& R335
R356
R35 \& 5
5
5 \&  \& 3
3
3 \& \({ }_{\substack{\text { Re5s } \\ \text { R653 } \\ \text { R63 }}}\) \& 4 \& \begin{tabular}{l} 
R844 \\
R846 \\
R846 \\
\hline
\end{tabular} \& 6
6
6 \\
\hline c2
cid \& 1 \& ( \({ }_{\text {cen }}^{\text {c243 }}\) \& \({ }_{13}^{2}\) \& (c503 \& 13
13 \& C2772
\(\mathrm{C2713}\) \& 7 \& K110 \& 1 \& \({ }_{0}^{0604}\) \& \({ }_{4}^{4}\) \& \({ }_{\substack{\text { R15 } \\ \text { R153 } \\ \hline 1}}\) \& 1 \& \({ }_{\text {R275 }}^{\text {R274 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\substack{\text { R335 } \\ \text { R35 }}}^{\text {R34 }}\) \& 5
5 \& \({ }_{\text {R4771 }}^{\text {R470 }}\) \& \({ }_{3}^{3}\) \& \({ }_{\substack{\text { R653 } \\ \text { R654 }}}\) \& \(4_{4}^{4}\) \& \({ }_{\substack{\text { R846 } \\ \text { R847 }}}^{\text {cen }}\) \& 6 \\
\hline \({ }^{\mathrm{Cl1}}\) \& 1 \& \({ }^{\text {c245 }}\) \& \({ }_{2}^{13}\) \& \({ }^{6600}\) \& 4 \& \({ }^{\text {c27275 }}\) \& 7 \& K112 \& 1 \& O606 \& 4 \& \({ }^{\text {R153 }}\) \& 1 \& \({ }^{2276}\) \& \& R358 \& 6 \& \({ }^{\text {R472 }}\) \& 3 \& R655 \& 4 \& \({ }^{\text {R848 }}\) \& 6 \\
\hline c20
c21 \& 1 \& ( \({ }_{\text {cen }}^{\text {c248 }}\) \& 22 \& (6601 \& \({ }_{4}^{4}\) \& \({ }_{\text {c }}^{\text {C2716 }}\) \& 7 \& L101 \& \& \({ }_{\text {a }}^{0607}\) \& 4 \& \({ }_{\text {R }}^{\text {R155 }}\) \& 1 \& \({ }_{\text {R278 }}^{\text {R278 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R3359 }}^{\text {R336 }}\) \& 5 \& \({ }_{\text {R4774 }}^{\text {R473 }}\) \& \(3_{3}^{3}\) \& \({ }_{\text {R6565 }}^{\text {R65 }}\) \& 4 \& R889 \& \({ }_{6}^{6}\) \\
\hline \({ }^{2} 101\) \& 1 \& -259 \& + \& (6602 \& \({ }_{4}^{4}\) \& C2717
c2719
c2720 \& 7 \& L102 \& \({ }_{13}^{13}\) \&  \& \({ }_{2}^{4}\) \&  \& 1 \& R279 \& 2 \& \({ }_{\substack{\text { R3661 } \\ \text { R362 }}}^{\text {R369 }}\) \& 5 \& \({ }_{\text {R }}^{\text {R476 }}\) \& \({ }_{3}^{3}\) \&  \& \({ }_{4}^{4}\) \& \({ }_{\substack{\text { R850 } \\ \text { R851 } \\ \text { R85 }}}\) \& - 6 \\
\hline ( \({ }_{\text {c102 }}^{\text {c10 }}\) \& 1 \& ( \({ }_{\text {c265 }}^{\text {c268 }}\) \& \({ }_{2}^{13}\) \& C604 \& 13
13 \& C2720
C 2721 \& 7 \& L130
L140 \& 1 \& \({ }_{\text {a }}^{\text {a }} 0\) \& 2 \& ( \({ }_{\substack{\text { R158 } \\ \text { R159 }}}^{\text {R15 }}\) \& \({ }_{1}^{2}\) \& \({ }_{\text {R281 }}^{\text {R280 }}\) \& 2 \& \({ }_{\substack{\text { R3662 } \\ \text { R363 }}}\) \& 5
5 \& \({ }_{\text {R4788 }}^{\text {R477 }}\) \& \({ }_{3}^{3}\) \& \({ }_{\substack{\text { R659 } \\ \text { R662 }}}\) \& \(4_{4}^{4}\) \& \({ }_{\text {Rex }}^{\text {R852 }}\) \& \({ }_{6}^{6}\) \\
\hline  \& 1 \& C271
c272
c272 \& 2 \& ( \({ }_{\text {C606 }}^{\text {C606 }}\) \& 13
4
4 \& C22723
C2724 \& 7 \& 1201
1216
1216 \& \begin{tabular}{|c}
13 \\
13
\end{tabular} \&  \& 2
6 \& - \(\begin{aligned} \& \text { R160 } \\ \& \text { R161 }\end{aligned}\) \& \({ }_{1}^{2}\) \& \({ }_{\substack{\text { R282 } \\ \text { R283 }}}^{\text {R28 }}\) \& 13
13 \& \({ }_{\substack{\text { R3364 } \\ \text { R365 }}}^{\text {Re3 }}\) \& 5 \& - \({ }_{\text {R479 }}^{\text {R481 }}\) \& \begin{tabular}{|c}
3 \\
13 \\
\hline
\end{tabular} \& \({ }_{\substack{\text { R663 } \\ \text { R664 }}}\) \& \({ }_{4}^{4}\) \& RR54
R855
R85 \& \({ }_{6}^{6}\) \\
\hline \begin{tabular}{l} 
c106 \\
\hline
\end{tabular} \& 1 \&  \& 2 \& -6608 \& \({ }_{4}^{4}\) \& C227
C2758 \& 7 \& \({ }^{21216}\) \& \({ }_{13}^{13}\) \& \({ }_{\text {a } 8802}^{\text {a81 }}\) \& \({ }_{6}^{6}\) \& \({ }_{\text {R162 }}\) \& 1 \& \({ }_{\text {R284 }}^{\text {R283 }}\) \& \({ }_{2}\) \& \({ }_{\text {R3666 }}\) \& 5 \& \({ }_{\text {R483 }}\) \& \({ }_{3}^{13}\) \& \({ }_{\text {R6665 }}\) \& \({ }_{4}^{4}\) \& \({ }_{\text {R8566 }}\) \& 6 \\
\hline  \& 1 \& ( \({ }_{\text {c }}^{\text {c274 }}\) \& 2 \& C609 \& 13
13 \& C22799
C2783 \& 7 \& L4266
L432 \& 3
3
3 \& -8833 \& \({ }_{6}^{6}\) \& \({ }_{\substack{\text { R171 } \\ \text { R175 }}}\) \& 1 \& \({ }_{\text {R285 }}^{\text {R286 }}\) \& 2 \& \({ }_{\substack{\text { R3667 } \\ \text { R369 }}}^{\text {Ref }}\) \& 5 \& (R484 \& 3 \& \({ }_{\substack{\text { R666 } \\ \text { R699 }}}\) \& \({ }_{4}^{4}\) \&  \& \({ }_{6}^{6}\) \\
\hline \({ }^{\text {col1 }}\) \& 1 \& -282 \& \({ }_{13}^{13}\) \& \({ }^{6611}\) \& \begin{tabular}{|c}
3 \\
\hline \\
\hline
\end{tabular} \& - 22784 \& 7 \& L445 \& \(\stackrel{13}{13}\) \& \({ }^{0} \mathbf{0} 804\) \& \({ }_{6}^{6}\) \& \({ }_{\text {R176 }}\) \& 1 \& \({ }_{\text {R287 }} \mathrm{R}\) \& 2 \& \({ }_{\text {R }}\) \& 5 \& - \({ }_{\text {R486 }}\) \& 3 \& \({ }_{\text {R6690 }}\) \& \({ }_{4}^{4}\) \& \({ }_{\text {Reseb }}^{\text {R930 }}\) \& 7 \\
\hline ( \({ }_{\text {clit }}^{\text {c112 }}\) \& 1 \& ( \({ }_{\text {c }}^{\text {c283 }}\) \& \begin{tabular}{|r}
13 \\
13
\end{tabular} \& C612 \& \({ }_{13}^{4}\) \& C2785 \& 7 \& L446 \& \({ }_{3}^{13}\) \& -8806 \({ }_{\text {a80 }}\) \& \({ }_{6}^{6}\) \&  \& 1 \& \({ }_{\text {R289 }}^{\text {R288 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R3372 }}^{\text {R331 }}\) \& 5 \& \({ }_{\substack{\text { R4487 } \\ \text { R490 }}}\) \& \(3_{3}^{3}\) \& \({ }_{\text {R } 6 \text { ¢671 }}{ }_{\text {6672 }}\) \& \(4_{4}^{4}\) \& \({ }_{\text {Regen }}^{\text {R9908 }}\) \& 7 \\
\hline  \& 1 \&  \& \begin{tabular}{r}
13 \\
\hline 15 \\
5
\end{tabular} \& C701
C702 \& 13
13
13 \& \({ }_{\text {CR131 }}\) \& 1 \& L475 \& \({ }_{13}^{13}\) \& -0808 \& \({ }_{6}^{6}\) \& \({ }^{\text {R179 }}\) \& 1 \& \({ }^{\text {R290 }}\) \& 2 \& \(\stackrel{\text { R373 }}{\substack{\text { a374 }}}\) \& 5 \& R491 \& 3 \& R701 \& \({ }^{13}\) \& R9099 \& 7 \\
\hline \({ }_{\substack{122}}\) \& 1 \& C302 \& 5 \& \({ }^{\text {c703 }}\) \& \({ }_{13}^{13}\) \& \({ }_{\text {chisi }}\) \& 1 \& \begin{tabular}{l} 
L476 \\
L701 \\
\hline
\end{tabular} \& \(\stackrel{13}{2}\) \& - \({ }_{\text {a }}^{\text {a819 }}\) \& \({ }_{6}^{6}\) \& \begin{tabular}{|c} 
RA180 \\
R181 \\
\hline 180
\end{tabular} \& 1 \& \({ }_{\text {R292 }}{ }_{\text {R291 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R3375 }}^{\text {R374 }}\) \& \({ }^{13}\) \& \({ }_{\text {R }}^{\text {R4993 }}\) \& \({ }_{3}^{3}\) \& R7702 \({ }_{\text {R }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R }}^{\text {R9910 }}\) \& 7 \\
\hline (123 \& 1 \& C-303 \({ }_{\text {c }}^{\text {c304 }}\) \& \begin{tabular}{|c}
5 \\
13
\end{tabular} \& C704
\(\mathrm{C705}\) \& 13
13 \& \({ }_{\text {CR201 }}^{\text {CR202 }}\) \& 2 \& \({ }_{\text {L702 }} \mathbf{L 7 0 3}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {a }}^{\text {a905 }}\) \& 7 \& \({ }_{\text {R }}^{\text {R282 }}\) \& \(\stackrel{13}{2}\) \& \({ }_{\text {R293 }}^{\text {R293 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\substack{\text { R3376 } \\ \text { R377 }}}^{\text {Re4 }}\) \& 5 \& \% \({ }_{\text {R494 }}\) \& \(3_{3}^{3}\) \& \({ }^{\text {R706 }}\) \& 2 \& \({ }_{\text {R915 }}\) \& 13 \\
\hline \({ }^{\text {c124 }}\) \& 1 \& \({ }_{\text {c305 }}\) \& \({ }_{5}\) \& \({ }^{6} 7706\) \& \({ }_{2}^{13}\) \& \({ }_{\text {cher }}\) \& \({ }_{2}^{2}\) \& \({ }^{1} 704\) \& \({ }_{2}\) \& \({ }^{\text {a9908 }}\) \& 7 \& \({ }_{\text {R202 }}\) \& 2 \& \({ }_{\text {R295 }}\) \& \({ }_{2}\) \& \({ }_{\text {R3378 }}\) \& 5 \& \({ }_{\text {R496 }}\) \& \({ }_{3}\) \& \({ }^{\text {R770 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R9220 }}\) \& 7 \\
\hline  \& 1 \& C306 \({ }_{\text {c307 }}\) \& 5
5 \& c707
\(\mathrm{C708}\) \& \({ }_{13}^{2}\) \& \({ }_{\text {CR261 }}^{\text {CR262 }}\) \& 2 \& P8 \& \& \({ }_{0}^{01001}\) \& 7 \& \({ }_{\text {R204 }}^{\text {R203 }}\) \& 2 \& \({ }_{\text {R2997 }}^{\text {R296 }}\) \& \({ }_{13}^{2}\) \& \({ }_{\substack{\text { R339 } \\ \text { R380 }}}\) \& 5
5 \& \({ }_{\text {R4998 }}^{\text {R497 }}\) \& \(3_{3}^{3}\) \& \({ }_{\text {R }}^{\text {R709 }}\) \& 13
2 \& \({ }_{\text {R9292 }}^{\text {R922 }}\) \& 7 \\
\hline \({ }^{\text {c }} 13132\) \& 1 \& \({ }^{\text {c308 }}\) \& 5 \& C711 \& 2 \& \({ }_{\text {CR263 }}\) \& 2 \& \({ }^{\text {P9 }}\) \& 7 \& \({ }^{0} 10003\) \& 7 \& \({ }^{20205}\) \& 2 \& \({ }^{2298}\) \& \({ }_{13}\) \& R381 \& 5 \& \({ }^{\text {a } 501}\) \& 4 \& R711 \& 2 \& \({ }_{\text {R923 }}\) \& 7 \\
\hline (133 \& , \& c309
c30
c30 \& \(\stackrel{13}{5}\) \& C712 \& \(\underset{13}{2}\) \& \({ }_{\text {Crabal }}^{\text {CR432 }}\) \& 5
3 \& \({ }_{\text {P18 }}^{\text {P17 }}\) \& \({ }_{6}^{6}\) \& \({ }_{0}^{01004}\) \& 7 \& \({ }_{\text {R }}^{\text {R206 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R }}^{\text {R2901 }}\) \& \({ }_{5}^{2}\) \& \({ }_{\text {R }}^{\text {R383 }}\) \& 5
5 \& \({ }_{\text {R }}^{\text {R } 503}\) \& 7 \& \({ }_{\text {R712 }}^{\text {R715 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\substack{\text { R9293 } \\ \text { R930 }}}^{\text {Re2 }}\) \& 7 \\
\hline (135 \& 13
13 \& C311
C312
c312 \& 5
5
5 \& ( \({ }_{\text {c }}^{\text {C802 }}\) \& \({ }_{6}\) \& - \& 3
4
4 \& P198
P1920 \& 2 \& - \& \({ }_{3}^{3}\) \& \({ }_{\text {R208 }}^{\text {R20 }}\) \& \({ }^{13}\) \& R3023 \& 5 \& \({ }_{\text {R }}^{\text {R384 }}\) \& 5 \& \({ }_{\text {R }}\) \& \({ }_{7}^{13}\) \& \({ }_{\text {R717 }}\) \& 2 \& \({ }^{\text {R9391 }}\) \& 7 \\
\hline \({ }^{\text {C137 }}\) \& 1 \& \({ }_{C 313}\) \& 5 \& \({ }_{\text {c } 8004}\) \& 6 \& CR801 \& \({ }_{6}\) \& \({ }_{\text {P2302 }}\) \& \({ }_{7}\) \& \({ }^{0} 11103\) \& 3 \& \({ }_{\text {R210 }}\) \& \({ }_{2}\) \& \({ }_{\text {R304 }}\) \& 5 \& \({ }_{\text {R336 }}\) \& 5 \& \({ }_{\text {R5006 }}\) \& 7 \& R7718 \& \({ }_{2}\) \& R933 \& 7 \\
\hline  \& 2 \& C314 \& 5 \& Criss \& \({ }_{13}^{6}\) \& CR802 \({ }_{\text {CR819 }}\) \& \({ }_{6}^{6}\) \& \({ }_{\text {P2304 }}\) \& \(\stackrel{13}{7}\) \& Q1104
O1105
0 \& \({ }_{3}^{3}\) \& \({ }_{\text {R212 }}^{\text {R211 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R }}^{\text {R306 }}\) \& 5 \& \({ }_{\substack{\text { R3387 } \\ \text { R388 }}}\) \& 5 \& \({ }_{\text {R }}^{\text {R }}\) R507 \& 7 \& R719
R720 \& 2 \& \({ }_{\text {Re934 }}^{\text {Re93 }}\) \& 7 \\
\hline (140 \& \(\stackrel{13}{1}\) \&  \& 13
13 \& C6807 \& 6 \& \({ }_{\substack{\text { CRa935 } \\ \text { Chas6 }}}\) \& 7 \& 0131 \& 1 \&  \& 3 \& \({ }_{\text {R }}\) \& 2 \& \({ }_{\substack{\text { R3007 } \\ \text { R308 }}}^{\text {and }}\) \& 5 \& \({ }_{\substack{\text { R3900 } \\ \text { R391 }}}\) \& +13 \& \({ }_{\substack{\text { R5310 } \\ \text { R512 }}}^{\text {and }}\) \& 7 \& \({ }_{\text {R }}^{\substack{\text { R721 } \\ \text { R72 }}}\) \& 2 \& \({ }_{\text {coser }}^{\substack{\text { R936 } \\ \text { R937 }}}\) \& 7 \\
\hline \({ }^{\text {C152 }}\) \& 1 \& \({ }^{\text {c318 }}\) \& 13 \& c809 \& 6 \& CR100 \& 7 \& \({ }^{\text {a }} 151\) \& 1 \& Q20702 \& 7 \& \({ }^{\text {R22 }}\) \& 2 \& \({ }^{\text {R3099 }}\) \& 5 \& \({ }_{\text {R392 }}\) \& \({ }^{13}\) \& \({ }_{\text {R601 }}\) \& 4 \& \({ }^{\text {R723 }}\) \& \({ }^{13}\) \& \({ }_{\text {R938 }}\) \& 7 \\
\hline (153 \& 1 \& C319 \& \({ }_{13}^{6}\) \& \({ }_{\text {c }}^{\text {C810 }}\) \& 2 \&  \& 7 \& \({ }_{0}^{0171}\) \& 1 \& -02703 \& 7 \& \({ }_{\text {R }}^{\text {R218 }}\) \& 2 \& \({ }_{\text {R3311 }}^{\text {R3310 }}\) \& 5
5 \& \({ }_{\text {R }}^{\text {R3934 }}\) \& 5
5
5 \& \({ }_{\substack{\text { R6002 } \\ \text { R603 }}}\) \& \({ }_{4}^{4}\) \& \({ }_{\text {R724 }}^{\text {R72 }}\) \& \({ }_{2}^{2}\) \& R939 \& 7 \\
\hline C155
C156
C15 \& 13
13 \& C321
c326
c32 \& 5 \& C814 \& -6 \& CRR1004
CR1005 \& 7 \& \({ }_{\text {a }}^{\text {a } 251}\) \& 2 \& Q22775
O2706 \& 7 \& \({ }_{\text {R }} \begin{aligned} \& \text { R220 } \\ \& \text { R221 }\end{aligned}\) \& 2 \& \({ }_{\substack{\text { R3312 } \\ \text { R313 }}}^{\text {Ren }}\) \& -13 \& \({ }_{\substack{\text { R3935 } \\ \text { R396 }}}\) \& 5 \&  \& \({ }_{4}^{4}\) \& \({ }_{\text {R }}^{\text {R2726 }}\) \& 2 \& (2941 \& 7 \\
\hline \({ }^{\text {c15 }} 15\) \& 1 \& - \(\begin{aligned} \& \text { c329 } \\ \& \text { c329 }\end{aligned}\) \& 5 \& - 6816 \& \({ }_{13}\) \& \({ }_{\text {CRF2700 }}\) \& 7 \& \({ }_{\text {a }}\) \& \({ }_{2}\) \& - \& 7 \& \({ }_{\text {R }}\) \& 2 \& \({ }_{\text {R314 }}\) \& 5 \& - \({ }_{\text {R4901 }}\) \& \({ }^{5}\) \& \({ }_{\text {R6006 }}\) \& \({ }_{4}^{4}\) \&  \& \({ }_{2}^{2}\) \& R100\%
R1002 \& 7 \\
\hline ( \begin{tabular}{c} 
c158 \\
c159 \\
\hline 159
\end{tabular} \& \({ }^{2}\) \& C330 \& 5
13 \& C817 \& \({ }_{13}^{6}\) \& \begin{tabular}{c} 
CR2702 \\
CR2703 \\
\\
\hline
\end{tabular} \& 7 \& \({ }_{\text {O284 }}^{\text {O285 }}\) \& 2 \& (02708 \& 7 \& \({ }_{\text {R223 }}^{\text {R223 }}\) \& 2 \& \({ }_{\text {R }}^{\text {R316 }}\) \& 5 \& \({ }_{\text {R4402 }}^{\text {R40 }}\) \& \({ }_{3}^{3}\) \& \({ }_{\substack{\text { R6607 } \\ \text { R609 }}}\) \& \({ }_{4}^{3}\) \&  \& \({ }_{2}\) \& R1003 \& 7 \\
\hline \({ }^{1} 171\) \& \({ }^{13}\) \& \({ }^{\text {c338 }}\) \& \({ }^{13}\) \& C819 \& \({ }^{6}\) \& \({ }^{\text {CR27204 }}\) \& 7 \& \({ }^{0301}\) \& 5 \& Q2711 \& 7 \& \({ }^{\text {R225 }}\) \& 2 \& \({ }^{\text {R317 }}\) \& 5 \& R411 \& 3 \& R610 \& 4 \& \({ }^{\text {A731 }}\) \& 2 \& R1005 \& 7 \\
\hline c172
c173
c17 \& \(\stackrel{13}{1}\) \& - \({ }_{\text {c339 }}^{\text {c351 }}\) \& 13
13 \& \({ }_{\text {c }}^{\text {c901 }}\) C902 \& 13
13 \& \begin{tabular}{c} 
CR2705 \\
CR2707 \\
\hline
\end{tabular} \& 7 \& \({ }_{\text {O}}^{0302}\) \& 5
5 \& \({ }^{\text {Q27712 }}\) O2713 \& 7 \& \({ }_{\text {R }}^{\text {R226 }}\) \& +13 \&  \& 5 \& \({ }_{\text {R }}^{\text {R412 }}\) \& \begin{tabular}{|}
3 \\
3
\end{tabular} \& \({ }_{\substack{\text { R6611 } \\ \text { R612 }}}^{\text {Re }}\) \& \({ }_{4}^{4}\) \& \({ }_{\text {R7733 }}^{\text {R732 }}\) \& \({ }_{13}^{2}\) \& R1006 \& 7 \\
\hline \({ }_{\text {cliso }} \mathbf{C}\) \& \({ }_{13}^{13}\) \& ( \({ }_{\text {ctan }}^{\text {c422 }}\) \& \({ }_{3}^{3}\) \& c903 \({ }_{\text {c904 }}\) \& 13 \& \(\mathrm{CRR2713}^{\text {Cra }}\) \& 7 \& 0304 \& 5 \& Q2715 \& 7 \& \({ }^{\text {R2288 }}\) \& \({ }_{2}\) \& \({ }_{\text {R3320 }}{ }_{\text {R32 }}\) \& 5 \& \({ }_{\text {R414 }}^{\text {R415 }}\) \& 3 \& \({ }_{\text {R613 }}^{\text {R613 }}\) \& \({ }_{4}^{4}\) \& \({ }^{\text {R7334 }}\) \& \({ }^{13}\) \& \({ }^{\text {R1008 }}\) \& 7 \\
\hline C190 \& 1 \& \({ }_{0}{ }^{423}\) \& 3 \& \({ }^{\text {c9010 }}\) \& 7 \& CR2715 \& 7 \& -0306 \& 5 \& \({ }^{\text {R12 }}\) \& 1 \& \({ }_{\text {R }} \mathrm{R} 2329\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R322 }}\) \& 5 \& \({ }_{\text {R416 }}\) \& \({ }_{3}\) \& \({ }_{\text {R615 }}\) \& \({ }_{4}^{4}\) \& \({ }_{\text {R8802 }}\) \& \({ }_{6}^{6}\) \&  \& 7 \\
\hline ( \({ }_{\text {c.191 }}^{\text {c201 }}\) \& 1 \&  \& \begin{tabular}{|}
3 \\
3
\end{tabular} \& C935 \& 7 \& CR2716 \& 7 \& \({ }_{\text {O}}^{0307}\) \& 5
5 \& (R13 \({ }_{\text {R22 }}\) \& 1 \& \({ }_{\text {R232 }}^{\text {R231 }}\) \& 2 \& \({ }_{\text {R332 }}^{\text {R323 }}\) \& 5
5 \& \({ }_{\substack{\text { R4417 } \\ \text { R420 }}}^{\text {R15 }}\) \& \(3_{3}^{3}\) \& \({ }_{\substack{\text { R } \\ \text { R617 }}}^{\text {8617 }}\) \& \({ }_{4}^{4}\) \&  \& \({ }_{6}^{6}\) \& R1020
R1021 \& 7 \\
\hline \({ }^{\text {c202 }}\) \& 2 \& \({ }^{4} 426\) \& 3 \& \({ }^{\text {c1002 }}\) \& 7 \& CR2718 \& 7 \& O309 \& 5 \& R23 \& 1 \& \({ }^{\text {R233 }}\) \& 2 \& ค326 \& 5 \& R421 \& 3 \& R618 \& 4 \& R805 \& 6 \& R1022 \& 7 \\
\hline c. \({ }_{\text {c203 }}^{\text {c204 }}\) \& \({ }_{2}^{2}\) \&  \& \({ }^{3}\) \& C1003
C1004
ctiol \& 7 \& DL21 \& 2 \& \({ }_{\text {a }}^{\text {a311 }}\) \& 5
5 \& \({ }_{\substack{\text { R101 } \\ \text { R102 }}}\) \& 1 \& \({ }_{\text {R }}^{\text {R235 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R }}^{\text {R328 }}\) \& 5 \& \({ }_{\text {R }}^{\text {R422 }}\) \& \({ }_{3}^{3}\) \&  \& 4 \& \(\underbrace{\substack{\text { R806 } \\ \text { R80 }}}_{\text {R }}\) \& \({ }_{6}^{6}\) \& R1023
R1024
R12 \& 7 \\
\hline \({ }^{2025}\) \& \({ }^{13}\) \& \({ }^{\text {c445 }}\) \& \({ }_{3}^{13}\) \& \({ }^{\text {C1005 }}\) \& \({ }^{13}\) \& DL22 \& 4 \& \({ }^{\text {a312 }}\) \& 5 \& R103 \& 1 \& \({ }^{\text {R238 }}\) \& 2 \& R329 \& 5 \& \({ }^{\text {R424 }}\) \& 3 \& R621 \& 4 \& R808 \& 6 \& R1025 \& 7 \\
\hline ( \({ }_{\text {c }}^{2068}\) \& \({ }_{2}^{13}\) \&  \& \({ }_{3}^{3}\) \& C11006 \& \begin{tabular}{|c}
13 \\
13
\end{tabular} \& DS901 \& 7 \&  \& 5
5 \& \({ }_{\substack{\text { RH104 } \\ \text { R105 }}}^{\text {R10 }}\) \& 1 \& \({ }_{\text {R }}^{\text {R241 }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R }}^{\text {R331 }}\) \& 5 \& (R426 \& \({ }_{3}^{3}\) \&  \& \({ }_{4}^{4}\) \& 号8899 \& \({ }_{6}^{6}\) \& R1026
R1027 \& 13
13 \\
\hline ( \({ }_{\text {c }}^{\text {c211 }}\) C212 \& 2 \& ( \({ }_{\text {c }}^{\text {c452 }}\) \& \(3_{3}^{3}\) \& C1102 \& \({ }_{3}^{13}\) \& \({ }_{\text {Ds902 }}^{\text {DS903 }}\) \& 7 \& \({ }_{\text {a }}^{\text {a317 }}\) \& 5 \& \({ }_{\substack{\text { R106 } \\ \text { R107 }}}\) \& 1 \& \({ }_{\text {R2243 }}^{\text {R242 }}\) \& 2 \& \({ }_{\text {R333 }}^{\text {R332 }}\) \& 5
5 \&  \& \(3_{3}^{3}\) \& \({ }_{\substack{\text { R624 } \\ \text { R625 }}}\) \& \({ }_{4}^{4}\) \& R811 \& \({ }_{6}^{6}\) \& R1208
R1101
R10 \& 7 \\
\hline - 212 \& +2 \&  \& \begin{tabular}{|}
3 \\
3 \\
3
\end{tabular} \& C1105
C1106
C10 \& \& - \& 7 \& -3318 \& 5 \& \({ }_{\text {RR108 }}\) \& 1 \& \({ }_{\substack{\text { R244 } \\ \text { R245 } \\ \text { R24 } \\ \hline}}\) \& 2
13 \& - \& 5
5
5 \&  \& \begin{tabular}{|}
3 \\
3 \\
3
\end{tabular} \& \({ }_{\text {8626 }} 68\) \& 4 \& \({ }_{\text {R }}^{\text {R813 }}\) \& \({ }_{6}^{6}\) \& \(\stackrel{\text { R1102 }}{ }\) \& 13 \\
\hline c215 \& \({ }_{13}^{13}\) \& -462 \& 3 \& \({ }^{\text {C1106 }}\) \& 3 \& \({ }_{\text {DS2702 }}\) \& 7 \& -321 \& 5 \& \({ }_{\text {R113 }}\) \& , \& \({ }_{\text {R }}^{\substack{\text { R248 } \\ \text { R24 }}}\) \& \({ }^{13}\) \& \({ }_{\text {R336 }}^{1335}\) \& 5 \& (R440 \& \({ }_{3}\) \& \({ }_{\text {R }}^{\text {R628 }}\) \& \({ }_{4}^{4}\) \&  \& \({ }_{6}^{6}\) \& R1103
R1104 \& \begin{tabular}{|}
3 \\
3
\end{tabular} \\
\hline ( \({ }_{\text {celt }}^{\text {c21 }}\) \& 13
13 \&  \& \({ }^{3}\) \& C1110
C1111 \& 3 \& DS2704 \& 7 \& \({ }_{0}^{0322}\) \& 5 \& \({ }_{\text {R }} \begin{gathered}\text { R114 } \\ \text { R115 }\end{gathered}\) \& 1 \& \({ }_{\text {Re250 }}^{\text {R250 }}\) \& 2 \& \({ }_{\text {R }}^{\text {R338 }}\) \& 5 \& \({ }_{\text {R4433 }}^{\text {R442 }}\) \& \(3_{3}^{3}\) \& \({ }_{\substack{\text { R } \\ \text { R631 } \\ \hline 630 \\ \hline}}\) \& \({ }_{4}^{4}\) \& \({ }_{\substack{\text { R816 } \\ \text { R819 }}}^{\text {R14 }}\) \& \({ }_{6}^{6}\) \& R1110
R111
R11 \& 3 \\
\hline ( \({ }_{\text {cele }}^{\text {c218 }}\) \& \(\stackrel{2}{2}\) \&  \& \(\begin{array}{r}13 \\ \hline\end{array}\) \& \({ }_{\substack{\text { C1114 } \\ C 1130}}\) \& 3 \& \({ }^{11}\) \& 1 \& \({ }^{\text {a325 }}\) \& 5 \& \({ }^{\text {R121 }}\) \& \& \({ }^{\text {R254 }}\) \& 2 \& R339 \& \({ }^{13}\) \& \({ }^{\text {R444 }}\) \& \({ }_{3}\) \& \({ }^{\text {R636 }}\) \& 4 \& R820 \& 6 \& R1112 \& 3 \\
\hline  \& 13
2
2 \& C477 \& - 13 \& C1130
C1154
c1, \& \({ }_{3}^{3}\) \& 等12 \& 1 \& - \({ }_{\text {a326 }}^{\text {a32 }}\) \& 5
5 \&  \& 1 \& \({ }_{\text {R }}\) \& \({ }_{2}^{2}\) \& \({ }_{\text {R }}^{\text {R34 }}\) \& 5 \& \({ }_{\substack{\text { R445 } \\ \text { R446 }}}^{\text {Res }}\) \& \({ }_{3}^{3}\) \& \({ }_{\substack{\text { R637 } \\ \text { R638 }}}^{\text {R37 }}\) \& \({ }_{4}^{4}\) \& \({ }_{\substack{\text { R8822 } \\ \text { R822 }}}^{\text {Ren }}\) \& \({ }_{6}^{6}\) \& R1113
R1114 \& 3
3 \\
\hline ( \({ }_{\text {c.221 }}^{\text {c222 }}\) \& \({ }_{2}^{2}\) \& ( \({ }_{\text {C482 }}^{\text {c483 }}\) \& 13
3 \& C1155 \& \({ }_{13}^{3}\) \& J14

J 5 \& 1 \& ${ }_{\text {a339 }}^{\text {a33 }}$ \& 5 \& ${ }_{\text {R }}^{\text {R125 }}$ \& ${ }_{1}^{2}$ \& ${ }_{\text {R262 }}^{\text {R260 }}$ \& 2 \& ${ }_{\substack{\text { R3343 }}}^{\text {R342 }}$ \& 5 \&  \& $3_{3}^{3}$ \& ${ }_{\substack{\text { R639 } \\ \text { R640 }}}^{\text {R }}$ \& ${ }_{4}^{4}$ \& R823
R825 \& ${ }_{6}^{6}$ \& ${ }_{\substack{\text { R2, } \\ \text { R1115 } \\ \text { R1116 }}}$ \& 3 <br>
\hline ${ }^{\text {c223 }}$ \& ${ }_{2}$ \& ${ }_{\text {C484 }}$ \& 3 \& C1159 \& 13 \& ${ }^{4927}$ \& 7 \& ${ }_{0} 031$ \& 5 \& ${ }_{\text {R132 }}$ \& 1 \& ${ }_{\text {R262 }}$ \& 2 \& ${ }_{\text {R344 }}$ \& 5 \& ${ }_{\text {R449 }}$ \& \& ${ }_{\text {R641 }}$ \& ${ }_{4}^{4}$ \&  \& ${ }_{6}$ \& ${ }_{\text {R1117 }}$ \& 3
3 <br>
\hline c225

c223 \& ${ }_{13}$ \& C486 \& 3 \& ${ }^{\text {c2720 }}$ \& \begin{tabular}{|c}
13 <br>
13

 \& J1204 \& ${ }^{13}$ \& ${ }_{\text {a }}^{\text {a33 }}$ \& 5 \& (R133 \& 1 \&  \& ${ }_{2}^{2}$ \&  \& $\stackrel{13}{5}$ \& ${ }_{\text {R }}^{\text {R451 }}$ \& 

3 <br>
3
\end{tabular} \& ${ }_{\substack{\text { R64 } \\ \text { R643 }}}^{\text {R4, }}$ \& ${ }_{4}^{4}$ \& ${ }_{\substack{\text { R8827 } \\ \text { R828 }}}^{\text {Re3 }}$ \& ${ }_{6}^{6}$ \& R1118

R1120

R12 \& | 3 |
| :---: |
| 3 | <br>

\hline  \& $\stackrel{2}{2}$ \&  \& 3 \& | C2273 |
| :--- |
| C 2704 | \& 7 \& ${ }_{\text {k100 }}$ \& 1 \& ${ }^{\text {Q }} 4.40$ \& 3 \&  \& 1 \& ${ }_{\text {R265 }}^{\text {R26 }}$ \& 2 \& ${ }_{\substack{\text { R3347 } \\ \text { R348 }}}$ \& 5 \&  \& 3 \& ${ }_{\text {R } 644}$ \& 4 \& ${ }^{8829}$ \& ${ }_{6}^{6}$ \& R1121

R122
R122 \& 3
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\hline - ${ }_{\text {c229 }}^{\text {c232 }}$ \& $\stackrel{13}{2}$ \& ( ${ }_{\text {c }}^{\text {c488 }}$ C489 \& | 3 |
| :---: |
| 3 |
| 3 | \& C22704

c2705 \& 7 \& ${ }_{\text {K101 }}^{\text {k102 }}$ \& 1 \&  \& ${ }_{3}^{3}$ \& ${ }_{\substack{\text { R136 } \\ \text { R137 } \\ \text { R13 }}}$ \& 1 \& ${ }_{\substack{\text { R226 } \\ \text { R267 }}}$ \& ${ }_{2}^{2}$ \& ${ }_{\text {chen }}^{\text {R334 }}$ \& 5 \& (R453 \& ${ }_{3}^{3}$ \& ${ }_{\text {R }}^{\substack{\text { R645 } \\ \text { R64 }}}$ \& ${ }_{4}^{4}$ \& ${ }_{\text {Re837 }}^{\text {R836 }}$ \& ${ }_{13}^{6}$ \& R11122
R1123 \& ${ }_{3}^{3}$ <br>
\hline C234 \& $\underset{13}{2}$ \& C491 \& ${ }_{2}^{2}$ \& C2206

c2707 \& 7 \& K103 \& 1 \& ( ${ }_{\text {Q }}^{\text {Q474 }}$ \& ${ }_{3}^{3}$ \& ${ }_{\substack{\text { R } \\ \text { R138 } \\ \text { R13 }}}$ \& 2 \& ${ }_{\substack{\text { R2268 } \\ \text { R269 }}}^{\text {268 }}$ \& 2 \& ${ }_{\substack{\text { R330 } \\ \text { R351 }}}^{\text {Re3 }}$ \& 5 \& ( $\begin{gathered}\text { R445 } \\ \text { R456 }\end{gathered}$ \& 3 \& ${ }_{\substack{\text { R } 64878}}^{\text {R647 }}$ \& ${ }_{4}^{4}$ \& ${ }_{\substack{\text { R840 } \\ \text { R841 }}}$ \& 6 \& (R1124 \& | 3 |
| :---: |
| 3 | <br>

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\end{tabular}

| A10－MAIN BOARD（cont） <br> （SN B710099 AND BELOW， 2246 1Y <br> SN B110099 AND BELOW， 2246 MOD A） |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| cincuit | ${ }_{\text {SCHEM }}$ | CIRCUIT | SCHEM NUMBER | CIRCUTT <br> number | SCHEM | CIRCUIT NUMBER | SCHEM | CIRCUTT | SCHEM NUMEER | Clincuit | SCHEM <br> NUMBER |
| R140 | 2 | R270 | 2 | ${ }^{\text {R332 }}$ | 5 | ${ }^{\text {R460 }}$ | 3 | ${ }^{\text {R649 }}$ | 4 | R842 | 6 |
| ${ }_{\substack{\text { R141 } \\ \text { R142 }}}$ | 1 | ${ }_{\text {R271 }}^{\text {R272 }}$ | 2 | R353 | ${ }_{5}^{6}$ | R461 | $3_{3}^{3}$ | ${ }_{\substack{\text { R650 } \\ \text { R651 }}}$ | ${ }_{4}^{4}$ | ${ }_{\text {R8843 }}^{\text {R84 }}$ | ${ }_{6}^{6}$ |
| ${ }_{\text {R142 }}^{\text {R142 }}$ | 1 | ${ }_{\text {R273 }}^{\text {R272 }}$ | ${ }_{2}^{2}$ | ${ }_{\text {R3354 }}^{\text {R33 }}$ | 5 | R462 ${ }_{\text {R463 }}$ | ${ }_{3}^{3}$ | ${ }_{\substack{\text { R651 } \\ \text { R65 }}}^{\text {Re9 }}$ | ${ }_{4}^{4}$ | ${ }_{\substack{\text { R8844 } \\ \text { R845 }}}^{\text {den }}$ | ${ }_{6}^{6}$ |
| ${ }_{\text {R }}^{\substack{\text { R151 } \\ \text { R152 }}}$ | 1 | $\stackrel{\text { R274 }}{ }$ | 2 | ${ }_{\text {R356 }}$ | 5 | $\stackrel{\text { R470 }}{ }$ | 3 | ${ }_{\text {R6533 }}$ | 4 | ${ }_{\substack{\text { R846 }}}^{\text {R84 }}$ | ${ }_{6}$ |
| R153 | 1 | R275 | 2 | ${ }^{\text {R357 }}$ | 5 | 8471 | 3 | R654 | 4 | R847 | 6 |
| （R154 ${ }_{\text {R15 }}^{\text {R15 }}$ | 1 | ${ }_{\substack{\text { R276 } \\ \text { R277 }}}^{\text {R27 }}$ | 2 | ${ }_{\substack{\text { R3358 } \\ \text { R359 }}}^{\text {R39 }}$ | －6 | ${ }_{\text {R4473 }}^{\text {R472 }}$ | $3_{3}^{3}$ | ${ }_{\substack{\text { R6555 } \\ \text { R65 }}}^{\text {R }}$ | ${ }_{4}^{4}$ | ${ }_{\substack{\text { R8488 } \\ \text { R849 }}}^{\text {R48 }}$ | ${ }_{6}^{6}$ |
| R156 | 1 | R278 | 2 | ${ }_{\text {R360 }}$ | 5 | R474 | 3 | ${ }^{\text {R657 }}$ | 4 | R850 | 6 |
| ${ }_{\text {R15 }} \mathrm{R} 158$ | 1 | ${ }^{\text {R279 }}$ | 2 | ${ }_{\text {R361 }}{ }^{\text {336 }}$ | 5 | ${ }_{\text {R476 }}$ | $3_{3}^{3}$ | ${ }_{\text {R658 }}^{\text {R65 }}$ | 4 | ${ }^{\text {R85 }}$ | ${ }_{6}^{6}$ |
| ${ }^{\text {R1588 }}$ | ${ }_{1}^{2}$ | ${ }^{\text {R280 }}$ | ${ }_{2}^{2}$ | ${ }_{\text {R362 }}$ | 5 5 | ${ }_{\text {R }}^{\text {R478 }}$ | 3 <br> 3 | ${ }^{8659}$ | ${ }_{4}^{4}$ | ${ }_{\text {R852 }}^{\text {R85 }}$ | ${ }_{6}^{6}$ |
| ${ }_{\text {R }} \times 1.159$ | 2 |  | ${ }_{13}^{2}$ | ${ }_{\text {R364 }}^{\text {R363 }}$ | 5 | R479 | 3 | ${ }_{\text {R663 }}$ | 4 | ${ }_{\text {R854 }}$ | 6 |
| R161 | 1 | ${ }^{2883}$ | ${ }^{13}$ | R365 | 5 | ${ }^{\text {R481 }}$ | ${ }^{13}$ | R664 | 4 | R855 | ${ }^{6}$ |
| ${ }_{\substack{\text { R162 } \\ \text { R171 }}}$ | 1 | ${ }_{\text {R285 }}^{\text {R284 }}$ | 2 | ${ }_{\text {R3667 }}^{\text {R366 }}$ | 5 | ${ }_{\text {R484 }}^{\text {R483 }}$ | ${ }_{3}^{3}$ | ${ }_{\text {R6665 }}^{\text {R665 }}$ | ${ }_{4}^{4}$ | ${ }_{\substack{\text { R8566 } \\ \text { R85 }}}^{\text {Re4 }}$ | ${ }_{6}^{6}$ |
| R175 | 1 | ${ }^{\text {R286 }}$ | 2 | R369 | 6 | R485 | 3 | ${ }^{8669}$ | 4 | R858 | 6 |
| ${ }_{\substack{\text { R176 } \\ \text { R177 }}}$ | 1 | ${ }_{\substack{\text { R287 } \\ \text { R288 }}}$ | 2 |  | 5 | R486 | $3_{3}^{3}$ | ${ }_{\text {R670 }}^{\text {R67 }}$ | 4 | ${ }_{\text {R906 }}^{\text {R90 }}$ | 7 |
| ${ }_{\text {R }}^{\text {R178 }}$ | 1 | ${ }_{\text {R289 }}^{\text {R288 }}$ | ${ }_{2}^{2}$ | ${ }_{\text {R372 }}^{\text {R331 }}$ | 5 5 |  | 3 <br> 3 <br> 3 | ${ }_{\substack{\text { R6727 } \\ \text { R67 }}}^{\text {Ref }}$ | ${ }_{4}^{4}$ | ${ }_{\substack{\text { R9007 } \\ \text { R908 }}}^{\text {Reg }}$ | 7 |
| R179 | 1 | R290 | 2 | R373 | 5 | R491 | 3 | R701 | 13 | R999 | 7 |
| ${ }^{\text {R180 }}$ | ， | ${ }_{\text {R291 }}^{\text {R292 }}$ | 2 | ${ }_{\text {R3374 }}^{\text {R33 }}$ | ${ }_{5}^{13}$ | Ra92 ${ }_{\text {R493 }}$ | $3_{3}^{3}$ | ${ }_{\text {R702 }}$ | 2 | R910 | 7 |
| ¢ ${ }_{\text {R }}^{\text {R181 }}$ | 13 | ${ }_{\text {R }}{ }_{\text {R2932 }}$ | ${ }_{2}^{2}$ | ${ }_{\text {R3376 }}^{\text {R33 }}$ | 5 | ${ }_{\text {R494 }}$ | ${ }_{3}$ | ${ }_{\text {R7706 }}$ | ${ }_{2}^{2}$ | ${ }_{\text {R9915 }}$ |  |
| R201 | 2 | ${ }^{2} 294$ | 2 | R377 | 5 | R495 | 3 | R707 | 2 | $\mathrm{R}^{\text {R916 }}$ | ${ }^{13}$ |
| ${ }^{\text {R2022 }}$ | 2 | ${ }^{\text {R295 }}$ | ${ }_{2}$ | ${ }_{\text {R3788 }}$ | 5 | ${ }^{\text {R4996 }}$ | 3 | R708 | ${ }^{2}$ | ${ }^{\text {R9220 }}$ |  |
| ${ }_{\text {R204 }}$ | ${ }_{2}$ | ${ }_{\text {R297 }}^{\text {R296 }}$ | ${ }_{13}^{2}$ | ${ }_{\text {R330 }}{ }_{\text {R379 }}$ | 5 | －${ }_{\text {R4998 }}$ | 3 | $\underset{\text { R7710 }}{ }$ | 13 <br> 2 <br> 2 | ${ }_{\substack{\text { R922 } \\ \text { R922 }}}^{\text {R92 }}$ | 7 |
| ${ }^{\text {R205 }}$ | 2 | ${ }^{2298}$ | ${ }^{13}$ | ${ }^{\text {R381 }}$ | 5 | ${ }^{\text {R501 }}$ | 4 | ${ }^{\text {R711 }}$ | 2 | ${ }^{\text {R9223 }}$ | 7 |
| ${ }_{\text {R }}^{\text {R20\％}}$ | ${ }_{2}$ | ${ }_{\text {R }}^{\text {R301 }}$ | ${ }_{5}^{2}$ | ${ }_{\text {R }}^{\text {R383 }}$ | 5 | R503 | ${ }_{7}^{4}$ | ${ }_{\text {R7115 }}^{\text {R772 }}$ | ${ }_{2}^{2}$ | $\substack{\text { R930 } \\ \text { R930 }}_{\text {Re29 }}$ | 7 |
| ${ }^{2008}$ | ${ }^{13}$ | R302 | 5 | ค384 | 5 | R504 | 13 | R716 | 2 | ค931 | 7 |
| ${ }^{\text {R209 }}$ | 2 | R303 | 5 | ${ }_{\text {R385 }}$ | 5 | R505 | 7 | ${ }_{8}^{8717}$ | ${ }^{2}$ | ${ }_{\text {R932 }}$ | 7 |
| R210 | ${ }_{2}^{2}$ | ${ }_{\text {R }}^{\text {R304 }}$ | 5 | ${ }_{\text {R387 }}^{\text {R386 }}$ | 5 | R506 | 7 | ${ }_{\substack{\text { R717 } \\ \text { R719 }}}^{\text {R18 }}$ | ${ }_{2}^{2}$ | $\stackrel{\text { R934 }}{\text { R933 }}$ |  |
| ${ }^{2} 212$ | 2 | R306 | 5 | R338 | 5 | R508 | 7 | R720 | 2 | R935 | 7 |
| ${ }_{\substack{\text { R213 } \\ \text { R214 }}}^{\text {R213 }}$ | ${ }_{2}^{2}$ | ${ }_{\text {R }}^{\text {R308 }}$ | $\begin{array}{r}5 \\ 5 \\ \hline\end{array}$ | ${ }_{\text {R3930 }}^{\text {R390 }}$ | $\stackrel{13}{5}$ | ${ }_{\text {R }}^{\text {R }}$ R512 | 7 | ${ }_{\text {R722 }}^{\text {R721 }}$ | ${ }_{2}^{2}$ | ${ }_{\substack{\text { R9936 } \\ \text { R93 }}}^{\text {Re9 }}$ | 7 |
| ${ }^{2} 215$ | 2 | R309 | 5 | R392 | ${ }^{13}$ | R601 | 4 | ${ }^{\text {R723 }}$ | ${ }^{13}$ | R938 | 7 |
| ${ }_{\text {R }}^{\text {R219 }}$ | ${ }_{2}^{2}$ | ${ }_{\text {R }}^{\text {R311 }}$ | 5 | ${ }_{\text {R }}^{\text {R394 }}$ | 5 | ${ }_{\substack{\text { R602 } \\ \text { R603 }}}$ | ${ }_{4}^{4}$ | R724 R2725 | ${ }_{2}^{2}$ | ${ }_{\substack{\text { R9939 } \\ \text { R930 }}}^{\text {Re39 }}$ | 7 |
| ${ }^{2220}$ | 2 | R312 | ${ }^{13}$ | R395 | 5 | 8604 | 4 | R726 | 2 | R941 | 7 |
| ${ }^{\text {R221 }}$ | 2 | ${ }^{\text {R3313 }}$ | 5 | ${ }^{\text {R396 }}$ | 5 | R605 | $4_{4}^{4}$ | ${ }^{\text {R2727 }}$ | ${ }_{2}^{2}$ | ${ }^{\text {R1001 }}$ | 7 |
|  | ${ }_{2}^{2}$ | ${ }_{\text {R315 }}^{\text {R314 }}$ | 5 | ${ }_{\text {R }}^{\text {R402 }}$ | ${ }_{3}$ | ${ }_{\text {R }}^{\text {R6007 }}$ | ${ }_{3}^{4}$ | R7288 R729 | 2 | R⿴囗才， | 7 |
| ${ }^{\text {R224 }}$ | 2 | ${ }^{\text {R316 }}$ | 5 | R410 | ${ }^{3}$ | R609 | 4 | R730 | 2 | R1004 | 7 |
| （ ${ }_{\text {R }}^{\text {R225 }}$ | 13 | ${ }_{\text {R318 }}^{\text {R317 }}$ | 5 5 | ${ }_{\text {R }}^{\text {R4112 }}$ | 3 <br> 3 <br> 3 | R610 | ${ }_{4}^{4}$ | R731 R732 | 2 | （R1005 | 7 |
| ${ }_{\substack{\text { R227 } \\ \text { R228 }}}$ | 2 | ${ }_{\text {R3320 }}^{\text {R319 }}$ | 5 | －${ }_{\text {R413 }}$ | ${ }_{3}^{3}$ | R612 | 4 | R733 R734 | 13 | ${ }_{\substack{\text { R1007 } \\ \text { R1008 }}}$ | 7 |
| ${ }_{\substack{\text { R228 } \\ \text { R229 }}}^{\text {R23 }}$ | ${ }_{2}^{2}$ | ${ }_{\text {R }}^{\text {R321 }}$ | 5 | ${ }_{\text {R415 }}^{\text {R414 }}$ | 3 | ${ }_{\text {R6614 }}$ | ${ }_{4}^{4}$ | R734 R801 | 13 6 | （R1008 | 7 |
| ${ }^{2330}$ | 2 | R322 | 5 | R416 | 3 | R615 | 4 | R802 | 6 | R1010 | 7 |
| ${ }_{\substack{\text { R232 } \\ \text { R231 }}}^{\text {R23 }}$ | ${ }_{2}$ | ${ }_{\text {R325 }}^{\text {R323 }}$ | 5 5 | ${ }_{\text {R }}^{\text {R420 }}$ | 3 <br> 3 | ${ }_{\text {R }}^{\text {R616 }}$ | ${ }_{4}^{4}$ | R803 R804 | ${ }_{6}^{6}$ | R1020 R1021 | 7 |
| ${ }_{\text {R233 }}^{\text {R234 }}$ | 2 | ${ }_{\substack{\text { R326 } \\ \text { R327 }}}$ | 5 | ${ }_{\text {R421 }}$ | 3 | ${ }_{\text {R618 }} 6$ | 4 | R805 | ${ }_{6}^{6}$ | ${ }_{\text {R1022 }}$ | 7 |
| ${ }_{\text {R235 }}$ | ${ }_{2}$ | ${ }_{\text {R328 }}$ | 5 | ${ }_{\text {R423 }}$ | 3 | ${ }_{\text {R620 }}$ | 4 | ${ }_{\text {R }}$ | ${ }_{6}$ | ${ }_{\text {R1024 }}$ |  |
| ${ }^{\text {2238 }}$ | 2 | R329 | 5 | ${ }^{\text {R424 }}$ | ${ }^{3}$ | R621 | 4 | R808 | 6 | $\mathrm{R}^{1} 1225$ | 7 |
| ${ }_{\substack{\text { R24 } \\ \text { R240 } \\ \text { R230 }}}$ | 2 | ${ }_{\text {R331 }}^{\text {R330 }}$ | 5 5 | ${ }_{\text {R4426 }}^{\text {R425 }}$ | ${ }_{3}^{3}$ | R622 | ${ }_{4}^{4}$ | R809 R810 | ${ }_{6}^{6}$ | ${ }_{\substack{\text { R1026 } \\ \text { R1027 }}}^{1029}$ | 13 13 13 |
| ${ }^{\text {2242 }}$ | 2 | ${ }^{\text {R3323 }}$ | 5 | ${ }^{\text {R430 }}$ | $3_{3}^{3}$ | ${ }^{\text {R624 }}$ | 4 | ${ }^{\text {R811 }}$ | ${ }_{6}$ | ${ }^{\text {R1028 }}$ | 7 |
| ${ }_{\text {R }}^{\text {R244 }}$ | ${ }_{2}^{2}$ | ${ }_{\text {R }}^{\text {R334 }}$ | 5 | ${ }_{\substack{\text { R4331 } \\ \text { R432 }}}^{\text {R }}$ | 3 <br> 3 | R626 | ${ }_{4}^{4}$ |  | ${ }_{6}^{6}$ | R1101 R1102 | 13 <br> 13 |
| ${ }^{2245}$ | ${ }^{13}$ | ：1335 | 5 | ${ }^{\text {R440 }}$ | 3 | ${ }^{6627}$ | 4 | R814 | 6 | R1103 | 3 |
|  | ${ }_{2}^{2}$ | $\stackrel{\text { R333 }}{\text { R33 }}$ | 5 | ${ }_{\text {R442 }}^{\text {R44，}}$ | 3 | ${ }_{\text {R630 }}$ | ${ }_{4}^{4}$ | ${ }_{\text {R8816 }}$ | ${ }_{6}$ | ${ }_{\text {R1110 }}$ | ${ }_{3}$ |
| ${ }_{\text {R251 }}^{\text {R25 }}$ | 2 | ${ }_{\text {R338 }}^{\text {R339 }}$ |  | ${ }^{\text {R443 }}$ | ${ }_{3}^{3}$ | ${ }^{\text {R631 }}$ | 4 | R819 | ${ }_{6}$ | ${ }^{\text {R1111 }}$ | ${ }_{3}^{3}$ |
| ${ }_{\text {R255 }}$ | ${ }_{2}$ | ${ }_{\text {R334 }}$ | ${ }_{5}$ | ${ }_{\text {R4445 }}$ | 3 | ${ }_{\text {R637 }}$ | 4 | R821 | 6 | R1113 | 3 |
| ${ }_{\substack{\text { R236 } \\ \text { R260 }}}$ | 2 | ${ }_{\text {R342 }}^{\text {R341 }}$ | 5 | ${ }_{\text {R }}^{\text {R446 }}$ | 3 | R638 | ${ }_{4}^{4}$ | R822 | ${ }_{6}^{6}$ | R1114 | 3 |
| ${ }^{2661}$ | 2 | ${ }_{\text {R343 }}$ | 5 | ${ }^{\text {R4448 }}$ | ${ }_{3}$ | ${ }_{\text {8640 }} 6$ | 4 | 号825 | ${ }_{6}$ | ${ }_{\text {R1116 }}$ | 3 |
| ${ }_{\substack{\text { R262 } \\ \text { R263 }}}^{\text {Red }}$ | ${ }_{2}$ | ${ }_{\text {R }}^{\text {R345 }}$ | 5 <br> 13 | （2449 | 3 <br> 3 | ${ }_{\text {R }}^{\text {R642 }}$ | ${ }_{4}^{4}$ | R826 R827 | ${ }_{6}^{6}$ | ${ }_{\substack{\text { R1117 } \\ \text { R1118 }}}^{\text {R115 }}$ | 3 <br> 3 <br> 3 |
| ${ }_{\substack{\text { R264 } \\ \text { R265 }}}^{\text {Re3 }}$ | 2 | ${ }_{\substack{\text { R346 } \\ \text { R347 }}}$ | 5 |  | 3 3 |  | ${ }_{4}^{4}$ | R8828 R289 | ${ }_{6}^{6}$ | ${ }_{\substack{\text { R1120 } \\ \text { R1121 }}}$ | ${ }_{3}^{3}$ |
| ${ }_{\text {R266 }}$ | ${ }_{2}$ | ${ }_{\text {R348 }}^{\text {R347 }}$ | 5 | － $\begin{aligned} & \text { R442 } \\ & \text { R453 }\end{aligned}$ | 3 | $\underset{\text { R645 }}{\substack{\text { R644 } \\ \hline}}$ | ${ }_{4}^{4}$ |  | ${ }_{6}$ | $\stackrel{\text { R1122 }}{ }$ | ${ }_{3}$ |
| ${ }^{\text {R267 }}$ | 2 | ${ }^{\text {R3499 }}$ | 5 | ${ }^{\text {R454 }}$ | $3_{3}^{3}$ | ${ }^{\text {R646 }}$ | 4 | ${ }^{\text {R837 }}$ | ${ }^{13}$ | ${ }^{\text {R123 }}$ | $3_{3}$ |
| ${ }_{\text {R269 }}$ | 2 | ${ }_{\text {R351 }}$ | 5 | ${ }_{\text {R456 }}$ | 3 | ${ }_{\text {R6488 }}$ | 4 | R841 | 6 | R1125 | 3 |

\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline \multicolumn{12}{|c|}{\begin{tabular}{l}
A10－MAIN BOARD（cont） \\
（SN B710099 AND BELOW， 2246 1Y \\
SN B110099 AND BELOW， 2246 MOD A）
\end{tabular}} \\
\hline CIRCUIT \& SCHEM \& Cincuit \& SCHEM \& \begin{tabular}{l}
circuit \\
NUMBER
\end{tabular} \& SCHEM \& CIRCUIT NUMBER \& SCHEM NUMBER \& CiRCUIT \& SCHEM \& CIRCUIT number \& SCHEM \\
\hline \({ }^{\text {R1126 }}\) \& \({ }^{3}\) \& R2736 \& 7 \& \(\mathrm{U}_{240}\) \& \({ }^{13}\) \& 4604 \& \({ }^{13}\) \& W17 \& 6 \& w810 \& 6 \\
\hline R1127
R1128 \& 3
3 \& \({ }_{\text {R }}^{\text {R22737 }}\) \& 7 \& U260
U260 \& \({ }_{13}^{2}\) \& U606
U606 \& \(\stackrel{4}{13}\) \& w／8
\(\mathbf{w} 19\) \& \({ }_{2}^{6}\) \& W811 \& 2 \\
\hline R1131 \& 3 \& R2739 \& 7 \& U280 \& \& 4701 \& 2 \& W20 \& 2 \& w900 \& \({ }^{13}\) \\
\hline \({ }^{\text {R1132 }}\) \& 3 \& \({ }^{\text {R2740 }}\) \& 7 \& 4301 \& 5 \& 4701 \& \({ }^{13}\) \& w100 \& 1 \& W1000 \& 7 \\
\hline \({ }^{\text {R1133 }}\) \& \({ }_{3}^{3}\) \& \({ }_{\text {R2271 }} \mathbf{R}\) \& 7 \& U301
U301 \& －6 \({ }_{13}\) \& \({ }_{4}^{4702}\) \& －\({ }_{13}^{2}\) \& W101 \& 1 \& W1010
w1101 \& 7 \\
\hline \({ }_{\text {R }}{ }_{\text {R1135 }}\) \& 3 \& \({ }_{\text {R22743 }}\) \& 7 \& U302 \& \({ }_{5}^{13}\) \& U801 \& \({ }_{2}^{13}\) \& w103 \& \({ }^{13}\) \& W1102 \& \\
\hline \({ }_{\substack{\text { R1136 } \\ \text { R1142 }}}^{1 / 148}\) \& \(3_{3}^{3}\) \& \({ }_{\text {R2275 }}^{\text {R275 }}\) \& 7 \& U302 \& 13
5 \& U801 \& \({ }_{13}^{6}\) \& W200 \& 2 \& W1103
w1104 \& \({ }_{3}^{13}\) \\
\hline R1143 \& 3 \& \({ }_{\text {R2751 }}\) \& 7 \& U303 \& \({ }^{13}\) \& U802 \& \({ }_{6}^{13}\) \& W202 \& 2 \& W1105 \& 3 \\
\hline  \& \begin{tabular}{|}
3 \\
3
\end{tabular} \& R2758
R2760 \& 7 \& U304 \& 5
13 \& บ802 \& 13
13 \& W203 \({ }_{\text {W }}^{\text {W205 }}\) \& 2 \& W1106 \& \({ }_{3}^{3}\) \\
\hline R1145
R11150
R11 \& \begin{tabular}{|}
3 \\
3
\end{tabular} \& \({ }_{\text {R }}^{\text {R27760 }}\) \& 7 \& U304 \& \begin{tabular}{|c}
13 \\
5
\end{tabular} \& －9901 \& \begin{tabular}{|}
13 \\
7
\end{tabular} \& W206 \& \({ }_{2}\) \& W1107 \& \({ }^{3}\) \\
\hline R1154 \& 3 \& R2783 \& 13 \& U307 \& \({ }^{13}\) \& บ930 \& 13 \& w207 \& 2 \& W1200 \& 13 \\
\hline \({ }^{\text {R1155 }}\) \& 3 \& \({ }^{\text {R2784 }}\) \& 7 \& บ308 \& \({ }^{5}\) \& \({ }^{\text {¢ }} 9331\) \& 7 \& W208 \& 2 \& \({ }_{\text {W }}{ }^{\text {W }} 1201\) \& \({ }_{13}^{13}\) \\
\hline （17158 \& \begin{tabular}{|l}
13 \\
13
\end{tabular} \& \({ }_{\text {R }} \begin{aligned} \& \text { R27785 } \\ \& \text { R278 }\end{aligned}\) \& 7 \& U308 \& \begin{tabular}{|}
13 \\
5
\end{tabular} \& －931 \& 13
13 \& － \(\begin{aligned} \& \text { W209 } \\ \& \text { W210 }\end{aligned}\) \& \({ }_{2}^{2}\) \& W1202
wi204 \& \begin{tabular}{|l}
13 \\
13
\end{tabular} \\
\hline \({ }^{\text {R11162 }}\) \& 3 \& \({ }^{\text {R2787 }}\) \& 7 \& U309 \& \({ }^{13}\) \& 41001 \& 7 \& \({ }^{2} 223\) \& 2 \& \({ }^{W} 1205\) \& \({ }^{13}\) \\
\hline \({ }_{\substack{\text { R1163 } \\ \text { R1170 }}}^{1 / 29}\) \& 3 \& \({ }_{\substack{\text { R27788 } \\ \text { R2789 }}}\) \& 7 \& U310 \& \({ }^{5}\) \& U1001 \& 13 \& W231 \& \({ }_{2}^{2}\) \& W1209 \& 13
13
13 \\
\hline R2701 \& 7 \& R2795 \& 7 \& U311 \& 5 \& U1101 \& 7 \& w235 \& \({ }^{13}\) \& w1216 \& 13 \\
\hline  \& 7 \& R2796 \& 7 \& U313 \& 13
5 \& U1101 \& \begin{tabular}{|c}
13 \\
3
\end{tabular} \&  \& \({ }_{6}\) \& \({ }_{\text {W }}^{\text {W }}\) W2178 \& 13
13 \\
\hline R2704 \& 7 \& RT201 \& 2 \& U315 \& 5 \& U1102 \& \({ }^{13}\) \& w401 \& \({ }^{3}\) \& \({ }^{\mathbf{W}} \mathbf{1 2 2 1}\) \& 13 \\
\hline R27705
R2706 \& 7 \& U112 \& 1 \& U315 \& 13
5 \& U1103 \& \({ }_{13}^{3}\) \& W603 \({ }_{\text {w }}^{\text {w }}\) \& \({ }_{3}^{3}\) \& W1222 \& \begin{tabular}{|l}
13 \\
13
\end{tabular} \\
\hline R2778 \& 7 \& \({ }_{4}^{412}\) \& \({ }_{1}^{13}\) \& \({ }^{3} 316\) \& \({ }_{3}^{13}\) \& \begin{tabular}{|c}
41104 \\
\hline 1104 \\
\hline
\end{tabular} \& \({ }^{3}\) \& wa05 \& \(3_{3}^{3}\) \& wi231
\(\mathbf{w} 1237\)
\(\mathbf{w}\) \& \({ }^{13}\) \\
\hline R2779
R2710
R211 \& 7 \& U122 \& \({ }_{13}^{13}\) \& U 4221
U421 \& 13
13 \& U1104 \& \({ }^{13}\) \& W406 \& \({ }_{3}^{3}\) \& W1237
\(\mathbf{w} 1247\)
\(\mathbf{w} 2123\) \& 13
13 \\
\hline \({ }_{\text {R2711 }}\) \& 7 \& 4171 \& 1 \& \({ }^{4} 431\) \& 13 \& U1106 \& 13 \& W408 \& \({ }_{3}^{3}\) \& \({ }_{\text {W1248 }}\) \& \({ }^{13}\) \\
\hline \({ }_{\text {R }} \begin{aligned} \& \text { R2772 } \\ \& \mathrm{R} 2713\end{aligned}\) \& 7 \&  \& \({ }^{13}\) \& U431 \& \begin{tabular}{|c}
13 \\
3
\end{tabular} \& va301 \& \& （ \(\begin{aligned} \& \text { w／410 } \\ \& \text { w411 }\end{aligned}\) \& \(3_{3}^{3}\) \& W1249 \& 13
13 \\
\hline \({ }^{\text {R2274 }}\) \& 7 \&  \& \({ }_{1}^{13}\) \& U441 \& \({ }_{4}^{13}\) \& VR3302 \& 5 \& \({ }_{\text {w }}\) \& 3
3
3 \& W1251
\(\mathbf{w} 125\)
\(\mathbf{w} 125\) \& \begin{tabular}{|c}
13 \\
13 \\
\hline 1
\end{tabular} \\
\hline R27
R2715
R27 \& 7 \& U173
U173 \& 13 \& U501 \& \({ }_{13}^{4}\) \& VR303 \& 5
5 \&  \& \begin{tabular}{|}
3 \\
3
\end{tabular} \& W1252 \& 13
13
13 \\
\hline \({ }_{\text {R2717 }}\) \& 7 \& \({ }^{174}\) \& 1 \& U502 \& \(\stackrel{4}{4}\) \& VR308 \& \({ }_{5}^{5}\) \& wa15 \& 2 \& \begin{tabular}{c} 
W1277 \\
\(\mathbf{w} 1288\) \\
\hline
\end{tabular} \& \({ }^{13}\) \\
\hline  \& 7 \& U201 \& 2 \& \({ }_{4} 503\) \& \({ }_{4}^{13}\) \& Vh339 \& 5 \& \({ }_{\text {w505 }}\) \& \({ }_{4}^{2}\) \& \({ }_{\text {W }}^{\text {W2302 }}\) \& 7 \\
\hline R2720
R2721 \& 7 \& 1201
4202 \& －13 \& U503 \& \(\stackrel{13}{7}\) \& VR311 \& 5 \& W510 \& 4 \& W2302 \& \(\stackrel{13}{7}\) \\
\hline R2722 \& \& 4202 \& \({ }^{13}\) \& U506 \& 13 \& vR801 \& 6 \& w604 \& 4 \& W2502 \& \({ }^{4}\) \\
\hline \begin{tabular}{c} 
R2723 \\
R2724 \\
\hline
\end{tabular} \& 7 \& U203
U203 \& \(\stackrel{2}{13}\) \& U6000 \& \({ }_{13}^{4}\) \& VR802
VR2701 \& \({ }_{7}^{6}\) \& W605 \& 4 \& W2502 \& \({ }_{7}^{13}\) \\
\hline \({ }_{\substack{\text { R2726 } \\ \text { R2727 }}}\) \& 7 \& 2210

U210 \& 2 \& 6001
U601 \& 4
4
13 \& \& \& W611 \& 4 \& －600 \& <br>

\hline ${ }_{\text {R }}^{\substack{\text { R222727 } \\ \text { R2728 }}}$ \& 7 \& （ $\begin{aligned} & 2210 \\ & \text { U220 }\end{aligned}$ \& | 13 |
| :---: |
| 2 | \& －6002 \& 13 \& ${ }_{\text {w }}{ }_{\text {w } 11}$ \& 1 \& W802 \& ${ }_{5}^{4}$ \& Y600 \& 4 <br>

\hline ${ }^{\text {R22729 }}$ \& 7 \& ${ }^{21220}$ \& ${ }_{2}^{13}$ \& U602 \& ${ }_{1}^{13}$ \& W12 \& 1 \& weos \& ${ }_{6}^{6}$ \& \& <br>

\hline | R2733 |
| :--- |
| R2234 | \& 7 \& | 23 |
| :---: |
| U230 |
| U230 | \& 2 \& －6603 \& 4 \& ${ }_{\text {W14 }}$ \& 1 \& W807 \& ${ }_{6}$ \& \& <br>

\hline R2735 \& 7 \& U240 \& 2 \& U604 \& 4 \& W16 \& 7 \& w808 \& 6 \& \& <br>
\hline
\end{tabular}

## WAVEFORMS FOR DIAGRAM 1



Figure 9-7. A9-2 mV Bandwidth board for SN B700100 to B714999 (2246 1Y), and SN B100100 to B119999 (2246 MOD A)

| A9-2 mV Bandwidth board |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | semem | cincur | Sctibu |  | scitem |
|  | ! |  |  |  |  |

(\#) $\begin{aligned} & \text { Static Sensitive Devices } \\ & \text { See Mantenance Section }\end{aligned}$




Figure 9-7. A9-2 mV Bandwidth board for SN B700100 to B714999 (2246 1Y) and SN B100100 to B119999 (2246 MOD A).

| A9-2 mV BANDWIDTH BOARD |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT | SCHEM number | CIRCUIT | SCHEM | CIRCUIT <br> NUMBER | SCHEM |
| ${ }_{\substack{c \\ C 185 \\ C 186}}$ | 1 | ${ }_{\substack{\text { R185 }}}$ | 1 | ${ }_{\text {W }} \mathbf{W} 106$ |  |
| (188 | 1 | ${ }_{\substack{\text { R188 } \\ \text { R189 }}}$ | 1 | ¢ $\begin{gathered}\text { w107 } \\ \text { w108 }\end{gathered}$ | 1 |
| 0181 |  | VR101 | 1 | W222 | 1 |
| 0182 | 1 | w105 | 1 |  |  |

\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline \multicolumn{12}{|c|}{VERTICAL INPUTS DIAGRAM 1 (SN B710100 AND ABOVE, 2246 1Y SN B110100 AND ABOVE, 2246 MOD A)} <br>
\hline \multicolumn{12}{|l|}{ASSEmbly al0} <br>
\hline Clircuit
NUMBER \& - SCHEM \&  \& Clircurn \& $$
\begin{aligned}
& \text { SCHEMEM} \\
& \text { LOCATON }
\end{aligned}
$$ \& $$
\begin{array}{|l|l|l|l|l|l|l|l|}
\hline \text { LOCATIN }
\end{array}
$$ \& CIRCUT \& SCHEM LOCATION \& BOARD \& Circuit \& $\stackrel{\text { SCHEM }}{ }$ \& - Boand <br>
\hline ${ }_{\text {AT1 } 117}^{\text {AT }}$ \& ${ }_{3 H}^{1 J}$ \& ${ }_{48}^{68}$ \& ${ }_{\text {R151 }}^{\text {R152 }}$ \& ${ }_{88}^{88}$ \& ${ }_{2}^{1 A}$ \& ${ }_{\substack{0.151 / A}}^{0.518}$ \& $\stackrel{8 K}{8}$ \& ${ }_{18}^{18}$ \& ${ }_{\substack{\text { R156 } \\ \text { R157 }}}$ \& ${ }_{8 \mathrm{gK}}^{8 \mathrm{~K}}$ \& ${ }_{1 c}^{1 c}$ <br>
\hline \& \& \& (R152 \& ${ }_{8}^{8 \mathrm{BL}}$ \& - ${ }_{10}^{2 A}$ \& ${ }_{\substack{0.1718 \\ 0.171}}^{0.18}$ \& ${ }_{5 c}^{8}$ \& ${ }_{28}^{18}$ \& ${ }_{\substack{\text { R159 } \\ \text { R159 }}}^{\text {R189 }}$ \& ${ }_{81}^{8 K}$ \& ${ }_{1}^{1 c}$ <br>
\hline C1 \& ${ }_{4}^{1 \mathrm{k}}$ \& ${ }_{4 C}^{6 C}$ \& -1758 \& ${ }_{8}^{8 \mathrm{BK}}$ \& ${ }^{16}$ \& - 0181 \& ${ }_{3}^{3 \mathrm{~m}}$ \& ${ }_{10}^{10}$ \&  \& \% \& ${ }_{18}^{18}$ <br>
\hline cid \& ${ }_{2}^{4 K}$ \& ${ }_{80}^{4 C}$ \& C185 \& ${ }_{\text {3M }}^{\text {3C }}$ \& 38
18
18 \& 0182 \& 6 \& ${ }^{1 A}$ \& ${ }_{\substack{\text { R182 } \\ \text { R17 } \\ \text { R17 }}}$ \& ${ }_{58}^{88}$ \& ${ }_{38}^{18}$ <br>
\hline c11
$\mathrm{Cl20}$

0 \& ${ }_{4}^{2 J}$ \& ${ }_{40}^{60}$ \& | c186 |
| :---: |
| $\substack{188 \\ C 188}$ | \& $\underset{\substack{6 \mathrm{c} \\ 3 \mathrm{M}}}{ }$ \& ${ }_{18}^{1 / 8}$ \& ${ }_{\substack{\text { R12 } \\ \text { R13 }}}$ \& ${ }_{2 k}^{2 K}$ \& ${ }_{60}^{60}$ \& ${ }_{\substack{\text { R175 } \\ \text { R178 }}}^{\text {R18 }}$ \& ${ }_{66}^{58}$ \& ${ }_{3 C}^{38}$ <br>

\hline ${ }^{2} 21$ \& 5 \& ${ }_{40}^{46}$ \& C190 \& 7 \& ${ }^{2 \mathrm{C}}$ \& ${ }_{\text {R22 }}$ \& ${ }_{4}^{2 k}$ \& ${ }_{50}^{60}$ \& ${ }_{\substack{\text { R117 } \\ \text { R17 }}}$ \& ${ }_{6}^{6 C}$ \& ${ }_{2}{ }^{2 C}$ <br>

\hline | c101 |
| :--- |
| cio2 |
| 102 | \& ${ }_{38}^{18}$ \& ${ }_{4}^{6 A}$ \& C191 \& ${ }^{\text {s }}$ \& 16 \& ${ }_{\text {R101 }}^{\text {R23 }}$ \& ${ }_{18}^{4 K}$ \& ${ }_{78}^{4 C}$ \& R178

R179 \& ${ }_{6}^{6 E}$ \& ${ }_{30}^{20}$ <br>
\hline cios \& ${ }_{88}^{78}$ \& $\stackrel{3}{3 A}$ \& ${ }_{\text {CRR131 }}^{\text {CR151 }}$ \& ${ }_{88}^{6 \mathrm{H}}$ \& ${ }_{18}^{28}$ \& ${ }_{\substack{\text { Re102 } \\ \text { R103 }}}^{\text {R2, }}$ \& ${ }_{78}^{38}$ \& ${ }_{7}^{7}$ \& R1180
R181
R17 \& ${ }_{\text {\% }}$ \& ${ }_{18}^{28}$ <br>
\hline cilea \&  \& ${ }_{\substack{\text { 6B } \\ 58 \\ 58}}$ \& ${ }_{\text {chalis }}^{\text {CR17 }}$ \& ${ }_{5 B}^{8 H}$ \& ${ }_{38}^{18}$ \& R103 \& ${ }_{78}^{78}$ \& ${ }_{7}^{7 A}$ \& R181
R1185
Rise \& ¢ \& -180 <br>

\hline | c106 |
| :--- |
| $\substack{107 \\ \hline}$ | \& ${ }_{4 F}^{26}$ \& ${ }_{5 B}^{5 A}$ \& ${ }^{11}$ \& \& \& ¢ \& ${ }_{38}^{18}$ \& ${ }_{7 B}^{7 A}$ \& ${ }^{\text {R188 }}$ \& ${ }^{6}$ \& 1 A <br>

\hline $C 108$
$\substack{1111}$
$C 1$ \& ${ }_{18}^{56}$ \& ${ }_{10 \mathrm{C}}^{4 \mathrm{C}}$ \& (112 \& ${ }_{7}^{4 A}$ \& ${ }_{3 A}^{4 A}$ \& R107
R108
R10 \& ${ }_{88}^{78}$ \& ${ }_{78}^{78}$ \& $\mathrm{c}_{\substack{412 \\ \hline 122}}^{412}$ \& ${ }_{4}^{1 L}$ \& ${ }_{4 C}^{6 C}$ <br>
\hline C112
$\substack{112 \\ C 113}$ \& $\stackrel{\text { IF }}{19}$ \& ${ }_{5}^{68}$ \& ${ }^{\text {J14 }}$ \& ${ }_{8 A}{ }^{\text {A }}$ \& ${ }^{3 A}$ \& R108
R111
R11 \& ${ }_{1}^{88}$ \& ${ }_{68}^{78}$ \& ${ }_{\substack{4122 \\ 4177}}$ \& ${ }_{4}^{4 L}$ \& ${ }_{38}^{4 C}$ <br>
\hline C113 \& ${ }_{25}^{15}$ \& ${ }_{50}^{54}$ \& к100 \& \& \&  \&  \& ${ }_{5 \text { 5A }}$ \& ${ }_{\substack{4172 \\ \hline 173}}^{1}$ \& ${ }_{\text {SE }}^{50}$ \& ${ }_{30}^{3 B}$ <br>

\hline | C121 |
| :---: |
| $\substack{\text { c12 } \\ \mathrm{Cl2}}$ | \& ${ }_{48}^{38}$ \&  \& K100

K101
k102 \& ${ }_{19}$ \& ${ }_{5}^{6 A}$ \& R114
R121
R123
R12 \& ${ }_{3}{ }^{18}$ \& ${ }_{4}^{5 A}$ \& ${ }_{\substack{4174 \\ \hline 175 \\ \hline 175 \\ \hline 1 . \\ \hline}}$ \& ${ }_{\text {lem }}^{6 \mathrm{E}}$ \& ${ }_{3}^{3 C}$ <br>
\hline  \& ${ }_{4}^{4 F}$ \& ${ }_{4}^{4 A}$ \& K102
K103 \& ${ }_{1}^{16}$ \& ${ }_{68}^{58}$ \& ¢ \& ${ }_{3 F}^{3 G}$ \& ${ }_{3 A}^{4 A}$ \& U175 \& 4 E \& ${ }^{3}$ <br>
\hline (c124 \& ${ }_{4 B}^{43}$ \& ${ }_{10}^{40}$ \& K104
K105 \& ${ }_{2 \mathrm{~L}}^{2 \mathrm{~K}}$ \& ${ }_{60}^{50}$ \& ${ }_{\substack{\text { R131 } \\ \text { R132 }}}^{\text {R123 }}$ \& ${ }_{6}^{7 \mathrm{~A}}$ \& ${ }_{2}^{2 A}$ \& W11 \& ${ }_{38}^{18}$ \& ${ }_{58}^{6 A}$ <br>

\hline | C126 |
| :--- |
| $\substack{131 \\ C 131}$ | \& ${ }_{78}^{7 \%}$ \& +100 \& K110

K108
k10 \& ${ }_{4}^{4 F}$ \& ${ }_{4}^{48}$ \&  \&  \& ${ }_{2}^{2 A}$ \& $\underset{\substack{\text { w13 } \\ \text { w14 }}}{ }$ \& ${ }^{78}$ \& ${ }_{3}^{5 A}$ <br>
\hline  \& ${ }_{7}^{78}$ \& ${ }^{12}$ \& ${ }_{\text {K109 }} \mathbf{k} 109$ \& ${ }_{4 \mathrm{a}}^{4 \mathrm{G}}$ \& ${ }_{4 B}^{4 A}$ \&  \& ${ }_{7}^{7 H}$ \& ${ }_{28}^{2 A}$ \& ${ }_{\text {W }}^{\text {W }} 1400$ \& ${ }_{4 F}^{7 \mathrm{~A}}$ \& ${ }_{5 C}^{2 A}$ <br>
\hline  \& ${ }_{7}^{7}$ \& ${ }_{28}^{28}$ \& $\underset{\substack{\text { K110 } \\ \text { K111 }}}{ }$ \& ${ }_{5 L}^{4 H}$ \& ${ }_{40}^{58}$ \& $\substack{\text { R136 } \\ \text { R137 }}_{\text {R13 }}$ \& ${ }_{7}^{7 \%}$ \& -20 \& \& ${ }_{56}^{56}$ \& <br>

\hline | C137 |
| :--- |
| $\substack{138 \\ C 138}$ | \& ${ }_{7}^{7}$ \& ${ }_{2}^{2 \mathrm{C}}$ \& K112 \& ${ }_{5 K}$ \& ${ }_{40}$ \& | R139 |
| :---: |
| $\substack{\text { R1141 }}$ |
| R13 | \& ${ }_{7}^{7}$ \& 2 C

${ }_{28} \mathrm{C}$ \& \& \& <br>
\hline ${ }_{C 151}$ \& ${ }_{88}$ \& ${ }_{10}$ \& ${ }^{L 138}$ \& \& \& ${ }_{\substack{\text { R142 } \\ \text { R142 }}}$ \& 75 \& ${ }_{28}^{28}$ \& \& \& <br>
\hline C152 \& ${ }_{8}^{8 \mathrm{H}}$ \& ${ }_{18}^{2 A}$ \&  \& ${ }_{8}^{86}$ \& ${ }^{16}$ \& ${ }_{\substack{\text { R153 } \\ \text { R154 }}}$ \& ${ }_{8}^{84}$ \& ${ }^{1 A}$ \& \& \& <br>
\hline ${ }^{\text {C154 }}$ \& ${ }_{8}$ \& ${ }_{18}$ \& (1318 \& ${ }_{7 J}^{7 K}$ \& ${ }_{28}^{28}$ \& ${ }_{\substack{\text { R154 } \\ \text { R155 }}}$ \& ${ }_{8}^{8 H}$ \& ${ }_{1 B}^{1 A}$ \& \& \& <br>
\hline \multicolumn{12}{|l|}{Partial A10 also shown on diagrams $2,3,4,5,6,7$ and 13.} <br>
\hline
\end{tabular}




| ASSEmbly ag |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT | SCHEM location |  | CIRCUIT | SCHEM LOCATION | LOCATION | ( ${ }_{\text {CiRCUIT }}^{\text {NUMEER }}$ | SCHEM location | ${ }_{\text {L }}^{\text {LOCAADO }}$ | CIRCUIT NUMEER | SCHEM LOCATION | ${ }_{\text {coill }}^{\text {Board }}$ |
|  | $\begin{aligned} & 3 M \\ & \text { 3M } \\ & 3 M \\ & 3 M \end{aligned}$ | $\begin{gathered} 10 \\ 1 \mathrm{~A} \\ 18 \\ 18 \end{gathered}$ | Q182 <br> R185 <br> R188 <br> R189 | $\begin{aligned} & 6 L \\ & 3 L \\ & 6 L \\ & 3 \mathrm{M} \end{aligned}$ | $\begin{aligned} & 1 A \\ & 10 \\ & 1 A \\ & 1 A \end{aligned}$ | VR101 <br> w105 W106 | $\begin{aligned} & 3 \mathrm{M} \\ & \begin{array}{l} 6 \mathrm{~L} \\ 3 \mathrm{~N} \end{array} \end{aligned}$ | $\begin{aligned} & 18 \\ & 18 \\ & 18 \end{aligned}$ | $\begin{aligned} & \text { wion } \\ & \text { wob } \\ & \text { woter } \\ & \text { w } 222 \end{aligned}$ |  | 18 18 18 18 18 |
| assembly ato |  |  |  |  |  |  |  |  |  |  |  |
| Clircuit NUMBER | SCHEM LOCATION |  | CIRCUIT | SCHEM <br> LOCATION | $\stackrel{\text { BOARD }}{ }$ | CIRCUIT | SCHEM | BOARD | CIRCUIT | SCHEM LOCATION | BOARD |
|  |  |  |  |  |  |  |  | $2 B$ $2 B$ $2 B$ $1 B$ 18 $2 B$ $8 C$ $8 C$ $4 C$ $4 C$ $4 A$ $8 A$ $7 A$ $7 A$ $7 A$ $7 A$ $7 A$ $7 B$ $7 B$ $6 A$ $5 A$ $5 A$ $4 A$ $3 A$ $3 A$ $2 A$ $2 A$ $2 A$ $2 A$ $2 B$ $2 C$ $2 C$ $2 C$ $2 B$ $2 B$ |  |  |  |
| Partala A10 also shown on ollagrams 2, 3, 4, 5, 6, 7 a and 13. |  |  |  |  |  |  |  |  |  |  |  |



## WAVEFORMS FOR DIAGRAM 2



SET READOUT CONTROL CCW (OFF).


VERTICAL PREAMPS AND OUTPUT AMPLIFIER DIAGRAM 2 (SN B715000 AND ABOVE, 2246 iY
SN B120000 AND ABOVE, 2246 MOD A)




VERTICAL PREAMPS AND OUTPUT AMPLIFIER DIAGRAM 2 (SN B714999 AND BELOW, 2246 1Y
SN B119999 AND BELOW, 2246 MOD A)

| ASSEMbLY A10 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| clircuit NUMEER | SCHEM LOCATION |  | Clircuir NUMER | SCHEM LOCATION | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT NUMBER | SCHEM LOCATION | $\xrightarrow{\text { Boato }}$ | CIRCUIT NUMBER | SCHEM LOCATION | BOARD LOCATION |
|  |  |  | 0251 <br> 0252 <br>  <br> 0 <br> $\begin{array}{l}\text { O252 } \\ \text { O253 } \\ 0284 \\ 0284\end{array}$ <br> 200285 <br> 0281 <br> 070 <br> 007029703 <br> 0704 <br> 0.0 $\underset{\substack{\text { R115 } \\ \text { R125 } \\ \hline}}{ }$ ${ }_{\text {R138 }}^{\mathrm{R} 125}$ ${ }^{R 140}$ <br>  ${ }_{\text {R202 }}^{\text {R202 }}$ ${ }^{\text {R202 }}$ ${ }^{R}$ R2045 R206 R207 ${ }_{\text {R2010 }}^{\text {R209 }}$ ${ }^{\text {R2 } 211}$ ${ }_{\text {R213 }}^{213}$ ${ }_{\text {R215 }}^{\text {R214 }}$ ${ }_{\text {R218 }}^{\text {R218 }}$ ${ }^{2220}$ <br>  R223 $R 224$ $R 2$ $\underset{\substack{\text { R2225 } \\ \text { R22 }}}{ }$ R228 R229 R229 R230 R231 R232 R232 R234 R234 R235 R238 R240 R240 R241 R242 R243 R244 R248 $\underset{\substack{\text { R250 } \\ \text { R250 }}}{2020}$ |  |  |  |  |  | ${ }_{\substack{\text { R712 } \\ \text { R715 }}}$ <br> ${ }_{8}^{8716}$ <br> ${ }_{8718}^{8718}$ <br> ${ }_{8} 87720$ <br> R721 R722 <br> R724 R725 <br> R726 R727 <br> R727 R728 <br> R729 R730 <br> R730 R731 <br> R732 <br> RT201 <br> ${ }^{2} 201$ <br> 4203 A <br> U2038 <br> U203C <br> 1020 <br> U 2303 D <br> U 210 <br> U220 <br> U230 U240 <br> U260 U280 <br> U701 <br> U702 U801B <br> W19 W20 <br> W200 <br> W202 <br> W203 W205 <br> W206 W207 <br> W209 <br> W210 <br> W231 <br> W232 W415A <br> W415B <br> W416B <br> W815 |  |  |
|  | stom |  |  |  |  |  |  |  |  |  |  |

[^12]





A AND B TRIGGER SYSTEM DIAGRAM 3
(SN B710100 AND ABOVE, 2246 1Y
SN B110100 AND ABOVE, 2246 MOD A)

| ASSEN | LY A10 |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Clircuit | (SCHEM | ${ }_{\text {L }}^{\text {LoCation }}$ | CiRCUIT NUMBER | SCHEM LOCATION | ${ }_{\text {cose }}^{\text {Boapd }}$ | (circuit | SCHEM LOCATON | ${ }_{\text {L }}^{\text {BoCard }}$ | (c) | $\underset{\text { SCHEM }}{\text { SOCATON }}$ | ¢ Boamo |
| ${ }^{\text {cat }}$ | ${ }^{1+}$ | ${ }^{26}$ | 0470 | $5 \mathrm{5K}$ | ${ }^{1 H}$ | R463 | ${ }^{6 L}$ | ${ }^{1 F}$ | R1135 |  |  |
| ${ }^{\text {C422 }}$ | ${ }_{2}^{2 H}$ | ${ }^{3+}$ | 0474 | ${ }^{7}$ | ${ }^{1 F}$ | ${ }_{8470}$ | ${ }^{6}$ | ${ }_{16}$ | ${ }_{\text {R1136 }}^{\text {R13 }}$ | ${ }_{4}^{4 F}$ |  |
| C424 | ${ }_{2}^{2 H}$ | ${ }_{39}^{26}$ | ${ }_{0}^{0480}$ | ${ }_{5}^{8 \mathrm{E}}$ | ${ }_{5}^{17}$ | ${ }_{\substack{\text { R4472 }}}^{\text {R471 }}$ | ${ }_{5}^{5 K}$ | $\underset{1}{11_{1}}$ |  | ${ }_{46}^{36}$ | ${ }_{4}^{46}$ |
| ${ }^{\text {C425 }}$ | ${ }_{20}^{2 J}$ | ${ }^{29}$ | 01102 | ${ }_{4}^{4 E}$ | ${ }_{5}^{51}$ | ${ }_{8473}$ | ${ }_{6} 6$ | $1{ }^{1}$ | R1144 | ${ }^{3+}$ | $4{ }_{4}$ |
|  | ${ }_{12}^{20}$ | ${ }^{3 \mathrm{3G}}$ |  | ${ }_{\text {SE }}^{\text {SF }}$ | ${ }_{5}^{51}$ | R4774 R475 | ${ }_{8 K}^{6 K}$ |  |  | $\stackrel{3}{23}$ | ${ }_{3 G}^{4 H}$ |
| ${ }^{\text {ca44 }}$ | ${ }^{21}$ | ${ }^{2 F}$ | 01105 | ${ }^{3 E}$ | ${ }^{5}$ | R476 | ${ }_{8 L}$ | 1 F | ${ }_{\text {R11 }}$ R1154 | ${ }_{6}$ | ${ }_{49}$ |
|  | ${ }_{66}^{3 L}$ | ${ }_{16}^{2 F}$ | 01106 | ${ }^{3}$ | 5 | ${ }_{\substack{\text { R4477 } \\ \text { R47 }}}^{\text {dit }}$ | ${ }_{7}^{8 L}$ | $\stackrel{1}{1 /}$ | ¢1155 | ${ }_{50}^{65}$ | ${ }_{4}^{4 \mathrm{a}}$ |
| C452 <br> $\substack{\text { C453 }}$ | ${ }_{8}^{76}$ | ${ }^{2 H}$ | ${ }_{\text {R400 }}$ | ${ }^{20}$ | ${ }_{3 F}^{3 F}$ | ${ }_{8479}$ | $\pi$ | ${ }^{\text {IF }}$ | ${ }_{\text {R11 }}$ | ${ }_{5}^{5 A}$ | ${ }_{5} 5$ |
| C-453 | ${ }_{76}^{86}$ | ${ }^{16}$ | (R402 | ${ }_{31}^{20}$ | $3_{3}^{3 F}$ |  | ${ }_{8 k}^{38}$ |  | ${ }^{\text {R1170 }}$ | 3A | 4 |
| C435 | ${ }_{6 L}^{6 H}$ | ${ }_{16}^{19}$ | R4410 | ${ }_{1}^{19}$ | ${ }_{3 \mathrm{l}}^{3 \mathrm{G}}$ | Resme | ${ }_{88}^{8 L}$ | ${ }^{1 / 5}$ | ${ }^{\mathrm{U421} 1 \mathrm{~A}}$ | 10 | ${ }_{3}^{3 F}$ |
| C463 $\substack{\text { C474 } \\ \text { C4id }}$ | ${ }_{\text {6L }}^{6 \text { 6L }}$ | ${ }_{\text {IF }}^{1 / 8}$ | ${ }_{\text {R412 }}$ | 2 M | ${ }_{3 G}$ | ${ }_{\text {Ras7 }}^{\text {R4ab }}$ | ${ }_{8}$ | ${ }_{1}^{11}$ | ${ }_{\text {U4210 }}$ | ${ }_{1}^{1 K}$ | ${ }_{3}^{3 F}$ |
| C476 | ${ }_{7}$ | ${ }_{1}{ }_{1}$ | ${ }_{\text {R414 }}^{\text {R413 }}$ | ${ }_{6 M}^{2 M}$ | ${ }_{3}^{3 \mathrm{Ha}}$ | ¢ | ${ }_{48}^{38}$ | ${ }_{3}^{2 J}$ |  | $\stackrel{70}{60}$ | ${ }_{2 F}^{2 F}$ |
| ${ }_{\text {C477 }}^{\text {C478 }}$ | ${ }_{8 \times}^{74}$ | 1 F | R415 | ${ }_{6}^{6 L}$ | ${ }^{26}$ | ${ }^{\text {R4922 }}$ | ${ }_{48}$ | 3 | 4431 C | 6 | ${ }_{2}$ |
| ${ }_{\text {Ca83 }}$ | ${ }_{38}$ | ${ }_{2}{ }_{2}$ | ${ }_{\text {R417 }}^{\text {R446 }}$ | ${ }_{6 M}^{6 L}$ | ${ }_{2}^{2 \mathrm{H}}$ | ${ }_{\substack{\text { Ra93 } \\ \text { R994 }}}^{\text {Red }}$ | ${ }_{78}^{48}$ | ${ }_{2}^{2 J}$ | U441A | ${ }_{\text {ck }}^{4 \mathrm{4K}}$ | $\underset{1}{1 H}$ |
| C488 | ${ }_{88}^{48}$ | ${ }_{2}^{3 G}$ | ${ }_{\substack{\text { Rata } \\ \text { R422 }}}$ | ${ }^{2 \mathrm{LE}}$ | ${ }^{26}$ | ${ }_{\text {R4995 }}$ | ${ }_{78}$ | ${ }_{2}$ | U4410 | ${ }_{7}$ | ${ }_{1}^{1+1}$ |
| Ci487 | ${ }_{38}^{88}$ | ${ }_{3 \mathrm{SF}}^{3 \mathrm{~F}}$ | ${ }_{\substack{\text { R4422 } \\ \text { R422 }}}^{18}$ | ${ }_{2}^{1 H}$ | ${ }_{26}^{2 G}$ |  | ${ }_{88}^{88}$ | ${ }_{25}^{2 J}$ | U4410 | ${ }_{2}^{4 \mathrm{~K}}$ | $\underset{1}{1+1}$ |
| C-488* | ${ }_{48}^{88}$ | ${ }_{36}^{2 G}$ |  | ${ }_{2}^{2 H}$ | ${ }_{36}^{36}$ | (R498 | ${ }_{30}^{38}$ | ${ }_{36}^{31}$ | U441F | ${ }^{1 \mathrm{k}}$ | $\stackrel{1 H}{1+}$ |
| ${ }^{\text {C498* }}$ | ${ }_{88}$ | 29 | ${ }_{\text {R425 }}$ | $2{ }_{2}$ | ${ }_{26}$ | R1103 | ${ }_{30}$ | ${ }_{46}^{36}$ | U442A |  |  |
| ${ }^{\text {C1103 }}$ | ${ }^{3 A}$ | ${ }_{5 k}^{43}$ | ${ }^{\text {Ra426 }}$ | ${ }_{2 k}^{20}$ | ${ }^{2 H}$ | ${ }_{\text {R1105 }}$ | ${ }^{8}$ | ${ }_{5}^{5 \mathrm{5K}}$ | U4asc | 1 M | ${ }^{2 H}$ |
| ${ }^{\text {C11106 }}$ | ${ }_{3}$ | ${ }_{46}$ | ${ }_{\text {R431 }}$ | ${ }_{1 K}^{2 k}$ | ${ }_{2 F}^{26}$ | R.108 R1110 R110 | ${ }_{5}^{85}$ | ${ }_{4}^{5 K}$ |  | ${ }_{6 G}^{2 E}$ | ${ }_{46}^{46}$ |
| C1107 C1110 C110 | ${ }_{30}^{3 H}$ | ${ }_{36}^{49}$ |  | ${ }_{11}^{2 L}$ | ${ }_{29}^{2 G}$ | ${ }_{\substack{\text { R1111 } \\ \text { R1112 } \\ \text { R112 }}}$ | ${ }_{4}^{4 E}$ | ${ }_{\text {sk }}^{5 \times}$ | ${ }_{4}^{4111028}$ | ${ }_{3}^{36}$ | ${ }^{49}$ |
| C1111 C1114 C11 | ${ }_{\substack{3 H \\ 5 E}}$ | ${ }_{4}^{49}$ | R4441 | , | ${ }_{2}^{2 H}$ | ${ }_{\substack{\text { R1112 } \\ \text { R111 } \\ \text { R114 }}}$ | ${ }_{4 \mathrm{E}}^{4}$ | ${ }_{5}^{5 K}$ | ${ }_{4}$ | ${ }_{58}^{58}$ | ${ }_{5}$ |
| ${ }^{\text {c1130 }}$ | ${ }_{4 \mathrm{SG}}$ | ${ }_{46}$ | ${ }_{\text {R443 }}$ | ${ }_{1}$ | ${ }_{2}^{2 H}$ | R1114 R11114 R111 | ${ }_{4 F}^{4 F}$ | ${ }_{4}^{5 K}$ | ¢ | $4{ }_{4}$ | ${ }_{4}^{4 H}$ |
| coile | ${ }_{6 \mathrm{CF}}^{4 \mathrm{~F}}$ | ${ }_{46}^{4 H}$ | Re444 | ${ }_{21}^{2 K}$ | ${ }_{2 \times}^{3 H}$ | ${ }_{\substack{\text { R1116 } \\ \text { R1117 } \\ \text { R117 }}}$ | ${ }_{\substack{5 \%}}^{5 \times}$ | ${ }_{3}^{4}$ | ${ }^{4} 11104 \mathrm{C}$ | ${ }^{68}$ | ${ }^{4 H}$ |
| C1155 | 6 6 | 49 | ${ }_{\substack{\text { Ra46 } \\ \text { R44 }}}$ | ${ }_{31}^{2 M}$ | ${ }_{\substack{3 F \\ 2 F}}^{25}$ | R11188 R1120 R12 |  | ${ }_{5}^{4}$ | Ul11006 | 81 81 80 | SL 5 51 |
| ${ }_{\text {CRA432 }}$ | ${ }_{6 K}^{1 K}$ | ${ }_{16}^{26}$ |  | 3 c <br> 3 l <br> 2M | ${ }_{3}^{2 F}$ | R11120 $\substack{\text { R1122 } \\ \text { R112 }}$ | ${ }_{\text {cke }}^{3 \mathrm{se}}$ | ¢ ${ }_{\text {5k }}$ | ${ }^{\text {U1106C }}$ | ${ }^{4}$ |  |
|  |  |  | ${ }_{\text {R }}^{\text {R } 450}$ | ${ }_{6 E}^{20}$ | ${ }^{16}$ | ${ }_{\text {R1123 }}^{\text {R12 }}$ | ${ }_{4 \mathrm{E}}^{3 \mathrm{E}}$ | ${ }_{5}^{5 K}$ | $\mathrm{w}_{\mathrm{W} 400}$ | ${ }_{3}^{20}$ |  |
| ${ }^{\text {OLL2 }}$ | ${ }_{69}^{6 M}$ | ${ }_{2 H}^{2 K}$ | ${ }_{\text {Ren }}^{\text {R45 }}$ | ${ }_{76}^{76}$ | ${ }_{16}^{16}$ | ¢ | ${ }_{3 F}^{4 F}$ | ${ }_{50}^{56}$ | ${ }_{\text {W4at }}^{\text {W414 }}$ | 19 2 20 | ${ }_{3}^{31}$ |
| 1426 |  |  | ${ }_{\substack{\text { Ratas } \\ \text { R454 }}}^{\text {asi }}$ | ${ }_{7}^{88}$ | ${ }_{26}^{26}$ | ${ }_{\substack{\text { R1126 } \\ 81127}}$ | ${ }_{3 F}$ | 5 | W1101 | 500 | ${ }_{5} 5$ |
| ¢ | $\underset{\substack{20 \\ 60}}{ }$ | 3H 16 | (Ras4 |  |  |  | ${ }_{\substack{4 F \\ 3 F \\ 39}}$ | 3 |  | 30 60 80 | ${ }_{5}^{51}$ |
|  |  |  |  | ${ }_{6 K}^{6 F}$ | ${ }_{16}^{19}$ | R1131 R1132 R132 | ${ }_{3}^{3 G}$ | ${ }_{46}^{46}$ | W1108 |  |  |
| O444 | ${ }_{21}$ | ${ }_{2 F}^{2 H}$ | ${ }_{\text {R462 }}^{\text {R461 }}$ | ${ }_{6}^{6 \mathrm{c}} \mathrm{6L}$ | ${ }_{16}^{16}$ | ¢, | $\stackrel{3 H}{3}$ | ${ }_{4}^{4 H}$ |  |  |  |

*See Parts List for
serial number range



A AND B TRIGGER SYSTEM DIAGRAM 3 (SN B710099 AND BELOW, 2246 1Y
SN B110099 AND BELOW, 2246 MOD A

| ASSEN | LY A10 |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Clircuit <br> NUMER | SCHEM LCCATON | $\xrightarrow{\text { LoCARD }}$ | Clircura | SCHEM | ${ }_{\text {coser }}$ | Clircuit | SCHEM <br> LOCATON | ${ }_{\text {cose }}^{\text {BOARD }}$ |  | SCHEM | - Board |
| ${ }^{4221}$ | ${ }_{1}$ | 4 4 | 01103 |  |  |  |  |  |  |  |  |
| ${ }^{\text {c422 }}$ | ${ }_{2}^{2 H}$ | ${ }_{4}^{4 \mathrm{H}}$ | ${ }^{01104}$ | ${ }_{3 E}{ }^{\text {SF }}$ | $\stackrel{1 N}{2 M}$ | ${ }_{\substack{\text { R4744 } \\ \text { R477 }}}^{\text {a }}$ | ${ }_{8 \times}^{6 K}$ | ${ }_{1}^{1 \mathrm{H}}$ | ${ }_{\substack{\text { R1150 } \\ \text { R1154 }}}$ | ${ }_{6 F}^{20}$ | $\mathrm{in}_{1 \times}^{1 M}$ |
|  | ${ }_{2}^{2 H}$ | ${ }_{4 G}^{4 G}$ | 011105 0.1106 0.108 | ${ }_{3 F}^{3 F}$ | ${ }_{2}^{2 N}$ |  | ${ }_{81}^{8 M}$ | ${ }_{2}^{2 H}$ | ${ }_{\substack{\text { R1155 } \\ \text { R11162 }}}$ | ${ }_{5}^{6 \mathrm{c}}$ | ${ }_{1 \times}$ |
| catas <br> $\substack{\text { c422 } \\ \hline}$ | $\stackrel{2}{24}$ | ${ }_{36}^{46}$ |  |  |  | R4779 R479 | ${ }_{7}$ | ${ }_{1}^{2 H}$ | R1162 <br> R11163 <br> 148 | ${ }_{5}^{5 A}$ | ${ }_{2 \times}{ }_{2}^{1 K}$ |
| C432 | ${ }_{1}^{2 \mathrm{~L}}$ | ${ }_{3}^{2 H}$ | R401 | ${ }_{10}^{20}$ | ${ }_{3 G}^{3 G}$ | 良4833 | ${ }_{88}^{38}$ | ${ }_{18}^{16}$ | R1170 | ${ }^{3 A}$ |  |
| ${ }_{\text {ca44 }}$ | 2 | ${ }_{36}$ | ${ }_{\text {R410 }}$ | ${ }_{1}$ | ${ }_{3}$ | ${ }_{\text {R485 }}$ | ${ }_{8}^{8 L}$ | ${ }_{1}$ | U421A | 10 | 36 |
| C447 | - ${ }_{\text {3H }}^{64}$ | ${ }_{29}^{36}$ | ${ }^{\text {R4411 }}$ | ${ }^{19}$ | ${ }_{4}^{4 F}$ | ${ }_{\text {R486 }}$ | ${ }^{8 L}$ | ${ }^{15}$ | U4218 | ${ }^{16}$ | ${ }_{3}{ }^{6}$ |
| cent | ${ }_{7}^{6}$ | ${ }_{2}^{26}$ | ${ }_{\text {R413 }}^{\text {R412 }}$ | 2 m 2 M | ${ }_{4 G}^{4 F}$ |  | ${ }_{38}^{8 L}$ | $\stackrel{1 F}{1 /}$ |  | ${ }_{70} 19$ | ${ }_{26}^{36}$ |
| ${ }^{\text {cas3 }}$ | ${ }_{8}^{8 H}$ | ${ }^{26}$ | R414 | ${ }_{\text {¢ }}$ | $2{ }^{2}$ | R4991 | ${ }_{4 B}$ | 3 | U4318 | ${ }_{66}$ | ${ }_{29}^{20}$ |
| C454 | ${ }_{81}^{7 H}$ | ${ }_{26}^{26}$ | ${ }_{\text {R415 }}^{\text {R415 }}$ | ${ }_{69}^{6 M}$ | ${ }_{25}^{2 G}$ | ${ }^{\text {R4992 }}$ | ${ }^{48}$ | 3 | U4316 | ${ }_{6}{ }^{\text {m }}$ | ${ }^{2 \mathrm{a}}$ |
| cis2 | ${ }_{6}^{6}$ | ${ }_{1}^{20}$ | ${ }_{\text {R417 }}^{\text {R416 }}$ | ${ }_{6 \times 9}$ | ${ }_{2 F}^{2 F}$ | ${ }_{\text {Re94 }}^{\text {R493 }}$ | ${ }_{78}^{48}$ | ${ }_{3}^{3}$ | U4441A | ¢ ${ }_{5}^{4 \mathrm{4K}}$ | ${ }_{2}^{2 H}$ |
| - | ${ }_{6}^{6 L}$ | $1{ }^{1+}$ | ${ }_{\text {R420 }}^{\text {Rat }}$ | ${ }_{2}{ }_{2}$ | ${ }_{4}^{2 F}$ | ${ }_{\text {R495 }}$ | 78 | ${ }_{2}$ | U4416 | ${ }_{7}$ | ${ }_{2}^{2 H}$ |
|  | ${ }_{8 \text { 8M }}^{6 L}$ |  |  | ${ }_{2}^{1 \mathrm{H}}$ | ${ }_{4}^{4 \mathrm{H}}$ | ${ }_{\substack{\text { Ra49 } \\ \text { R497 }}}^{\text {as }}$ | ${ }_{88}^{88}$ |  |  | ${ }^{4 K}$ | ${ }_{2}^{2 H}$ |
| ${ }^{\text {cas3 }}$ | ${ }^{38}$ | 1 F | R423 | ${ }_{2}{ }^{2}$ | 49 | R4988 | ${ }_{38}$ | 3 | U441F | ${ }_{1}^{2 k}$ | ${ }_{2}^{2 H}$ |
| Ca84 | ${ }_{88}^{48}$ | ${ }_{25}^{3 F}$ | ${ }_{\substack{\text { Ra24 } \\ \text { Ras2 }}}^{\text {and }}$ | ${ }_{2}^{2 H}$ | ${ }_{4}^{4 \mathrm{G}}$ | ${ }_{\substack{\text { R607 } \\ \text { R1103 }}}$ | ${ }^{2 N}$ | ${ }^{3}$ | $\pm 1101 \mathrm{~A}$ | ${ }^{2 \mathrm{E}}$ | ${ }^{2 M}$ |
| casi | ${ }_{88}^{88}$ | ${ }_{2}^{2 F}$ | ${ }_{\text {R426 }}^{\text {R423 }}$ | ${ }_{2}^{2 \mathrm{E}}$ | $\stackrel{4}{4}$ | RR1104 | ${ }_{3 \mathrm{E}}^{30}$ | 2 M 2 | $\underset{4}{411028}$ | - ${ }_{36}{ }^{66}$ | ${ }_{12}^{2 M}$ |
|  |  |  | 品4330 | ${ }_{1 K}^{2 K}$ | ${ }_{3}^{3 H}$ | ${ }_{\text {RR1110 }}^{\text {R1111 }}$ |  | iN | ${ }^{4111028}$ | ${ }^{3+}$ | ${ }_{2}^{14}$ |
| ${ }_{\text {case }}$ | ${ }_{48}^{48}$ | ${ }^{3 F}$ | ${ }_{\text {R432 }}$ | 21 | ${ }_{3}$ | ${ }^{211112}$ | ${ }_{4}{ }^{\text {E }}$ | in | $\pm 1104 \mathrm{~A}$ | ${ }_{3}^{58}$ | ${ }_{2} 2 \mathrm{M}$ |
|  | ${ }_{38}^{2 N}$ | 3 | Re40 | $1{ }_{1}$ | ${ }_{3}^{3 H}$ | R.1113 | ${ }_{45}^{4 F}$ | iN | ${ }^{4} \mathrm{U} 11048$ | 4 4 | ${ }^{2 \mathrm{M}}$ |
| ${ }^{C 1105}$ | ${ }_{3 E}$ | ${ }_{2}$ | ${ }_{\text {R442 }}$ | 1 | ${ }_{3}$ | ${ }_{\text {R11115 }}$ |  | ${ }_{\text {in }}$ | U1104C | ${ }_{28}^{48}$ |  |
|  | ${ }_{3}^{3+}$ | ${ }_{21}^{21}$ | R4433 | ${ }_{21}^{12}$ | ${ }_{2}^{2 H}$ | ${ }^{\text {R11116 }}$ | ${ }_{5} 5$ | in | ${ }^{4} \mathrm{H} 11068{ }^{\text {a }}$ | ${ }_{8}^{28}$ | ${ }_{2}^{2 J}$ |
| ${ }^{11110}$ | ${ }_{3 E}$ | ${ }^{2 \times}$ | ${ }_{\text {R445 }}^{\text {R444 }}$ | ${ }_{22}^{2 L}$ | ${ }_{3}^{2 H}$ | ${ }_{\substack{\text { Rel117 } \\ \text { R.118 }}}^{\text {R11 }}$ | ${ }_{5 F}^{4 F}$ | in | U1106C |  |  |
| ${ }^{C 1111}$ | ${ }^{3 \mathrm{H}}$ | 11 | ${ }^{\text {R446 }}$ | 2 M | ${ }^{26}$ | R1120 | ${ }^{3}$ | ${ }^{2 N}$ | w401 | ${ }^{2 E}$ |  |
| C1114 C1130 C11 | ${ }_{4 G}^{5 E}$ | ${ }_{21}^{1 N}$ | ${ }_{\text {R448 }}^{\text {R447 }}$ | ${ }_{3}^{3 M}$ | ${ }_{3 H}^{3 H}$ |  | ${ }_{3 \mathrm{~s}}^{4 \mathrm{E}}$ | ${ }_{2}^{2 \mathrm{~N}}$ | W403 | $\stackrel{0}{6}$ | 1 i |
| C1154 $\substack{1155}$ $C$ | ${ }_{66}^{68}$ | 19 20 | ${ }_{\substack{\text { R4499 } \\ \text { R450 }}}$ | ${ }^{2 \times 1}$ | ${ }^{3+}$ |  | ${ }_{4}^{4 \mathrm{~F}}$ | ${ }_{2}^{2 N}$ | $\mathrm{W}^{4} 405$ | ${ }_{8}^{86}$ | is |
|  |  |  | ${ }_{\text {Rasi }}^{\text {Ras }}$ | ${ }_{7}^{6 E}$ | ${ }_{2}^{2 \mathrm{H}}$ |  | ${ }_{3 F}^{4 F}$ | ${ }_{2}^{2 N}$ | ${ }_{\text {W400 }}^{\text {W407 }}$ | $3_{3}^{3}$ | ${ }_{2,}^{2 H}$ |
| CRA32 | ${ }_{6 K}^{14}$ | ${ }_{1}^{3 H}$ | ${ }_{\text {R453 }}^{\text {R452 }}$ | ${ }_{8 H}^{7 \%}$ | $\stackrel{2 \mathrm{ar}}{2 \mathrm{G}}$ | R1126 R1127 R127 | ${ }_{45}^{3 F}$ | 2 N 2 N | W408 W410 W4iol | 3 | 131 |
|  |  |  |  |  | ${ }_{2}^{2 \mathrm{~F}}$ | R1127 R1128 R11131 R113 | ${ }_{3}^{4 F}$ | ${ }_{2}^{2 N}$ | W410 w4411 w412 |  | ${ }_{2}^{23}$ |
| Li432 | ${ }_{2}^{26}$ | ${ }_{3}^{4 H}$ |  | ${ }^{66}$ | $\stackrel{2}{2 H}$ | R1131 R1132 R122 | ${ }_{3}^{3 \mathrm{ab}}$ | ${ }_{2 L}^{12}$ | W412 ${ }_{\text {W4 }}$ | ${ }_{1}^{2 \mathrm{LM}}$ | ${ }_{3}^{1 J}$ |
| $\stackrel{462}{ }$ |  | $1{ }^{1}$ |  | ${ }_{6 K}^{6 K}$ | ${ }_{1+1}^{2 H}$ |  | ${ }_{3}^{3 H}$ | ${ }_{1 \times}^{2 L}$ |  | ¢ ${ }_{50}^{2 M}$ | 3 |
| O9440 | ${ }_{21}^{1 L}$ | ${ }_{36}^{3 H}$ | ${ }_{\text {R463 }}^{\text {R462 }}$ | ${ }_{6}^{6 L}$ | $\xrightarrow[1+14]{1+}$ | R1135 R1136 R13 | ${ }_{46}^{4 G}$ | ${ }_{10}^{2 M}$ | W1102 | ${ }_{5 \text { 3 }}$ | ${ }_{1}^{2 K}$ |
| O470 | ${ }_{7}^{5 L}$ | $\underset{16}{11_{16}}$ | ${ }_{\text {R4771 }}^{\text {R470 }}$ | $\underset{\substack{\text { 6L } \\ 5 \\ \hline 1}}{ }$ | $\stackrel{2 H}{1 H}$ | (R1422 |  | $1{ }_{1}^{12}$ | w1109 w1106 W12 | $\stackrel{4}{4}$ | $\xrightarrow{1 \mathrm{k}}$ |
| 0980 0.101 0.0 | ${ }_{\substack{8 L \\ 56}}$ | 1/19 | ${ }_{\substack{\text { R4472 } \\ \text { R473 }}}^{\text {R47 }}$ | ${ }_{\substack{5 L \\ 6 K}}$ | $2 H$ $2 H$ |  | $\underset{3}{31}$ | $1 / 2$ 1 1 | W106 $\begin{aligned} & \text { w1107 } \\ & \text { w1120 }\end{aligned}$ | $\underset{\substack{8 \\ 4{ }_{6} \\ \hline \\ \hline}}{ }$ | 2K $2 \times$ $2 \times$ |
| 01102 | ${ }_{4 F}^{5 E}$ | iN |  |  |  |  |  |  |  |  |  |




WAVEFORMS FOR DIAGRAM 4


DISPLAY AND TIRGGER LOGIC AND PROCESSOR INTERFACE DIAGRAM 4 (SN B710100 AND ABOVE, 2246 1Y
SN B110100 AND ABOVE, 2246 MOD A)

| Assembly A10 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (circuit | SCHEM LOCATION | $\stackrel{\text { LOCARO }}{\text { LOCATON }}$ | CIRCUIT NUMBER | $\begin{gathered} \text { SCHEM } \\ \text { LOCATON } \end{gathered}$ | BOARD LOCATION | CIRCUIT NUMBER | SCHEM LOCATION | $\begin{array}{\|c} \text { BOARD } \\ \text { LOCATION } \end{array}$ | CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{array}{\|l\|l\|} \hline \text { BOAARO } \\ \text { LOCATION } \end{array}$ |
| C600 <br> C 601 C 602 <br> C603 <br> C 607 C 608 <br> C611 <br> C614 <br> C636* <br> CR603 <br> Q600 <br> 0601 <br> 0602 <br> Q604 <br> Q605 Q606 <br> Q607 Q608 <br> R501 <br> R502 <br> R602 R604 R605 R606 R609 R610 |  |  | R611 R612 <br> R612 R613 <br> R614 <br> R615 <br> R616 R617 <br> R618 <br> $R 619$ $R 620$ <br> R620 $R 621$ <br> R622 R623 <br> R624 <br> R625 <br> R626 R627 <br> R628 <br> R630 R631 <br> R636 R637 <br> $R 637$ $R 638$ <br> R639 R640 <br> R641 <br> $R 642$ $R 643$ <br> R644 R645 <br> R646 <br> R648 |  | $3 K$ $3 M$ $2 K$ $3 L$ $3 L$ $3 K$ $3 M$ $3 M$ $3 M$ $4 M$ $3 K$ $3 K$ $2 L$ $2 L$ $4 L$ $3 L$ $2 K$ $2 K$ $3 L$ $3 N$ $3 N$ $3 N$ $3 L$ $3 L$ $4 M$ $3 M$ $3 M$ $3 M$ $3 M$ $3 M$ $3 K$ $3 K$ | R650 R651 R652 R653 R654 R655 R656 R657 R658 R659 R662 R663 R662 R663 R664 R665 R666 R669 R670 R671 R671 R672 R673 R673 R674 R686 R686 R687 R688 R689 R690 R691 $R 692$ $R 693$ $R 694$ |  | $3 M$ $4 M$ $4 M$ $3 K$ $3 L$ $3 L$ $4 K$ $4 K$ $4 K$ $4 M$ $2 M$ $3 K$ $3 K$ $4 M$ $3 K$ $3 K$ $3 M$ $3 M$ $3 M$ $4 M$ $4 M$ $3 L$ $3 L$ $2 K$ $2 L$ $2 L$ $2 L$ $2 L$ $2 L$ $2 L$ $5 M$ $4 N$ $5 N$ | U600 <br> U601 U602 U603A U603B U603D U604B U604C U606B U606D U606E W502 W504 W505 W507 W603 W604 W605 W610 W611 W612* W2502 W2502 Y 800 |  | $2 M$ 1 N 4 K $2 K$ $2 K$ $2 K$ $2 K$ $2 L$ $2 L$ $2 L$ $3 M$ $3 M$ $3 M$ $3 M$ $3 M$ $3 M$ $3 M$ $3 M$ $3 M$ $3 N$ $3 N$ $5 M$ $4 M$ $4 L$ $6 L$ $3 N$ $3 N$ $8 N$ $1 L$ $1 L$ $3 L$ 3 |
| Partial AI | shown o | , grams 1, | 6,7, |  |  |  |  |  |  |  |  |

See Parts List for
erial number range

RAM 4


## Parts List tor al number ranges.



DISPLAY AND TIRGGER LOGIC AND PROCESSOR INTERFACE DIAGRAM 4 (SN B710099 AND BELOW, 2246 1Y






A AND B SWEEPS AND DELAY COMPARATORS DIAGRAM 5

| ASSEmbLY A10 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| circuit <br> NUMEER | SCHEM | Location | CIRCUIT | SCHEM <br> LOCATION | - ${ }_{\text {BOARD }}$ | CIRCUIT | SCHEM LOCATION | $\stackrel{\text { board }}{\text { Location }}$ | CIRCUIT NUMBER | SCHEM LOCATION | $\xrightarrow{\text { BOARD }}$ |
|  |  |  |  |  |  |  |  |  | R382 <br> R383 R384 <br> R385 <br> R386R387 R388R391* R393R394 R395R396 U301AU301C U302 U304A U304BU307 U308 U309AU309B U310 U313U315A U315BU315C U316A U316C VR301 VR302VR303 VR304VR308 VR308VR309 VR310VR311 VR312$W_{\text {w }} \mathbf{w} 304$ |  |  |
| Partial A10 also shown on dlagrams 1. 2. 3. 4. . 6.7 and 13. |  |  |  |  |  |  |  |  |  |  |  |

*See Parts List for
serial number ranges



WAVEFORMS FOR DIAGRAM 6







HORIZONTAL OUTPUT AMPLIFIER DIAGRAM 6 SNN B710100 AND ABOVE, 2246 IY

| ASSEMBLY A10 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT NUMBER | SCHEM LOCATION | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { CIRCUIT } \\ & \text { NUMBER } \end{aligned}$ | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { Boatio } \\ & \text { LOCATION } \end{aligned}$ |
| $C 319$$C 802$$c 802$ <br> $C 803$ <br> 803c804 C805C807 C808C809 C814C817c817 6819c819$c 820$ <br> $c 880$ <br> 880 C870C880 <br> CR801* <br> CR802* ${ }_{P}^{P 17}$ 08010802 08020803 |  |  |  |  |  |  |  |  | R892 R893 R894 R896 R898 U301B U802 VR801 W17 W305 W805 W807 W808 W811 W821* |  |  |
| Parial A10 also shown on diagrams 1, 2, 3, , 5, 7 and 13. |  |  |  |  |  |  |  |  |  |  |  |

*See Parts List for
serial number ranges



HORIZONTAL OUTPUT AMPLIFIER DIAGRAM 6
(SN B710099 AND BELOW, 2246 1Y

| ASSEmbLY A10 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT NUMBER | LCCATON | ${ }_{\text {L }}^{\text {Boatio }}$ | CiRcurf NUMBER | SCHEM LOCATION | $\xrightarrow{\text { board }}$ Location | circuir <br> NUMER | ${ }_{\text {cose }}^{\substack{\text { SCHEM } \\ \text { LOCATON }}}$ | $\underset{\text { LOCATON }}{\text { Lit }}$ | Cliccuit | SCHEM LOCATION | $\stackrel{\text { Boato }}{\text { LOCATON }}$ |
|  |  |  |  |  |  | R816 | ${ }^{7}$ | a | ${ }^{\text {R85 }}$ | 侕 |  |
| ${ }_{6802}$ | ${ }_{3}^{68}$ | 7 7 | ${ }_{0}^{0807}$ | ${ }_{5}^{5 H}$ | ${ }_{8}^{8}$ | ${ }^{\text {R819 }}$ | ${ }_{7}^{5 L}$ | ${ }_{8}^{8 K}$ | Ress | $\stackrel{6}{5 \times}$ | ${ }_{84}^{8}$ |
| ci803 | ${ }_{3 G}^{3}$ | ${ }_{8}^{75}$ | (0808 | ${ }_{5}^{5 H}$ | ${ }_{8}$ | ${ }_{\text {R822 }}$ | $\pi$ | 7 K | R855 | ${ }_{4}^{4}$ | ${ }_{7}^{7 H}$ |
| ${ }^{\text {c805 }}$ | ${ }^{4 \mathrm{4K}}$ | 8 | ${ }^{0810}$ | ${ }^{3}$ | 7H | ${ }_{\substack{\text { R8823 }}}^{\text {R822 }}$ | \% | ${ }_{86}^{7 K}$ | ${ }_{\text {R }}^{\text {R856 }}$ | ${ }_{4}^{4 \mathrm{E}}$ | ${ }_{8}^{8 H}$ |
|  | ${ }_{58}^{2 H}$ | ${ }_{96}$ | ${ }^{\text {R353 }}$ | ${ }^{68}$ | 8 8 | ${ }_{\text {R882 }}$ | 70 | ${ }_{8}^{8+}$ | R858 | зк | 7 |
| ${ }^{\text {c889 }}$ | $\stackrel{5 K}{5}$ | ${ }_{81}^{8 K}$ | $\substack{\text { R338 } \\ \text { R369 }}^{\text {R3, }}$ | ${ }_{68}^{68}$ | ${ }_{80}^{80}$ | $\substack{\text { R8827 } \\ \text { R882 }}_{\text {Re3 }}$ | ${ }_{4 C}$ | ${ }_{8 \mathrm{H}}^{8 \mathrm{H}}$ | U3018 |  | 80 |
|  | ${ }_{7}{ }^{51}$ | ${ }_{8}^{8}$ |  | ${ }_{2}^{68}$ | ${ }_{7}$ |  | ¢ | ${ }_{8}^{7 \mathrm{7k}}$ | U881A | ${ }_{30}^{7 H^{2}}$ | ${ }_{88}^{7 \mathrm{7H}}$ |
| C8817 | ${ }_{7}^{80}$ | ${ }_{7,}^{66}$ | (r802 | ${ }_{2 j}^{3 L}$ | ${ }_{7}^{7}$ | ${ }_{\substack{\text { R8839 } \\ \text { R83 }}}^{\text {R27 }}$ | ${ }_{40}^{6 K}$ | ${ }_{8}^{8 \mathrm{BK}}$ |  |  |  |
| C819 |  |  | ${ }_{\text {R804 }}$ | ${ }_{2 J}$ | 7 | R880 | 2 K | 7 | VR801 | ${ }^{4 \mathrm{~K}}$ | 8 |
| св801 |  | ${ }^{9+}$ | ${ }_{\text {Rros }}^{\text {R809 }}$ | ${ }_{4}^{24}$ | 75 | ${ }_{\text {R8481 }}$ | ${ }_{3}^{4 \mathrm{~F}}$ | ${ }_{7 J}$ | V8802 | ${ }^{36}$ | * |
| CR802 | ${ }_{66}^{3 L}$ | ${ }_{8}^{7 \mathrm{~K}}$ |  | $\stackrel{5}{5 B}_{2 H}$ | ${ }_{96}^{7 J}$ |  | ${ }_{26}$ | 7 K |  | зм |  |
|  |  |  | ${ }_{\text {R808 }}$ | ${ }_{40}^{5 B}$ | ${ }_{7}{ }^{96}$ |  | ${ }_{2}^{26}$ | ${ }_{7}^{7}$ | W18 | ${ }_{68}^{5 M}$ | ${ }_{\substack{86 \\ 100}}$ |
| ${ }_{\text {P18 }}^{\text {P17 }}$ | ${ }_{5 M}^{3 M}$ | ${ }_{8 k}^{7 k}$ | Reos | ${ }_{5 \text { c }}^{4 \mathrm{E}}$ | ${ }_{8}^{8 H}$ | ${ }_{\substack{\text { R8845 } \\ \text { R84 }}}^{\text {Re4 }}$ | ${ }_{4 K}$ | 75 | W805 | ${ }_{58}^{68}$ | ${ }_{6} 6$ |
|  |  |  | R811 | 70 | ${ }^{\text {7 }}$ | ${ }^{\text {R847 }}$ | ${ }_{5}^{56}$ | ${ }_{8}^{8}$ | ${ }_{\text {W80\% }}$ | 5c | ${ }_{6}^{66}$ |
| -0801 | ${ }_{3 K}^{2 K}$ | ${ }_{7 J}^{73}$ | ${ }_{\substack{\text { R812 } \\ \text { R813 }}}$ | ${ }_{6}^{70}$ | ${ }_{8 \mathrm{8K}}^{8 \mathrm{H}}$ | ${ }_{\substack{\text { R8848 } \\ \text { R849 }}}^{\text {Red }}$ | ${ }_{5}^{5 H}$ | ${ }_{8 K}^{8 K}$ | W80\% | ${ }_{5 C}^{58}$ | ${ }_{6} 8$ |
| (0802 | ${ }_{3}^{3 \mathrm{3}}$ | ${ }_{7 J}^{75}$ | R813 | $\stackrel{66}{6}$ | ${ }_{8}^{8 \times}$ |  |  |  | w810 | ${ }_{48}^{48}$ | ${ }_{5}^{56}$ |
| (0804 | ${ }_{5 k}^{36}$ | 73 | R815 | ผ |  |  |  |  |  |  |  |




(*) $\begin{aligned} & \text { Static Sensitive Devices } \\ & \text { See Maintenance Section }\end{aligned}$
Component number example

| Component Number |  |
| :---: | :---: |
|  |  |
|  | $\underset{\substack{\text { Subassembly }}}{\substack{\text { Schematic } \\ \text { Circuit } \\ \text { Number (if used) }}}$ |


| A8-CRT CONTROL BOARD |  |  |  |
| :---: | :---: | :---: | :---: |
| CIRCUIT | SCHEM NUMBER | $\begin{aligned} & \text { cliRCUIT } \\ & \text { NUMBR } \end{aligned}$ | SCHEM NUMBER |
| $\begin{aligned} & \text { R901 } \\ & \text { R902 } \\ & \text { R903 } \\ & \text { R9905 } \end{aligned}$ | 7 7 7 | w900 | ${ }_{13}^{7}$ |



B)
ov
2dn
$\qquad$

R905
(6081 3277062.09





| ASSEmbly ab |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{array}{\|l\|l\|} \hline \text { CIRCUIT } \\ \text { NUMBER } \end{array}$ | SCHEM LOCATON |  |  | SCHEM LOCATON | ¢ | (circuit | $\xrightarrow{\text { SCHEM }}$ | $\xrightarrow{\text { board }}$ Location | circuir <br> NUMBEH |  |  |
| R2001 | 5A | 1A | п902 | 6 6 | 10 | R903 | 6 6 | $1 F$ | R Ros | ${ }^{\wedge}$ | "' |
| Parial Alo also shown on diagram 13. |  |  |  |  |  |  |  |  |  |  |  |
| ASSEmbly alo |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  | con |  |
| Partial A10 also shown on diagrams 1, 2, 3, 4, 5, 6 and 13 . |  |  |  |  |  |  |  |  |  |  |  |
| OTHER PARTS |  |  |  |  |  |  |  |  |  |  |  |
| ${ }^{16}$ | ${ }^{7 A}$ | CHassis | ${ }^{\text {P26 }}$ | 11 | CHAss | $1: 1$ | $"$ | 14.444 | $\checkmark$ | 14 |  |



Z-AXIS, CRT, PROBE ADJUST AND CONTROL MUX DIAGRAM 7 SN B119999 AND BELOW, 2246 MOD A)



|  |  |
| :---: | :---: |
|  |  |
|  |  |
|  |  |
| ${ }_{\substack{\text { Schen }}}^{\text {Schem }}$ | ${ }_{\text {cosem }}^{\text {Boano }}$ |
|  |  |
|  |  |
|  |  |
| ${ }_{\text {Schen }}^{\text {SCOATON }}$ | ${ }_{\text {cosafo }}^{\text {Loatoo }}$ |
| 1 M | Crassis |





| A16-PROCESSOR BOARD |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Clircuit NUMEER | ${ }_{\text {SCHEM }}$ | ${ }_{\text {ctircuit }}$ | SCHEM NUMEER | ${ }_{\text {ctircuit }}$ | ${ }_{\text {SCHEM }}$ | $\xrightarrow{\text { clicuit }}$ NUMEER | SCHEM |  | $\xrightarrow{\text { SCHEM }}$ | Clicult | SCHEM |
| BT2501 | ${ }^{8}$ | C2514 | 8 | 02506 | 10 | R2363 | 11 | R2540 | 10 | U2408 | 14 |
| вт2502 | 8 | ${ }_{\text {C22515 }}^{\text {C2516 }}$ | ${ }_{8}^{8}$ | Q2507 |  | ${ }_{\substack{\text { R2364 } \\ \text { R2365 }}}$ | 11 | ${ }_{\substack{\text { R2542 } \\ \text { R254 }}}^{\text {20, }}$ | 10 10 10 | U2409 | 9 14 |
| C2300 | 11 | ${ }^{\text {c2517 }}$ | ${ }_{8}^{8}$ | R2301 | 11 | ${ }_{\text {R2200 }}$ | ${ }_{9}$ | ${ }_{\text {R2547 }}$ | 10 | U2410 | ${ }_{9}$ |
| ${ }^{\text {c2301 }}$ | 11 | C2518 | 8 | ${ }_{\text {R2302 }}$ | 11 | R2401 | 9 | R2548 | 10 | U2410 | 14 |
| ${ }^{\text {c2302 }}$ | 11 | C2521 | ${ }^{10}$ | ${ }^{\text {R2303 }}$ | 11 | ${ }^{\text {R2402 }}$ | 9 | ${ }^{\text {R2549 }}$ | 10 | U2411 | 9 |
| ${ }_{\text {cer }}^{\substack{\text { c2303 } \\ C}}$ | 11 11 11 |  | 10 10 | ${ }_{\text {R2305 }}^{\text {R2304 }}$ | 111 | ${ }_{\text {R2404 }}^{\text {R2405 }}$ | 9 | ${ }_{\text {Ren }}^{\text {R2550 }}$ | 10 10 | U2411 | 14 9 |
| Cerem | ${ }_{11}^{11}$ | ${ }^{\text {ce2523 }}$ | ${ }_{10}$ | R2305 R2306 | 11 | R2245 R2406 | 9 | ${ }_{\text {R2552 }}$ | 10 | U2412 | 14 |
| ${ }^{\text {c2306 }}$ | 11 | C225 | 10 | R2307 | 11 | R2407 | 9 | R2553 | 10 | U2413 | 9 |
| ${ }^{\text {c2307 }}$ | 11 | ${ }^{\text {c2256 }}$ | 10 | R2308 | 11 | R2408 | 9 | ${ }^{\text {R2554 }}$ | 8 | U2413 | 14 |
| C2308 | 11 | ${ }^{\text {C2530 }}$ | 14 14 | ${ }_{\text {Ren }}^{\text {R2339 }}$ | 14 11 11 | ${ }_{\text {R2409 }}^{\text {R2410 }}$ | 9 | ${ }_{\text {R2560 }}^{\text {R255 }}$ | ${ }_{8}^{8}$ | U244 | ${ }_{14}^{9}$ |
| C2310 | 11 | ${ }^{\text {c2532 }}$ | ${ }_{14}^{14}$ | ${ }^{\text {R2311 }}$ | 11 | ${ }_{\text {R2411 }}$ | 9 | ${ }_{\text {R2561 }}$ | 8 | U2415 | 9 |
| ${ }_{\substack{\text { cen } \\ \text { C2312 } \\ \text { c2312 }}}$ | 14 14 | ${ }_{\text {Ce2s41 }}^{\text {C2543 }}$ | 14 10 | ${ }_{\text {R }}^{\text {R2313 }}$ | ${ }_{11}^{11}$ | ${ }_{\text {R }}^{\substack{\text { R2412 } \\ \text { R2413 }}}$ | ${ }_{9}^{14}$ | ${ }_{\text {R2563 }}^{\text {R252 }}$ | ${ }_{8}^{8}$ | U2415 | 14 |
| ${ }^{2} 2313$ | 14 | C2544 | 10 | ${ }_{\text {R2314 }}$ | 11 | ${ }_{\text {R2414 }}$ | 9 | R2564 | ${ }_{8}^{8}$ | ${ }^{2} 2416$ | 14 |
| C2314 | 14 | C2545 | 10 | R2315 | 11 | R2415 | 9 |  |  | U2417 | 9 |
| ${ }^{\text {c2315 }}$ | 14 | ${ }^{\text {c2546 }}$ | 10 | ${ }^{\text {R2316 }}$ | 11 | ${ }_{\text {R2416 }}$ | 9 | ${ }^{2} 2300$ | 11 | U2417 | 14 |
| C | 14 14 14 |  | 10 10 | R2317 <br> R 2318 <br> 2318 | 11 11 11 | ${ }_{\substack{\text { R2417 } \\ \text { R2418 }}}$ | ${ }_{9}^{9}$ | $\mathrm{y}_{\substack{2330 \\ \text { U2301 }}}$ | 14 11 11 | U2501 | 14 |
| ${ }^{\text {C2318 }}$ | 11 | C2549 | 10 | R2319 <br> R2320 <br> 18 | 11 | ${ }^{\text {R2449 }}$ | 9 | 42301 <br> 42302 | ${ }_{11}^{14}$ | U2502 | ${ }^{8}$ |
| c2319 c2320 230 | ${ }_{11}^{14}$ | C2350 | ${ }_{8}^{10}$ | ${ }_{\substack{\text { R2320 } \\ \text { R2321 }}}^{\text {R23 }}$ | ${ }_{11}^{11}$ | ${ }_{\substack{\text { R2420 } \\ \text { R2421 }}}$ | ${ }_{9}^{9}$ | U2302 | 14 | U2502 | 8 |
| ${ }_{\text {c } 2321}$ | 14 | ${ }^{\text {c2552 }}$ | 8 | $\stackrel{\text { R2322 }}{ }$ | 11 | ${ }_{\text {R2501 }}$ | 8 | U2303 | 11 | U2503 | 14 |
| ${ }^{\text {c }} 23222$ | 11 | ${ }^{\text {c2553 }}$ | 8 | ${ }^{\text {R2324 }}$ | 11 | ${ }^{\text {R2502 }}$ | 8 | $\pm$ | 14 | ${ }^{22506}$ | $\stackrel{8}{14}$ |
| C ${ }^{2323}$ | 11 | C2555 | 8 | R2325 R2328 | 11 | ${ }^{\text {R2203 }}$ | 8 | U2304 | 14 | ${ }^{4} 25512$ | ${ }_{8}^{8}$ |
| ${ }^{\text {c2401 }}$ | 14 |  |  | R2329 | 11 | R2505 | 8 | U2305 | 11 | U2512 | 14 |
|  | 14 14 14 | CR2501 | ${ }_{8}^{8}$ | ${ }_{\text {R2330 }}^{\text {R2331 }}$ | 11 | ${ }_{\text {R2506 }}$ | ${ }_{8}^{8}$ |  | 14 11 | U2513 <br> U2513 <br> 12512 | ${ }_{14}^{8}$ |
| ( ${ }_{\text {c204 }}$ | 14 | cicher | 8 | ${ }_{\text {R2332 }}$ | 11 | ${ }_{\text {R2508 }}$ | 8 | 42306 | 14 | U2514 | ${ }_{8}$ |
| ${ }^{\text {C2005 }}$ | 14 | CR2505 | 8 | R2333 | 11 | R2509 | 8 | ${ }^{42308}$ | 14 | U2514 | 14 |
| (e2406 | 14 14 | DS2501 | 8 | R2334 R2335 | 11 | ${ }_{\text {R }}$ | 8 | ${ }^{\text {U2309 }}$ | 14 11 | ${ }^{2} 25515$ | ${ }_{14}^{8}$ |
| C2408 c209 c20 | 14 14 14 | -1232 |  | R2336 R237 | 11 | ${ }_{\text {R }}^{\text {R2512 }}$ | ${ }_{8}^{8}$ | U2399 <br> 42313 <br> 1. | ${ }_{11}^{14}$ | 42517 <br> 12517 | 8 14 14 |
| ( 22409 | 14 14 | - | 11 | ${ }_{\substack{\text { R2337 } \\ \text { R2338 }}}^{\text {233 }}$ | 11 11 11 | ${ }_{\text {R }}^{\text {R2513 }}$ | 8 | ( ${ }_{\text {U2313 }}$ | 114 | ${ }_{\substack{0517 \\ \text { U2518 }}}$ | - |
| ${ }^{\text {C2441 }}$ | 14 | J2302 | 14 | ${ }_{\text {R2339 }}$ | 11 | R2515 | 8 | U2314 | 11 | U2518 | 14 |
| C 2412 <br> $C 2415$ <br> 245 | 9 | J2304 | 11 11 | R2340 | 11 | ${ }_{\text {R2516 }}$ | ${ }_{8}^{8}$ | U2314 | 14 <br> 1 | U2519 | ${ }_{14}^{8}$ |
| C2416 | 9 | J2501 | 14 | R2334 R234 | 11 | R2518 <br> R 2518 <br> 2885 | 8 | $\checkmark 2400$ | 14 | ${ }^{4} 25521$ | ${ }^{14}$ |
| ${ }^{\text {C2417 }}$ | 9 | J2502 <br>  <br>  <br> 2502 | 8 | R2343 R234 | ${ }_{11}^{11}$ | R2519 R2520 | 8 | U2401 | ${ }_{14} 9$ | ${ }_{\substack{42523 \\ \hline 12523}}$ | 10 14 14 |
|  | 9 | J2502 | ${ }^{14}$ | R2334 R2345 | ${ }_{11}^{11}$ | R2520 R2521 | ${ }_{8}^{8}$ | U2401 | ${ }_{9}^{14}$ |  | 14 10 |
| ( $\begin{aligned} & \text { c2420 } \\ & \text { c2501 }\end{aligned}$ | $\stackrel{9}{14}$ | - 22503 | 10 | ${ }_{\substack{\text { R2346 } \\ \text { R2347 }}}^{\text {Ren }}$ | ${ }_{11}^{11}$ | ${ }_{\text {R2523 }}^{\text {R252 }}$ | ${ }_{8}^{8}$ | U2402 | +14 | U2524 U2525 | 14 |
| C2502 | 14 14 14 | J2601 | 14 | R2347 R2349 R34, | 11 | R2523 R2525 R2525 | ${ }_{10}^{8}$ | cick | 14 | U2525 | 14 |
| - | 14 14 | P2105 |  | R2349 R2350 | 11 | ${ }_{\text {R }}$ | ${ }_{8}^{8}$ | U2404 | 14 | W2105 | 11 |
| C2505 C2506 | 14 14 14 | P2105 | 14 | R2351 R2352 | ${ }_{11}^{11}$ | ${ }_{\substack{\text { R2527 } \\ \text { R2528 }}}$ | 10 | ¢ 42405 | 14 | W2105 | 14 |
| (2206 | 14 14 | Q2501 | 10 | R2352 R2355 | ${ }_{11}^{11}$ | R2528 R 2529 | 10 10 | U2406 | ${ }_{9}^{14}$ | Y2501 | 8 |
| C2508 | 14 14 14 | - ${ }_{\text {O2502 }}^{\text {O2503 }}$ | 10 10 | R2356 R235 | ${ }_{11}^{11}$ | ${ }_{\text {R }}^{\substack{\text { R2532 } \\ \text { R254 }}}$ | 10 | ( 22406 | ${ }^{14}$ |  |  |
| c25010 c2511 | 14 14 14 |  | 10 10 | R2361 R2362 R23 | 11 11 | R2536 R2538 R23 | 10 10 | (20407 | 14 |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |

MEASUREMENT PROCESSOR DIAGRAM 8

| ASSEmbiy A16 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Crincur | Schem | $\underset{\substack{\text { Boano } \\ \text { Loation }}}{ }$ | cincur | SCHEM | ${ }_{\text {Logafo }}^{\text {Loation }}$ | cincur | Schem | Looaro | Clacur | $\xrightarrow{\text { SCOEAM }}$ | ${ }_{\text {coind }}^{\text {Boano }}$ |
| ${ }^{\text {Brizon }}$ |  | 70 |  |  |  | ${ }^{\text {R2516 }}$ |  | ${ }^{\circ}$ | U2501 | ${ }^{1}$ |  |
|  | ${ }_{66}^{64}$ | ${ }_{80}^{70}$ | ${ }_{\substack{\text { j2502 } \\ \text { J2502 }}}$ | ${ }^{18}$ | $3_{3}^{3}$ | ${ }_{\substack{\text { R2517 } \\ \text { R2518 }}}^{\text {a }}$ | $\underbrace{88}_{8}$ | $\underbrace{6 \times}_{\substack{6 \times \\ 86}}$ |  |  |  |
| ${ }^{\text {c2514 }}$ | ${ }_{68}^{68}$ | ${ }^{64}$ | ${ }^{22507}$ | ${ }^{7}$ | ${ }^{\text {\％}}$ |  | ¢ | $\stackrel{\text { sk }}{\substack{\text { ak }}}$ | 边 | ${ }_{\substack{20 \\ 88 \\ 80}}^{20}$ | $\stackrel{4}{4+}$ |
|  | ${ }_{\text {cis }}^{68}$ |  | R2501 | 20 | ${ }^{46}$ | ${ }_{\substack{\text { R2521 } \\ \text { R2522 }}}^{\text {R2，}}$ | ${ }_{\substack{\text { SE } \\ 3 \mathrm{H} \\ \hline \\ \hline}}$ | ${ }_{\text {an }}$ |  | ${ }_{70}^{170}$ |  |
|  | ${ }_{8}^{8,8}$ | $\underbrace{\substack{\text { ¢ }}}_{\substack{\text { ¢K }}}$ | （incien | ${ }_{7}$ | ${ }_{\text {cke }}^{5 \text { ck }}$ | ${ }_{\substack{\text { R } \\ \text { R2523 } \\ \text { R25 }}}$ | ${ }_{88}^{7 A}$ | ${ }_{6 \times}^{6 \times}$ | U2008 | ${ }_{78}^{70}$ | $\begin{gathered} 5 \mathrm{sk} \\ \substack{\text { sk } \\ \hline} \end{gathered}$ |
|  | ${ }_{4}^{4 \times 1}$ |  |  | ${ }^{80}$ |  | $\underbrace{208}_{\substack{\text { R2228 } \\ \text { R25 }}}$ | ${ }_{70}^{70}$ | ${ }_{\text {cos }}^{68}$ | U2050 | ${ }_{3}^{26}$ | $\begin{gathered} \frac{5 K}{4 E} \\ \hline ⿷ 匚 ⿳ 丨 コ 丨 又 巾 \end{gathered}$ |
| （eise | $\underset{\substack{\text { anc } \\ \text { sm }}}{\substack{\text { cm }}}$ | $\underset{\substack{4 \times \\ 4 \times \\ 4 \times}}{\text { and }}$ |  |  | ${ }_{\substack{\text { ¢\％H．} \\ 30}}$ |  | ${ }_{\substack{58 \\ 26}}$ | ${ }_{\text {che }}^{7 \times}$ | U2313 | ${ }_{\substack{36 \\ 26}}$ | ${ }_{5}{ }_{5}^{4}$ |
|  |  |  |  | ${ }^{28}$ |  | $\underbrace{\text { Res }}_{\substack{\text { R2355 } \\ \text { R250 }}}$ | ${ }_{\text {13 }}^{18}$ |  | U235 | ${ }_{\text {L }}$ | $\underset{5}{\substack{4 H}}$ |
|  |  | $\underset{\substack { 7 \mathrm{k} \\ \begin{subarray}{c}{\text { KK }{ 7 \mathrm { k } \\ \begin{subarray} { c } { \text { KK } } }\end{subarray}}{ }$ | （inction | ¢ |  |  |  | ${ }_{4}^{4 \mathrm{~F}}$ | ciel | ${ }_{6}^{56}$ | $\stackrel{4 K}{*}$ |
|  | ${ }_{7}^{8 E}$ | $\underset{\substack{\text { ¢K，} \\ \text { \％K }}}{ }$ | （inctin | ${ }^{15}$ |  |  | $\substack{\begin{subarray}{c} { 3 M \\ \begin{subarray}{c}{\text { m }{ 3 M \\ \begin{subarray} { c } { \text { m } } } \\{5} \end{subarray}} \\{\hline} \end{subarray}$ |  | U521 | ${ }_{\text {\％}}$ | \％ |
| DS2501 | ${ }_{85}$ | ${ }_{7 k}$ | ${ }_{\substack{\text { R2514 } \\ \text { R2515 }}}^{\substack{\text { and }}}$ | ${ }_{\substack{30 \\ 80}}$ |  | $\underbrace{\text { Res }}_{\substack{\text { R2564 } \\ \text { R265 }}}$ | $\substack{5 M \\ 68}_{50}$ | ${ }_{66}^{4 \mathrm{aK}}$ | V2501 | ${ }_{68}$ | вн |
| Pamaialit alsos stom on digagams，10，11， 14. |  |  |  |  |  |  |  |  |  |  |  |





READOUT SYSTEM DIAGRAM 9

| ASSEmbLY A16 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (CIRCUIT | SCHEM LOCATON | ${ }_{\text {L }}^{\text {Boation }}$ | Clircuit NUMBER | ${ }_{\text {SCOMEM }}^{\text {SCHEM }}$ |  | Clircuit NUMBER | SCHEM LOCATON | $\underset{\text { LOCATON }}{\text { Boad }}$ | cincuit | SCHEM LOCATON | $\underset{\substack{\text { Boato } \\ \text { LOCATON }}}{ }$ |
| C2411 |  |  |  |  |  |  |  |  |  |  |  |
| ${ }^{\mathrm{C} 2412}$ | ${ }_{4}^{4}$ | ${ }_{5}^{56}$ | ${ }_{\substack{\text { R2200 }}}^{\text {R2209 }}$ | ${ }_{4}^{4 G}$ | 50 | ${ }_{\text {R2421 }}^{\text {R240 }}$ | ${ }_{1 A}^{2 M}$ | ${ }_{2 B}^{2 A}$ | - | ${ }_{7}^{2 E}$ |  |
|  | ${ }_{2}^{2 \mathrm{j}}$ | ${ }_{18}^{18}$ | ${ }_{\substack{\text { R206 } \\ \text { R2007 }}}$ | $\stackrel{\substack{5 K \\ 5 K}}{ }$ | ${ }_{6 E}^{60}$ | U2400 | 20 | 3 C | (entid | ${ }_{3}^{4}$ | 50 50 |
| ce248 | ${ }^{21}$ | ${ }_{18}^{18}$ | ${ }_{\substack{\text { R22088 } \\ \text { R2409 }}}$ | ${ }_{3}^{3 k}$ | ${ }_{60}^{60}$ | ${ }^{12401}$ | ${ }_{88}^{48}$ | ${ }_{5}^{55}$ | U2414 | ${ }^{4 L}$ | ${ }_{60}^{60}$ |
| cersis | ${ }_{2 k}^{2 J}$ | ${ }_{18}^{18}$ | (R2409 | ( ${ }_{\text {3K }}$ | ${ }_{76}^{60}$ | ${ }_{\text {U }}$ | ${ }_{16}^{68}$ | ${ }_{10}^{5 F}$ |  | ${ }_{5}^{3 L}$ | 700 |
|  |  |  | ${ }_{\text {R2411 }}$ | ${ }_{3}{ }^{\text {m }}$ | 7 c | U2404 | ${ }_{5 E}^{56}$ | ${ }_{4 B}$ | ${ }_{\text {U24168 }}$ | ${ }_{3}$ | 70 |
| (J2302 | ${ }_{1 /}^{3 M}$ | ${ }_{3}^{88}$ | $\underbrace{\text { R2213 }}_{\text {R2214 }}$ | ¢ ${ }_{\text {6u }}$ | ${ }_{\substack{5 E \\ 5 E}}$ | (22005 | ${ }_{56}^{6 F}$ | - ${ }_{48}^{3 B}$ |  | 3M 5 | 70 70 |
| J2502 | ${ }_{19}$ | $3_{3}$ | ${ }_{\substack{\text { R22414 } \\ \text { R2415 }}}^{\text {R2418 }}$ | ${ }_{4}^{6}$ | ${ }_{50}^{50}$ | ${ }_{\text {U2406 }}$ | ${ }_{4 F}^{56}$ | ${ }_{5 \mathrm{SE}}^{4 \mathrm{C}}$ |  | ${ }^{518}$ | $1{ }_{1}$ |
|  |  |  | ${ }_{22416}$ | 4 | 50 | U24078 | ${ }^{3 F}$ | ${ }^{5 E}$ | ${ }^{1241788}$ | 1 c | 1A |
|  | 28 2 M | ${ }_{2}^{2 A}$ | ${ }_{\text {R2418 }}^{\text {R2417 }}$ | 4 M 4 | ${ }_{7}^{7 c}$ | ( | ${ }_{18}^{46}$ | ${ }_{38}^{5 C}$ | ${ }_{\text {U }}^{\substack{\text { U2417c }}}$ | ${ }_{38}^{28}$ | ${ }_{1}^{1 / A}$ |
| (R2020 | ${ }_{2 F}^{2 m}$ | ${ }_{2 B}^{28}$ | ${ }_{\text {R2419 }}$ | ${ }_{2 M}$ | ${ }_{2 C}$ | U24098 | 10 | ${ }_{38}$ |  |  |  |




(*) $\begin{gathered}\text { Static Sensitive Devices } \\ \text { See Minterance Secton }\end{gathered}$


| A14-SWITCH BOARD |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT number | SCHEM | CIRCUIT NUMBER | SCHEM | CIRCUIT NUMBER | SCHEM NUMBER | CIRCUIT | SCHEM |
| C2001 | 14 | DS2023 | 10 |  |  | ${ }_{5} 5230$ | ${ }^{10}$ |
| CR2001 | 10 | - ${ }_{\text {DS3225 }}$ | 10 10 | ${ }_{\text {R2001 }}^{\text {R202 }}$ | 10 10 | ${ }_{\text {S }}^{\text {s2031 }}$ | 10 10 |
| ${ }^{\text {CR20202 }}$ | 10 | DS22027 | 10 |  |  | S2034 | 10 |
|  | 10 10 | DS2028 OS2029 | 10 10 | S2001 | 10 10 | S2035 | 10 10 |
| ${ }_{\text {CR2000 }}$ | 10 | ${ }^{\text {ds20230 }}$ | 10 | S2003 | 10 | ¢ ${ }_{\text {S2037 }}$ | 10 |
| CR2006 | 10 | ${ }^{\text {DSS2031 }}$ | ${ }_{10}^{10}$ | S2004 | 10 | - | 10 |
| DS2001 |  | (0s2032 | 10 10 | S2005 | 10 10 | S2039 $\mathbf{S 2 0 4 0}$ | 10 10 |
| DS2002 | 10 | DS2034 | 10 | S2007 | 10 | s2041 | 10 |
| (tss2033 | 10 10 | (is2035 | 10 10 | S2008 | 10 10 | S2042 S2043 | 10 10 |
| DS2005 | 10 | DS2037 | 10 | s2010 | 10 | S2045 | 10 |
| ${ }^{\text {DSS2006 }}$ | 10 | DS2038 | 10 | S2011 | 10 | S2046 | 10 |
| DSS2007 <br> DS208 | 10 10 | DS2393 DS2041 | 10 10 | \$2013 | 10 10 | S22047 | 10 10 |
| ${ }^{\text {DS2009 }}$ | 10 | DS2042 | 10 | S2015 | 10 |  |  |
| ${ }^{\text {DSS2010 }}$ | 10 | DS2043 | 10 | S2016 | 10 | ${ }^{2} 2001$ | 10 |
| DSS2011 DS2012 | 10 10 | SS2044 | 10 10 | S2017 S2018 | 10 10 | ${ }_{\text {U }}$ | 14 |
| DS2013 | 10 | Ds2046 | 10 | S2019 | 10 | U2002 | 14 |
| DSS2014 DS2015 | 10 10 | 边 $\begin{aligned} & \text { DS2047 } \\ & \text { DS2048 }\end{aligned}$ | 10 10 | S2020 | 10 10 |  |  |
| DS2020 | 10 |  |  | S2022 | 10 | W2501 | 14 |
| (tand $\begin{aligned} & \text { DSS221 } \\ & \text { DS2022 }\end{aligned}$ | 10 10 | ${ }^{\text {P2501 }}$ | 14 | S2024 | 10 |  |  |

$$
32036 \quad \text { DS2044 }
$$

$$
32057 \text { DS2045 }
$$

$$
12038 \text { DS2046 }
$$

$$
32039 \text { DS2047 }{ }^{8}
$$

$$
32047 \quad 52048
$$

| A14-SWITCH BOARD |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT | SCHEM | CIRCUTT | SCHEM NUMBER | CIRCUIT nUMBER | SCHEM | $\xrightarrow{\substack{\text { CiRCUIT } \\ \text { NUMEER }}}$ | $\begin{gathered} \text { SCHEM } \\ \text { NUMBER } \end{gathered}$ |
| c2001 | 14 | DS2023 | 10 |  |  | S2030 | 10 |
|  |  | DS2025 | 10 | ${ }_{\text {R2001 }}^{\text {R2002 }}$ | 10 10 | ${ }_{\substack{\text { S2033 }}}^{\text {s2033 }}$ | 10 10 |
| CR2001 <br> CR2002 | 10 10 | DS2026 DS2027 | ${ }_{10}^{10}$ |  |  | s2034 | 10 |
| ${ }_{\text {crene }}^{\text {CR203 }}$ | 10 | DS2028 DS2029 | 10 10 | ${ }_{\text {S }}^{\text {s2000 }}$ | 10 10 | S | 10 |
| ${ }^{\text {CR22004 }}$ | 10 10 | 号52029 | ${ }_{10}^{10}$ | S2002 | 10 | S2037 | 10 |
| CR2006 | 10 | DS2031 | 10 | S2004 | 10 | S2038 | 10 |
|  |  | DS2032 | 10 | 52005 | 10 | S2039 | 10 |
| DS2001 DS2002 | 10 10 |  | ${ }_{10}^{10}$ | S2006 | ${ }_{10}^{10}$ | S2041 | 10 |
| DS22003 | 10 | DS2035 | 10 | 52008 | 10 | S2042 | 10 |
| Ds2004 | 10 | ${ }^{\text {DS } 2036}$ | 10 | 52009 | 10 | S2043 | 10 |
| ${ }^{\text {DS } 22005}$ | 10 | ${ }^{\text {DS2037 }}$ | 10 | S2010 S2011 | 10 10 | S | 10 10 |
| (is2006 | 10 10 | ${ }^{\text {DSS2039 }}$ | 10 | s2013 | 10 | S2047 | 10 |
| DS2008 | 10 | ${ }_{\text {DS2041 }}$ | 10 | S2014 | 10 | S2048 | 10 |
| DS22099 OS2010 | 10 10 |  | 10 10 | s. | ${ }_{10}^{10}$ | U2001 | 10 |
| OS2011 | 10 | DS2044 | 10 | s2017 | 10 | U2001 | 14 |
| DS2012 | 10 | DS2045 | 10 | ${ }^{\text {s2018 }}$ | 10 | ${ }^{4} 220202$ | 10 14 |
| DS2013 DS2014 | 10 10 | - | 10 10 | ( ${ }_{\text {s2019 }}^{\text {s2020 }}$ | 10 10 | 2002 |  |
| DS2015 DS2020 | 10 | DS2048 | 10 | S2021 | 10 10 | W2501 | 10 14 |
| DS5220 DS2022 | 10 | P2501 | 10 | ¢ | 10 |  |  |
| DS2022 | 10 | P2501 | 14 | S2028 |  |  |  |



SWITCH BOARD AND INTERFACE DIAGRAM 10

| ASSEMBLY A14 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUT NUMBER | SCHEM LOCATION | $\xrightarrow{\text { Boabd }}$ Location |  | SCHEM LOCATON | $\begin{array}{\|l\|l\|l\|l\|l\|l\|} \hline \text { LOCAATN } \\ \text { LOCAT } \end{array}$ | CIRCUT | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT NUMBER | ¢ SCHEM | $\xrightarrow{\text { BOAAD }}$ |
|  |  | $3 F$ $2 F$ $4 E$ $3 D$ 30 $3 A$ $3 A$ $2 A$ $2 B$ $2 B$ $2 B$ 30 $3 D$ $3 D$ $3 D$ $4 B$ $4 B$ $4 B$ $4 B$ $3 B$ $4 C$ $4 C$ $4 C$ $2 C$ $2 C$ $2 D$ $2 E$ |  |  |  |  |  |  |  |  | $\begin{aligned} & 3 C \\ & 20 \\ & 2 E \\ & 2 F \\ & 1 B \\ & 2 A \\ & 3 A \\ & 3 B \\ & 3 B \\ & 10 \\ & 4 C \\ & 3 C \\ & 4 C \\ & 10 \\ & 40 \\ & 3 F \\ & 3 F \\ & 3 F \\ & 2 F \\ & 4 B \\ & 4 B \\ & 18 \\ & 18 \end{aligned}$ |
| Partial A14 also shown on dlagram 14. |  |  |  |  |  |  |  |  |  |  |  |
| assembly A16 |  |  |  |  |  |  |  |  |  |  |  |
| CIRCUT NUMERR | $\begin{aligned} & \text { SCHEM } \\ & \text { SOCATON } \end{aligned}$ | BOARD | $\begin{aligned} & \text { ciRCUIT } \\ & \text { NUMBER } \end{aligned}$ | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { BOCATION } \end{aligned}$ | $\begin{array}{\|l\|l\|l\|l\|ccl\|r} \text { NUMBER } \end{array}$ | SCHEM LOCATION | $\xrightarrow{\text { Boatro }}$ |
|  | $\begin{aligned} & 2 F \\ & 16 \\ & 11 \\ & 10 \\ & 10 \\ & 10 \\ & 3 \mathrm{C} \\ & 4 \mathrm{C} \\ & \hline 5 \mathrm{C} \\ & 88 \\ & 78 \end{aligned}$ | $\begin{aligned} & 7 H \\ & 7 H \\ & 7 H \\ & 7 H \\ & 7 H \\ & 7 H \\ & 7 G \\ & 7 H \\ & 7 J \\ & 7 J \\ & 7 J \\ & 7 J \\ & 7 J \end{aligned}$ |  | $\begin{aligned} & 78 \\ & 8 B \\ & 2 \mathrm{E} \\ & 2 \mathrm{C} \\ & 1 \mathrm{M} \\ & 26 \\ & 1 \mathrm{y} \\ & 1 \mathrm{H} \\ & 1 \mathrm{j} \\ & 10 \end{aligned}$ | $\begin{aligned} & 7 \mathrm{~J} \\ & \underset{\sim}{2} \\ & \begin{array}{l} 8 H \\ 8 H \\ 7 \mathrm{~K} \\ 7 \\ 7 \mathrm{~J} \\ 7 \mathrm{H} \\ 7 H \\ 7 H \end{array} \end{aligned}$ | $\begin{aligned} & \text { O2506 } \\ & \text { R2525 } \\ & \text { R2528 } \\ & \text { R2528 } \\ & \text { R2532 } \\ & \text { R2534 } \\ & \text { R2536 } \\ & R 2538 \\ & R 2540 \\ & R 2542 \\ & \text { R2546 } \end{aligned}$ | $\begin{aligned} & 1 \mathrm{MA} \\ & 2 \mathrm{AA} \\ & 4 \mathrm{~A} \\ & 1 \mathrm{~F} \\ & 1 \mathrm{G} \\ & 1 \mathrm{H} \\ & 110 \\ & 10 \\ & 3 B \end{aligned}$ |  |  | $\begin{aligned} & 4 C \\ & 4 C \\ & 5 B \\ & 6 B \\ & 7 B \\ & 7 B \\ & 7 B \\ & \hline 8 \\ & \hline 1 A \\ & 3 A \\ & \hline 8 \end{aligned}$ | $\begin{aligned} & 7 J \\ & 7 J \\ & 7 J \\ & 7 J \\ & 7 J \\ & 7 \mathrm{~K} \\ & 8 \\ & \hline 7 \\ & 7 \mathrm{H} \\ & 60 \end{aligned}$ |
| Partal A16 also shown on dlagrams 8, .9, 11 and 14. |  |  |  |  |  |  |  |  |  |  |  |





|  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| A12-POTENTIOMETER BOARD |  |  |  |  |  |

(112)

| A12-POTENTIOMETER BOARD |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| circuit nUMBER | schem NUMBER | Circuit NUMBER | SCHEM | circuit NUMBER | SCHEM nUMBER |
| ${ }^{2} 105$ | 11 | R2104 | 11 | R2110 |  |
| J2105 | 14 | ${ }^{\text {R2105 }}$ | 11 | ${ }^{\text {R2 } 2111}$ | 1 |
| R2101 | 11 | ${ }_{\text {R }}^{\text {R2106 }}$ | 11 | ${ }_{\substack{\text { R2112 } \\ \text { R2113 }}}^{2}$ | ${ }_{11}^{11}$ |
| ${ }^{\text {R2102 }}$ | 11 | ${ }_{\text {R2108 }}$ | 11 |  |  |
| R2103 | 11 | R2109 | 11 |  |  |

WAVEFORMS FOR DIAGRAM 11

(6555-73)6557-48

| ASSEMBLY A12 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT NUMBER | SCHEM location | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT | SCHEM | $\xrightarrow{\text { BOARD }}$ |
| J2105 <br> R2101 ${ }_{\substack{\text { R2102 } \\ \text { R2103 }}}^{\mathrm{R}^{2}}$ R20103 | $\begin{aligned} & 4 B \\ & 8 A \\ & 8 A \\ & 7 A \end{aligned}$ | $\begin{aligned} & 1 B \\ & 3 A \\ & 3 A \\ & 3 B \\ & 3 \end{aligned}$ | $\begin{aligned} & \text { R2 } 10 \end{aligned}$ | $\begin{aligned} & 8 A \\ & 7 A \\ & 3 A \\ & 4 A \end{aligned}$ | $\begin{aligned} & 2 B \\ & 4 \mathrm{C} \\ & 10 \\ & 30 \end{aligned}$ | $\begin{aligned} & \hline \hline \text { R2108 } \\ & \text { R2109 } \\ & \text { R2110 } \\ & \text { R2111A } \end{aligned}$ | $\begin{aligned} & 8 A \\ & 8 A \\ & 5 A \\ & 3 A \\ & 3 A \end{aligned}$ | $\begin{aligned} & 40 \\ & 4 F \\ & 2 E \\ & 20 \end{aligned}$ | R2111B ${ }^{\mathrm{R} 2112}$ R 212 A $\mathrm{R2113B}$ $\qquad$ | $\begin{aligned} & 3 B \\ & 5 A \\ & 4 B \\ & 4 A \end{aligned}$ | $\begin{aligned} & 20 \\ & 1 F \\ & 20 \\ & 2 E \end{aligned}$ |
| Partala A12 also shown on dlagram 14. |  |  |  |  |  |  |  |  |  |  |  |
| ASSEmbly A16 |  |  |  |  |  |  |  |  |  |  |  |
| $\begin{aligned} & \text { CIRCUIT } \\ & \text { UUMBER } \end{aligned}$ | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT | SCHEM LOCATION |  | CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ |  | $\begin{gathered} \text { CiRCuIt } \\ \text { CUWMER } \end{gathered}$ | $\begin{gathered} \text { SCHEM } \\ \text { LOCATON } \\ \hline \end{gathered}$ | $\begin{array}{\|c\|c\|c\|c\|c\|c\|c\|c\|} \hline \text { LOCATIO } \end{array}$ |
|  |  |  |  |  | $6 C$ $7 A$ $7 B$ $7 C$ $7 C$ $7 C$ $2 A$ $7 F$ $7 E$ $7 E$ $7 E$ $7 E$ $7 E$ $7 E$ $7 E$ $7 A$ $3 A$ $5 A$ $5 A$ $5 B$ 86 $6 A$ $6 A$ $6 A$ $8 A$ |  | $2 H$ $2 G$ $2 F$ $2 F$ $2 F$ $2 K$ $4 F$ $4 F$ $4 F$ $4 F$ $4 F$ $4 F$ $4 F$ $5 H$ $4 H$ $5 E$ $5 E$ $5 E$ $5 E$ $5 E$ $5 H$ $5 H$ $6 L$ $8 L$ $8 L$ 8 8 8 |  | R236 <br> R2363 <br> R2364 R2365 <br> U2300 <br> U2301 U2302 <br> U2303 <br>  <br> U 23035 S <br> U 23058 <br>  42306 <br>  $\underset{\substack{2 \\ \text { U2314 } \\ \text { U2314 }}}{ }$ w2105 |  |  |
| Partial A16 also shown on clagrams 8, 9, 10 and 14. |  |  |  |  |  |  |  |  |  |  |  |



## 2246 1Y and 2246 Mod A Service




(12c)

(120)


MORE $\downarrow$

WAVEFORMS FOR DIAGRAM 12 (cont)

(6555-46)6557-86

| ASSEmbly al8 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT | SCHEM LOCATION | BOARD | CIRCUIT NUMBER | SCHEM LOCATON |  | CIRCUIT | SCHEM SCATON | BOARD LOCATION | CIRCUIT NUMBER | SCHEM LOCATON | ${ }_{\text {L }}^{\text {BOARO }}$ |
|  |  |  | CR2209 CR2210CR2211 CR2212CR2213 CR2214 CR2215CR2216 CR2218CR2219 ${ }^{\text {CR2220 }}$CR22288CR2231CR2232 CR2233CR2234 ${ }_{c}^{\text {CR22233 }}$ CR2236CR2237 DS2201 F2201 J 2208J 225$\stackrel{2}{2} 201$ <br> 12202122023 <br> 12004$\stackrel{L}{L 2205}$42200 <br> 1202 <br> 1208L2208P220402201 <br> 0202 02202020302020 02204022060 022208020902210 |  |  |  |  |  | R2252 $R 2253$ $\mathrm{R2254}$ $\mathrm{R2255}$ <br>  R2257 R2259 R2260 R2265 ${ }^{\text {R2226 }}$ R22268 R228 R 22270 R 2271 R2272 R2273 R2274 R2275 <br> R2276 <br> RT2201 S2201 $\mathbf{S 2 2 0 2}$ T2203 T2204 T2205 T2206 U2201 U2230 VR2201 VR2202 VR2203 VR2204 VR2205 VR2206 VR2207 W28 w29 w 31 W32 W2201 |  |  |
| OTHER PARTS |  |  |  |  |  |  |  |  |  |  |  |
| Clircuit NUMEER | SCHEM LOCATION | $\begin{aligned} & \text { BoaAdo } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT NUMBER | SCHEM LOCATION | $\begin{aligned} & \text { Boapo } \\ & \text { Location } \end{aligned}$ | CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT <br> NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\xrightarrow{\text { bocaro }}$ |
| 325 | $\mathrm{on}^{1}$ | CHassis | FL2201 | 2 A | chassis | ${ }^{2726}$ | ¢м | CHASSIS | P25 | ${ }^{6}$ | CHASSIS |



MAIN BOARD POWER DISTRIBUTION DIAGRAM 13 (SN B710100 AND ABOVE, 2246 1Y
SN B110100 AND ABOVE, 2246 MOD A)

| ASSEmbly ab |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT <br> NUMBER | SCHEM LOCATON | ${ }_{\text {coser }}^{\text {BOARD }}$ |  | SCHEM | ${ }^{\text {L }}$ LOCATOD | circuit NUMEER | SCHEM LOCATON | ${ }_{\text {L }}^{\text {Bocation }}$ | ${ }_{\text {circuit }}^{\text {Cumber }}$ | SCHEM LOCATION | $\xrightarrow{\text { boato }}$ |
| w900 | 5 M | $1 E$ |  |  |  |  |  |  |  |  |  |
| Parial A A also shown on diagram 7. |  |  |  |  |  |  |  |  |  |  |  |
| ASSEmbly A10 |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |
| Partia $A$ | shown | rams | 5,6 and |  |  |  |  |  |  |  |  |

See Parts List for
serial number range



MAIN BOARD POWER DISTRIBUTION DIAGRAM 13 (SN B710099 AND BELOW, 2246 1Y
SN B110099 AND BELOW, 2246 MOD A)

| ASSEmbly as |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT | SCHEM LOCATION | ${ }_{\text {cose }}^{\text {BOARD }}$ | (CIRCUIT | SCHEM LOCATON | Lection | (1arcuin | SCHEM LOCATON | Location | Clircuit | SCHEM LOCATON | $\underset{\substack{\text { Boato } \\ \text { LOCATON }}}{ }$ |
| w900 | ${ }^{5 M}$ | 1 E |  |  |  |  |  |  |  |  |  |
| Pattal A8 also shown on dlagram 7 . |  |  |  |  |  |  |  |  |  |  |  |
| ASSEmbly alo |  |  |  |  |  |  |  |  |  |  |  |
| ALIRCUT | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { CIRCUIT } \\ & \text { CUMBER } \end{aligned}$ | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{array}{\|l\|l\|} \hline \text { BOARD } \\ \text { LOCATION } \end{array}$ | $\begin{aligned} & \text { CIRCUIT } \\ & \text { NUMBER } \end{aligned}$ | SCHEM location | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { CIRCUIT } \\ & \text { NUMBER } \end{aligned}$ | SCHEM LOCATION | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ |
|  |  |  | C610 <br> C613 C701 <br> C701 C702 <br> $C 703$ $C 704$ <br> $C 705$ $C 708$ <br> C 708 C 801 C 806 C 815 $C 815$ C 816 C 818 C 901 C 902 C 903 C 904 C 1005 C 1006 C1101 C1102 C1158 C1159 C2701 C2709 $J 1204$ <br> L101 <br> $L$ <br> $L$ <br>  <br>  <br> $L 475$ $L 476$ <br> P2302 <br> R182 R208 R226 R245 R283 R297 R297 $R 298$ R312 R339 R345 |  |  |  |  |  | U600 6601 U602 ${ }_{\mathrm{U}}^{\mathrm{U} 604}$ 4600 4702 $\stackrel{4}{4} 801$ Y901A 4901 ч931 <br>  U1101 $\underset{\substack{41102 \\ \text { U1103 } \\ \hline 1021}}{ }$U 1104 <br> U 1106${ }_{W}^{W} 1035$ wson W1200 $w 1202$Wi20W1204 <br> w 1205${ }^{W} 12209$ ${ }^{W}{ }^{W} 1216$ W 1218 $\underset{W}{ }{ }^{2222}$$W_{1223}^{123}$ <br> $w 1231$ <br> 102 W1237 W1248 W1249 W1251W1252 W1255 W2302W2502 |  |  |




PROCESSOR BOARD POWER DISTRIBUTION DIAGRAM 14

| ASSEMbLY A12 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT NUMBER | SCHEM | $\underset{\text { LOCATION }}{\text { Boad }}$ | CIRCUIT | SCHEM LOCATION | Boabd LOCATION | CIRCUIT NUMBER | SCHEM SCATION | $\xrightarrow{\text { Boado }}$ | CIRCUIT NUMBER | SCHEM LOCATION | $\underset{\text { Location }}{\text { Boad }}$ |
| J2105 | 3 L | 18 |  |  |  |  |  |  |  |  |  |
| Partial A12 also shown on olagram 11. |  |  |  |  |  |  |  |  |  |  |  |
| ASSEMBLY A14 |  |  |  |  |  |  |  |  |  |  |  |
| CIRCUIT NUMBER | SCHEM LOCATION | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT NUMBER | SCHEM LOCATION |  | CIRCUIT NUMBER | SCHEM LOCATION | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT <br> NUMBER | SCHEM LOCATION | $\begin{array}{\|l\|l\|} \hline \text { BOAARD } \\ \text { LOCATIN } \end{array}$ |
| c2001 | 19 | ${ }^{2 B}$ | P2501 | 1 | ${ }^{18}$ | U2001 | $1{ }_{1}^{12}$ | ${ }_{4 B}^{28}$ | W2501 | 1 | ${ }^{18}$ |
| Partala A14 also shown on dlagram 10. |  |  |  |  |  |  |  |  |  |  |  |
| ASSEmbly A16 |  |  |  |  |  |  |  |  |  |  |  |
| CIRCUIT | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { ClRCUIT } \\ & \text { NUMBER } \end{aligned}$ | SCHEM LOCATION | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT IUMBER | $\underset{\text { SCHEM }}{ }$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATIN } \end{aligned}$ |
|  | $3 B$ $1 C$ $1 C$ $1 C$ 10 $7 B$ $1 B$ 10 10 $1 C$ $1 C$ $1 C$ $1 C$ $1 C$ $1 C$ $1 C$ $1 C$ $1 C$ $2 C$ $2 C$ $2 C$ $2 C$ $8 H$ $2 C$ $2 C$ |  |  |  |  | U2300 <br> U2302 <br> U2303 <br> U2304 <br> U2306 U2308 <br> U2309 <br> U2314 <br> U2400 U2401 <br> U2402 U2403 <br> $U 2404$ $U 2405$ <br> U2406 <br> U2407 U2408 <br> U2409 U2410 |  |  | U2411 U2413 U2414 U2415 U2416 U2417 U2501 U2502 U2503 U2506 $U 2512$ $\cup 2513$ U2515 U2517 U2518 U2519 $\begin{array}{r}U 2524 \\ \\ \hline\end{array}$ w2105 |  |  |
| Partal A16 also shown on olagrams 8, 9.9 , 10 and 11. |  |  |  |  |  |  |  |  |  |  |  |






## REPLACEABLE MECHANICAL PARTS

## PARTS ORDERING INFORMATION

Replacement parts are available from or through your local Tektronix, Inc. Field Office or representative.

Changes to Tektronix instruments are sometimes made to accommodate improved components as they become available, and to give you the benefit of the latest circuit improvements developed in our engineering department. It is therefore important, when ordering parts, to include the following information in your order: Part number, instrument type or number, serial number, and modification number if applicable.

If a part you have ordered has been replaced with a new or improved part, your local Tektronix, Inc. Field Office or representative will contact you concerning any change in part number.

Change information, if any, is located at the rear of this manual.

## ITEM NAME

In the Parts List, an item Name is separated from the description by a colon(:). Because of space limitations, an Item Name may sometimes appear as incomplete. For further Item Name identification, the U.S. Federal Cataloging Handbook H6-1 can be utilized where possible.

## FIGURE AND INDEX NUMBERS

Items in this section are referenced by figure and index numbers to the illustrations.

## INDENTATION SYSTEM

This mechanical parts list is indented to indicate item relationships. Following is an example of the indentation system used in the description column.

## 12345 <br> Name \& Description

Assembly and/or Component Attaching parts for Assembly and/or Component

END ATTACHING PARTS
Detail Part of Assembly and/or Component Attaching parts for Detail Part

END ATTACHING PARTS
Parts of Detail Part
Attaching parts for Parts of Detail Part
END ATTACHING PARTS
Attaching Parts always appear in the same indentation as the item it mounts, while the detail parts are indented to the right. Indented items are part of, and included with, the next higher indentation.

Attaching parts must be purchased separately, unless otherwise specified.

## ABBREVIATIONS

Abbreviations conform to American National Standards Institute YI.I

## CROSS INDEX - MFR. CODE NUMBER TO MANUFACTURER

Mfr.

| Code | Manufacturer |
| :--- | :--- |
| S3109 | FELLER |
| S3629 | SCHURTER AG H |
|  | C/O PANEL COMPONENTS CORP |
| TK0435 | LEWIS SCREW CO |
| TK0860 | LABEL GRAPHICS |
| TK0892 | GEROME CORP |
|  |  |
| TK1163 | POLYCAST INC |
| TK1326 | NORTHWEST FOURSLIDE INC |
| TK1499 | AMLAN INC |
| TK1614 | STUCKEL R J CO |
| TK1692 | SEA LECT PRODUCTS INC |
| TK1808 | FASTEX |
| TK1908 | PLASTIC MOLDED PRODUCTS |
| TK1918 | SHIN-ETSU POLYMER AMERICA INC |
| TK1935 | ACCRA FAB INC |
| TK1938 | GALGON INDUSTRIES |
| TK2120 | LARSCO |
| TK2147 | WALKER SPRING \& STAMPING CORP |
| TK2432 | UNION ELECTRIC |
| OJRZ2 | BADGLEY MFG CO |
| OJR05 | TRIQUEST CORP |
| OJ260 | COMTEK MANUFACTURING OF OREGON |
|  | (METALS) |

## OJ9P9 GEROME MFG CO INC

OKBZ5 MORELLIS Q \& D PLASTICS
OKB01 STAUFFER SUPPLY

07416 NELSON NAME PLATE CO
12327 FREEWAY CORP
13103 THERMALLOY CO INC
18565 CHOMERICS INC
2K262
28520

70903

71400
73743
75915

BOYD CORP
HEYCO MOLDED PRODUCTS

AAVID ENGINEERING INC
BOGEN COMMUNICATIONS INC
COOPER BELDEN ELECTRONICS WIRE AND CABLE SUB OF COOPER INDUSTRIES INC BUSSMANN DIV OF COOPER INDUSTRIES INC FISCHER SPECIAL MFG CO
LITTELFUSE INC
SUB TRACOR INC

Address
72 Veronica Ave Unit 4 2015 SECOND STREET

4300 S RACINE AVE 6700 SW BRADBURY CT OLIVER RD PO BOX 1089 9898 SW TIGARD ST 18224 SW 100TH CT 97 THORNWOOD RD 1385 HOWARD ST 821 S THIRD 195 ALGONQUIN RD 4336 SO ADAMS 1181 NORTH 4TH ST 11007 NE 37TH CIRCLE 37399 CENTRAL MONT PLACE PO BOX 1127 14831 SPRING AVE 15/F \#1, FU-SHING N. ROAD 1620 NE ARGYLE 3000 LEWIS AND CLARK HWY PO BOX 4200

PO BOX 737
1812 16TH AVE
810 SE SHERMAN
3634 EUCLID P O BOX 6925
5825 N TRIPP AVE 3191 CASITAS 9301 ALLEN DR
2021 W VALLEY VIEW LN PO BOX 810839 77 DRAGON COURT
6136 NE 87th AVE PO BOX 20038
750 BOULEVARD P O BOX 160 ONE KOOL PATH PO BOX 400 50 SPRING STREET PO BOX 575

114 OLD STATE RD PO BOX 14460 111 INDUSTRIAL RD 800 E NORTHWEST HWY

City, State, Zip Code
Summerset NJ 08873
BERKELEY CA 94170
CHICAGO IL 60609-3320
PORTLAND OR 97224
UNIONTOWN PA 15041
TIGARD OR 97223
TUALATIN OR 97062
STAMFORD CT 06903-2617
ELK GROVE VILLAGE IL 60007-2213
KENT WA 98032-6129
DES PLAINES IL 60016
TACOMA WA 98409
SAN JOSE CA 95112
VANCOUVER WA 98682
FREMONT CA 94536
CLACKAMAS OR 97015
SANTA FE SPRINGS CA 90670-5109
TAIPEI, TAIWAN ROC
PORTLAND OR 97211
VANCOUVER WA 98661-2999
BEAVERTON OR 97076-4200
NEWBERG OR 97132
FOREST GROVE OR 97116
PORTLAND OR 97214
CLEVELAND OH 44101
CHICAGO IL 60646-6013
LOS ANGELES CA 90039-2410
CLEVELAND OH 44125-4632
DALLAS TX 75381
WOBURN MA 01801-1039
PORTLAND OR 97220
KENILWORTH NJ 07033-1721
LACONIA NH 03247
RAMSEY NJ 07446

ST LOUIS MO 63178
COLD SPRING KY 41076-9749
DES PLAINES IL 60016-3049

## CROSS INDEX - MFR. CODE NUMBER TO MANUFACTURER

| Mfr. <br> Code | Manufacturer | Address | City, State, Zlp Code |
| :--- | :--- | :--- | :--- |
| 78189 | ILLINOIS TOOLWORKS INC | ST CHARLES ROAD | ELGIN IL 60120 |
|  | SHAKEPROOF DIV | 14150 SW KARL BRAUN DR |  |
| 80009 | TEKTRONIX INC | PEA BOX 500 |  |
| 83014 | HARTWELL CORP | 950 S RICHFIELD RD | PLACENTIA CA 92670-673 |
|  | LOCKWELL DIVISION |  |  |

Replaceable Mechanical Parts 2246 1Y and 2246 MOD A Service

| Fig. \& Index No. | Tektronix Part No. | Serlal Effective | No. Dscont | Oty | 12345 Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1-1 | 334-6445-00 |  |  | 1 | MARKER,IDENT:MKD TEKTRONIX 2246 | 07416 | ORDER BY DESC |
| -2 | 437-0390-00 |  |  | 1 | CABINET ASSY:390-1057-00 W/FEET \& HANDLE | TK1935 | ORDER BY DESC |
| -3 | 337-2395-00 |  |  | 2 | SHIELD,ELEC:HANDLE ATTACHING PARTS | TK1614 | ORDER BY DESC |
| -4 | 213-0138-00 |  |  | 4 | .SCREW,TPG,TF:4-24 X 0.188,TYPE B,PNH,STL <br> END ATTACHING PARTS | TK0435 | ORDER BY DESC |
| -5 | 367-0289-00 |  |  | 1 | .HANDLE,CARRYING:13.855,SST, ATTACHING PARTS | 80009 | 367028900 |
| -6 | 212-0144-00 |  |  | 2 | .SCREW,TPG,TF:8-16 X 0.562 L,PLASTITE,SPCL .HD <br> END ATTACHING PARTS | OKBO1 | ORDER BY DESC |
| -7 | 390-1057-01 |  |  | 1 | .CABINET,SCOPE:EMI VERSION,ALUMINUM | 80009 | 390105701 |
| -8 | 348-0659-00 |  |  | 2 | .FOOT,CABINET:BLACK POLYURETHANE | OJR05 | ORDER BY DESC |
| -9 | 348-0764-04 |  |  | 1 | SHLD GSKT,ELEK:0.125 X 0.188,WIRE MESH, 2 .LAYERS,37.0 L | 18565 | ORDER BY DESC |
| -10 | 200-3660-00 |  |  | 1 | COVER,AUTO CAL:POLYCARBONATE | TK1163 | ORDER BY DESC |
| -12 | 200-3233-02 |  |  | 1 | COVER,REAR:PLASTIC,W/LABELS ATTACHING PARTS | 80009 | 200323302 |
| -13 | 211-0691-00 | B100100 | B114168 | 4 | SCREW,MACHINE:6-32 $\times 0.625$,PNH,STL ( 2246 MOD A,2Y) | OKB01 | ORDER BY DESC |
|  | 213-0942-00 | B114169 |  | 4 | SCREW,TPG,TR:6-32 $\times 0.75$,TYPE TT,PNH,STL ( 2246 MOD A.2Y) | OKB01 | ORDER BY DESC |
|  | 211-0691-00 | B700100 | B711094 | 4 | SCREW,MACHINE:6-32 X 0.625,PNH,STL $(2246 \text { 1Y) }$ | OKB01 | ORDER BY DESC |
|  | 213-0942-00 | B711095 |  | 4 | SCREW,TPG,TR:6-32 $\times 0.75$,TYPE TT,PNH,STL (2246 1Y) <br> END ATTACHING PARTS | OKB01 | ORDER BY DESC |
| -14 | - |  |  | 2 | (PART OF FIG 1-12) |  |  |
| -15 | 334-6707-00 |  |  | 1 | MARKER,IDENT:MKD CAUTION | 07416 | ORDER BY DESC |
| -16 | 334-6708-00 |  |  | 1 | MARKER,IDENT:MKD REAR PANEL Z-AXIS, | 07416 | ORDER BY DESC |




| Fig. 8 Index No. | Tektronix Part No. | Serial Effective | No. Dscont | Oty | 12345 Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 2-1 | 334-6446-00 |  |  | 1 | MARKER,IDENT:MKD TEKTRONIX 2246 | 07416 | ORDER BY DESC |
| -2 | 426-1765-02 |  |  | 1 | FRAME,CRT:POLYCARBONATE,GRAY, ATTACHING PARTS | OJR05 | ORDER BY DESC |
| -3 | 211-0690-01 |  |  | 2 | SCREW,MACHINE:6-32 $\times 0.875$ PNH,SST END ATTACHING PARTS | OKBO1 | ORDER BY DESC |
| -4 | 337-2775-00 |  |  | 1 | SHLD,IMPLOSION:FILTER,BLUE | OJR05 | ORDER BY DESC |
| -5 | 333-3290-00 |  |  | 1 | PANEL,FRONT: | 07416 | ORDER BY DESC |
| -6 | 351-0752-00 |  |  | 1 | GUIDE,LIGHT:ACRYLIC GRATICULE | TK1908 | PER TEK DRAWING |
| -7 | 348-0660-00 |  |  | 4 | CUSHION,CRT:POLYURETHANE | 80009 | 348066000 |
| -8 | 366-2089-00 |  |  | 5 | KNOB:GRAY,PUSH ON, 0.185 ID X 0.392 OD X 0.495 H | TK1908 | PER TEK DRAWING |
| -9 | 366-2093-00 |  |  | 2 | KNOB:DOVE GRAY, 0.235 ID $\times 0.36$ OD X 0.495H W/SHAFT PUSHON W/O INDEX | TK1908 | PER TEK DRAWING |
| -10 | 366-1510-00 |  |  | 3 | KNOB:DOVE GRAY,VAR, 0.127 ID X 0.392 OD X <br> 0.466 H | 0.JR05 | ORDER BY DESC |
| -11 | 366-2090-00 |  |  | 3 | KNOB:GRAY,VAR, 0.2 ID X 0.546 ODX 0.69 H | TK1908 | PER TEK DRAWING |
| -12 | 366-2089-00 |  |  | 8 | KNOB:GRAY,PUSH ON, 0.185 ID X 0.392 OD X 0.495 H | TK1908 | PER TEK DRAWING |
| -13 | 333-3282-00 |  |  | 1 | PANEL,FRONT: | 07416 | ORDER BY DESC |
| -14 | 386-3339-00 |  |  | 1 | SUBPANEL,FRONT: | TK1908 | PER TEK DRAWING |
| -15 | 348-1021-00 |  |  | 1 | SHLD GSKT,ELEK: 2 LAYER, $0.094 \times 0.188 \times 37$ | OJ9P9 | ORDER BY DESC |
| -16 | 337-3485-01 |  |  | 1 | SHIELD,ELEC:CRT,MAGNETIC FRONT | 0J9P9 | ORDER BY DESC |
| -17 | 348-1015-00 | B100100 | B110537 | 1 | GASKET,CRT SHLD:CU BE (2246 MOD A,2Y) | 80009 | 348101500 |
|  | 348-1130-00 | B110538 |  | 1 | SHLD GSKT,ELEK:EMI,SST ( 2246 MOD A,2Y) | TK1326 | ORDER BY DESC |
|  | 348-1015-00 | B700100 | B710264 | 1 | GASKET,CRT SHLD:CU BE (2246 1Y) | 80009 | 348101500 |
|  | 348-1130-00 | B710265 |  | 1 | SHLD GSKT,ELEK:EMI,SST (2246 1Y) | TK1326 | ORDER BY DESC |
| -18 | 348-0532-00 |  |  | 2 | GROMMET,PLASTIC:BLACK,ROUND, 0.625 ID | 28520 | 2096 |
| -19 | ----------- |  |  | 1 | FILTER,RFI: (SEE FL2201 REPL) ATTACHING PARTS |  |  |
| -20 | 213-0882-00 |  |  | 2 | SCREW,TPG,TR:6-32 X 0.437 TAPTITE,PNH,STL END ATTACHING PARTS | OKBO1 | ORDER BY DESC |
| -21 |  |  |  | 1 | CONN,RCPT,ELEC:BNC (SEE J16 REPL) |  |  |
| -22 | 441-1791-00 | B100100 | B111389 | 1 | CHASSIS,REAR: <br> ( 2246 MOD A,2Y) | 0J260 | ORDER BY DESC |
|  | 441-1791-01 | B111390 |  | 1 | CHASSIS,REAR:EMI VERSION (2246 MOD A,2Y) | 0 J 260 | ORDER BY DESC |
|  | 441-1791-00 | B700100 | B710984 | 1 | CHASSIS,REAR: <br> (2246 1Y) | 0J260 | ORDER BY DESC |
|  | 441-1791-01 | B710985 |  | 1 | ```CHASSIS,REAR:EMI VERSION (2246 1Y) ATTACHING PARTS``` | 0J260 | ORDER BY DESC |
| -23 | 213-0882-00 |  |  | 10 | SCREW,TPG,TR:6-32 X 0.437 TAPTITE,PNH,STL END ATTACHING PARTS | OKB01 | ORDER BY DESC |
| -24 | 343-1240-00 |  |  | 2 | CLAMP,CABLE:0.25 ID,NYLON | TK1808 | 220-340802-00 |
| -25 | ----------- |  |  | 1 | LEAD,ELECTRICAL: (SEE W30 REPL) ATTACHING PARTS |  |  |
| -26 | 210-0457-00 |  |  | 1 | NUT,PL,ASSEM WA:6-32 $\times 0.312, \mathrm{STL}$ CD PL | TK0435 |  |
|  | 210-0006-00 |  |  | 1 | WASHER,LOCK:\#6 INTL,0.018 THK,STL END ATTACHING PARTS |  | 1206-00-00-0541 |
| -27 | 214-1061-06 |  |  | 1 | SPRING,GROUND:CRT SHIELD | TK1326 | ORDER BY DESC |
| -27.1 | 200-2519-00 | B114612 |  | 1 | CAP,CRT SOCKET:NATURAL LEXAN, (2246 MOD A,2Y) | 80009 | 200251900 |
|  | 200-2519-00 | B711095 |  | 1 | CAP,CRT SOCKET:NATURAL LEXAN, (2246 1Y) | 80009 | 200251900 |

Replaceable Mechanical Parts 2246 1Y and 2246 MOD A Service

| Fig. \& Index No. | Tektronix Part No. | Serial Effective | No. Dscont | Qty | 12345 Name \& Description | Mfr. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| -28 | 337-3411-00 | B100100 | B114611 | 1 | SHIELD,ELEC:CRT,REAR (2246 MOD A,2Y) | 0J9P9 | ORDER BY DESC |
|  | 200-2809-00 | B114612 |  | 1 | COVER,CRT:REAR,ALUMINUM (2246 MOD A,2Y) | TK1692 | 76280-000 |
|  | 337-3411-00 | B700100 | B711094 | 1 | SHIELD,ELEC:CRT,REAR (2246 1Y) | 0J9P9 | ORDER BY DESC |
|  | 200-2809-00 | B711095 |  | 1 | COVER,CRT:REAR,ALUMINUM (2246 1Y) <br> ATTACHING PARTS | TK1692 | 76280-000 |
| -29 | 213-0882-00 | B100100 | B114611 | 3 | SCREW,TPG,TR:6-32 $\times 0.437$ TAPTITE,PNH,STL (2246 MOD A,2Y) | OKB01 | ORDER BY DESC |
|  | 213-0882-00 | B114612 |  | 2 | SCREW,TPG,TR:6-32 $\times 0.437$ TAPTITE,PNH,STL ( 2246 MOD A,2Y) | OKB01 | ORDER BY DESC |
|  | 213-0882-00 | B700100 | B711094 | 3 | SCREW,TPG,TR:6-32 $\times 0.437$ TAPTITE,PNH,STL (2246 1Y) | OKB01 | ORDER BY DESC |
|  | 213-0882-00 | B711095 |  | 2 | SCREW,TPG,TR:6-32 X 0.437 TAPTITE,PNH,STL (2246 1Y) <br> END ATTACHING PARTS | OKB01 | ORDER BY DESC |
| -30 | 426-1766-00 |  |  | 1 | MOUNT,RESILIENT:CRT,REAR, | 80009 | 426176600 |
| -31 | ----------- |  |  | 1 | WIRE SET,ELEC:CRT SOCKET (SEE A10W9 REPL) |  |  |
| -32 | 337-2774-00 | B100100 | B113054 | 1 | SHIELD,ELEC:CRT,STEEL (2246 MOD A,2Y) | TK0892 | ORDER BY DESC |
|  | 337-3487-00 | B113055 |  | 1 | SHIELD,ELEC:CRT,STL (2246 MOD A,2Y) | 0J9P9 | 337-3487-00 |
|  | 337-2774-00 | B700100 | B711092 | 1 | SHIELD,ELEC:CRT,STEEL (2246 1Y) | TK0892 | ORDER BY DESC |
|  | 337-3487-00 | B711093 |  | 1 | SHIELD,ELEC:CRT,STL $(2246 \text { 1Y) }$ | 0J9P9 | 337-3487-00 |
| -33 | 386-4443-00 |  |  | 1 | SUPPORT,SHIELD:CRT,FRONT,PLASTIC | 80009 | 386444300 |
| -34 | 334-1951-00 |  |  | 1 | MARKER,IDENT:MKD WARNING,CRT VOLTAGES | TK0860 | ORDER BY DESC |
| -35 | 334-1379-00 |  |  | 1 | MARKER,IDENT:MKD HI VACUUM | 07416 | ORDER BY DESC |
| -36 | ------------ |  |  | 1 | DELAY LINE: (SEE DL21 REPL) |  |  |
| -37 | 343-0549-00 |  |  | 2 | STRAP,TIEDOWN,E:0.098 W X 4.0 L,ZYTEL | TK1499 | HW-047 |
| -38 | 441-1720-00 |  |  | 1 | CHAS,PWR SUPPLY:GPSB | 0J260 | ORDER BY DESC |
| -39 | ------------ |  |  | 1 | FAN,TUBAXIAL: (SEE B25 REPL) ATTACHING PARTS |  |  |
| -40 | 213-0991-00 |  |  | 4 | SCREW,TPG,TC:6-32 X 1.25 L,TYPE T,PNH,STL END ATTACHING PARTS | OKB01 | ORDER BY DESC |
| -41 | 343-1305-00 |  |  | 1 | CLP,WIRE SADDLE:0.437 ID,NYLON | TK2120 | ORDER BY DESC |
| -42 | 348-0532-00 |  |  | 2 | GROMMET,PLASTIC:BLACK,ROUND,0.625 ID | 28520 | 2096 |
| -43 | 344-0347-00 |  |  | 1 | CLIP,ELECTRICAL:ANODE,0.72 OD,NYLON | 0 JR05 | ORDER BY DESC |
| -44 | 441-1719-03 |  |  | 1 | CHASSIS,MAIN:ALUMINUM ATTACHING PARTS | 0J9P9 | ORDER BY DESC |
| -45 | 213-0882-00 |  |  | 6 | SCREW,TPG,TR:6-32 X 0.437 TAPTITE,PNH,STL END ATTACHING PARTS | OKB01 | ORDER BY DESC |
| -46 | 378-0295-00 |  |  | 1 | GRILLE,AIR DUCT:ALUMINUM | 0J260 | ORDER BY DESC |
| -47 | 214-3835-00 |  |  | 1 | ARM,PIVOT:POWER SWITCH | TK1908 | PER TEK DRAWING |
| -48 | 384-1697-00 |  |  | 1 | EXTENSION SHAFT:6.25 L 0.285 OD,NYLON | TK1908 | PER TEK DRAWING |
| -49 | 384-1696-00 | B100100 | B100585 | 1 | EXTENSION SHAFT:12.2 LX 0.285 OD,NYLON (2246 MOD A,2Y) | TK1908 | PER TEK DRAWING |
|  | 384-1696-01 | B100586 |  | 1 | EXTENSION SHAFT:13.341 LX 0.285 OD,PC (2246 MOD A,2Y) | TK1908 | ORDER BY DESC |
|  | 384-1696-01 |  |  | 1 | EXTENSION SHAFT:13.341 LX 0.285 OD,PC (2246 1Y) | TK1908 | ORDER BY DESC |


| Fig. \& Index No. | Tektronix Part No. | Serial Effective | No. Dscont | Qty | 12345 Name \& Description | Mfr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 3-1 | ------------ |  |  | 1 | CIRCUIT BD ASSY:POTENTIOMETER (SEE A12) |  |  |
| -2 | 214-3826-00 |  |  | 7 | LATCH,PLUNGER:BLACK | 83014 | 3P-32-4-1 |
|  | 348-0904-00 |  |  | 7 | GROMMET,FSTNR:0.187 DIA,BLACK | 83014 | HN3G-32-1 |
| -3 | 376-0130-00 |  |  | 3 | COUPLER,SHAFT:2.260 LX 0.132 ID,POLYCARBONATE | TK1908 | PER TEK DRAWING |
| -4 | ----------- |  |  | 1 | CIRCUIT BD ASSY:SWITCH (SEE A14 REPL) |  |  |
| -5 | 260-2271-00 |  |  | 1 | SWITCH,PUSH:42 BUTTON, 2 POLE | TK1918 | PER TEK DRAWING |
| -6 | 366-2088-00 |  |  | 21 | PUSH BUTTON:GRAY,0.172 SQ $\times 0.3 \mathrm{H}$ | TK1908 | PER TEK DRAWING |
| -7 | 105-0984-01 |  |  | 3 | ACTR SWITCH AS:W/CONTACT | 80009 | 105098401 |
| -8 | 214-1126-01 |  |  | 3 | SPRING,FLAT:0.7 X 0.125,CU BE GRN CLR | OJ260 | 214-1126-01 |
| -9 | 214-0274-00 |  |  | 3 | BALL,BEARING:0.125 DIA,SST,GRADE 100 | 05469 | ORDER BY DESC |
| -10 | 366-2091-00 |  |  | 17 | PUSH BUTTON:CLEAR,0.312 DIA X 0.3 H | TK1908 | PER TEK DRAWING |
| -11 | 380-0767-00 |  |  | 1 | HOUSING,SWITCH:POLYCARBONATE | TK1908 | PER TEK DRAWING |
| -12 | ----------- |  |  | 1 | CIRCUIT BD ASSY:PROCESSOR (SEE A16 REPL) ATTACHING PARTS |  |  |
| -13 | 213-0882-00 |  |  | 12 | SCREW,TPG,TR:6-32 $\times 0.437$ TAPTITE,PNH,STL | OKB01 | ORDER BY DESC |
| -14 | 211-0691-00 | B100100 | B114168 | 2 | SCREW,MACHINE:6-32 $\times 0.625$,PNH,STL (2246 MOD A,2Y) | OKB01 | ORDER BY DESC |
|  | 213-0942-00 | B114169 |  | 2 | SCREW,TPG,TR:6-32 $\times 0.75$,TYPE TT,PNH,STL (2246 MOD A,2Y) | OKB01 | ORDER BY DESC |
|  | 211-0691-00 | B700100 | B711094 | 2 | SCREW,MACHINE:6-32 $\times 0.625$,PNH,STL (2246 1Y) | OKB01 | ORDER BY DESC |
|  | 213-0942-00 | B711095 |  | 2 | SCREW,TPG,TR:6-32 $\times 0.75$,TYPE TT,PNH,STL (2246 1Y) <br> END ATTACHING PARTS | OKB01 | ORDER BY DESC |
|  | 352-0863-00 | B700100 | B715000 | 2 | HOLDER,BATTERY:PLASTIC (2246 1Y) | 65249 | BH500 |
| -15 | 337-3290-01 | B100100 | B113293 | 1 | SHIELD,ELEC:TOP,W/CAUTION LABEL (2246 MOD A,2Y) | 80009 | 337329001 |
|  | 337-3681-01 | B113294 |  | 1 | SHIELD,ELEC:TOP <br> ( 2246 MOD A,2Y) | 80009 | 337368101 |
|  | 337-3290-01 | B700100 | B711094 | 1 | SHIELD,ELEC:TOP,W/CAUTION LABEL (2246 1Y) | 80009 | 337329001 |
|  | 337-3681-01 | B711095 |  | 1 | SHIELD,ELEC:TOP $(2246 \text { 1Y) }$ | 80009 | 337368101 |
|  | 334-4251-00 |  |  | 1 | MARKER,IDENT:MKD CAUTION | 07416 | ORDER BY DESC |
|  | 131-1428-00 |  |  | 1 | CONTACT,ELEC:GROUNDING,CU BE CD PL | TK1326 | ORDER BY DESC |
| -16 | 361-1427-00 |  |  | 2 | SPACER,CABLE:SILICONE | 2 K 262 | ORDER BY DESC |
| -17 | ----------- |  |  | 1 | CIRCUIT BD ASSY:LVPS (SEE A18 REPL) ATTACHING PARTS |  |  |
| -18 | 213-0882-00 |  |  | 6 | SCREW,TPG,TR:6-32 X 0.437 TAPTITE,PNH,STL END ATTACHING PARTS CIRCUIT BOARD ASSY INCLUDES: | OKB01 | ORDER BY DESC |
|  | 214-3796-00 |  |  | 2 | .HEAT SINK,XSTR:ALUMINUM,TO-220;VERTICAL .ECB. MOUNT,TINNED TAB <br> .(FOR A18Q2209 \& A18Q2210) | 30161 | 5968B |
| -19 | 204-0906-00 |  |  | 1 | .BODY,FUSEHOLDER:3AG \& $5 \times 20 \mathrm{MM}$ FUSES | S3629 | TYPE FAU 031.35 |
| -20 | 200-2264-00 |  |  | 1 | .CAP,FUSEHOLDER:3AG FUSES, | S3629 | FEK 0311666 |
| -21 | 214-3821-00 |  |  | 2 | .HEAT SINK,XSTR:PWR SPLY,GOLD .W/CHROMATE PL | 13103 | 214-3821-00 |
| -22 | ----------- |  |  | 1 | .SWITCH,THRMSTC: (SEE A18S2202 REPL) ATTACHING PARTS |  |  |
| -23 | 213-0882-00 |  |  | 2 | .SCREW,TPG,TR:6-32 $\times 0.437$ TAPTITE,PNH,STL END ATTACHING PARTS | OKB01 | ORDER BY DESC |
| -24 | --------- |  |  | 1 | .TRANSISTOR: (SEE A18Q2201 REPL) ATTACHING PARTS |  |  |
| -25 | 213-0882-00 |  |  | 1 | .SCREW,TPG,TR:6-32 X 0.437 TAPTITE,PNH,STL END ATTACHING PARTS | OKBO1 | ORDER BY DESC |


| Fig. \& Index No. | Tektronix Part No. | Serial Effective | No. Dscont | Oty | 12345 Name \& Description | Mr. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| -26 | ----------- |  |  | 1 | .TRANSISTOR: (SEE A18Q2214 REPL) ATTACHING PARTS |  |  |
| -27 | 213-0882-00 |  |  | 1 | .SCREW,TPG,TR:6-32 $\times 0.437$ TAPTITE,PNH,STL END ATTACHING PARTS | OKB01 | ORDER BY DESC |
|  | 344-0410-00 | B700100 | B710099 | 1 | .CLIP,COIL SPRT: $1 \times 1.46$,POLYCARBONATE .(2246 1Y) | OKBZ5 | ORDER BY DESC |
|  | 344-0410-02 | B710100 |  | 1 | .CLIP.COIL SPRT:1.0X 1.46,POLYCARBONATE (2246 1Y) | TK1908 | ORDER BY DESC |
|  | 344-0410-00 | B100100 | B103215 | 1 | .CLIP,COIL SPRT: $1 \times 1.46$, POLYCARBONATE (2246 MOD A,2Y) | OKBZ5 | ORDER BY DESC |
|  | 344-0410-02 | B103216 |  | 1 | .CLIP,COIL SPRT:1.0 X 1.46,POLYCARBONATE (2246 MOD A,2Y) | TK1908 | ORDER BY DESC |
| -28 | 342-0781-00 |  |  | 1 | INSUL,PWR SPLY:POLYCARBONATE | 2K262 | PER TEK DRAWING |
| -29 | 342-0324-00 |  |  | $\begin{aligned} & 1 \\ & 6 \end{aligned}$ | CIRCUIT BD ASSY:MAIN (SEE A10 REPL) .INSULATOR,DISK:TRANSISTOR,NYLON | 13103 | 7717-5N-BLUE |
| -30 | 337-3342-02 |  |  | 2 | .SHIELD,ELEC:HIGH VOLTAGE | TK1908 | ORDER BY DESC |
| -31 | 337-3358-01 |  |  | 1 | .SHIELD,ATTEN:FRONT,MAIN BD ATTACHING PARTS | TK1938 | ORDER BY DESC |
| -32 | 211-0690-01 |  |  | 2 | .SCREW,MACHINE:6-32 X 0.875 PNH,SST END ATTACHING PARTS | OKB01 | ORDER BY DESC |
| -33 | 337-3279-00 | B100100 | B113516 | 1 | .SHIELD,ATTEN:ALUMINUM .(2246 MOD A,2Y) | TK1938 | ORDER BY DESC |
|  | 337-3358-02 | B113517 |  | 1 | .SHIELD.ATTEN:FRONT,MAIN BD . 2246 MOD A,2Y) | 0.J9P9 | ORDER BY DESC |
|  | 337-3279-00 | B700100 | B711092 | 1 | .SHIELD,ATTEN:ALUMINUM (2246 1Y) | TK1938 | ORDER BY DESC |
|  | 337-3358-02 | B711093 |  | 1 | .SHIELD,ATTEN:FRONT,MAIN BD . (2246 1Y) <br> ATTACHING PARTS | 0.J9P9 | ORDER BY DESC |
| -34 | 213-0882-00 |  |  | 10 | .SCREW,TPG,TR:6-32 $\times 0.437$ TAPTITE,PNH,STL END ATTACHING PARTS | OKBO1 | ORDER BY DESC |
| -35 | 344-0286-00 |  |  | 6 | .CLIP,ELECTRICAL:FUSE,SPR BRS, | 75915 | 102074 |
| -35.1 | 337-0896-00 | B110100 |  | 1 | .PLATE,ELEC SHLD:B SWEEP CKT BD <br> (2246 MOD A,2Y) | 0J260 | ORDER BY DESC |
|  | 337-0896-00 | B710100 |  | 1 | .PLATE,ELEC SHLD:B SWEEP CKT BD .(2246 1Y) | O. 260 | ORDER BY DESC |
| -36 | 343-0003-00 |  |  | 1 | .CLAMP,LOOP:0. 25 ID.PLASTIC ATTACHING PARTS | 06915 | E4 CLEAR ROUND |
| -37 | 213-0882-00 | B100100 | B114168 | 1 | .SCREW,TPG,TR:6-32 $\times 0.437$ TAPTITE,PNH,STL .(2246 MOD A,2Y) | OKBO1 | ORDER BY DESC |
|  | 213-0942-00 | B114169 |  | 1 | .SCREW,TPG,TR: $6-32 \times 0.75$, TYPE TT,PNH,STL .(2246 MOD A,2Y) | OKBO1 | ORDER BY DESC |
|  | 213-0882-00 | B700100 | B711094 | 1 | .SCREW,TPG,TR:6-32 $\times 0.437$ TAPTITE,PNH,STL .(2246 1Y) | OKB01 | ORDER BY DESC |
|  | 213-0942-00 | B711095 |  | 1 | .SCREW,TPG,TR:6-32 $\times 0.75$, TYPE TT,PNH,STL (2246 1Y) | OKB01 | ORDER BY DESC |
| -38 | 210-0949-00 | B100100 | B114168 | 1 | .WASHER,FLAT:0. $141 \mathrm{ID} \times 0.5$ OD X 0.062,BRS .(2246 MOD A,2Y) | 12327 | ORDER BY DESC |
|  | 210-0949-00 | B700100 | B711094 | 1 | .WASHER,FLAT: 0.141 ID X 0.5 OD X 0.062,BRS .(2246 1Y) <br> END ATTACHING PARTS | 12327 | ORDER BY DESC |
| -39 | 407-3416-00 |  |  | 1 | .BRACKET,ATTEN:BRASS | 0J9P9 | ORDER BY DESC |
| -40 | ---------- |  |  | 4 | .CONN,RCPT,ELEC:BNC(SEEA10J11,J12,J13,J14) ATTACHING PARTS |  |  |
| -41 | 220-0497-00 |  |  | 4 | .NUT,PLAIN,HEX:0.5-28 X 0.562 HEX,BRS CD PL | 73743 | ORDER BY DESC |
| -42 | 210-1039-00 |  |  | 4 | .WASHER,LOCK:0.521 ID,INT,0.025 THK,SST | OKB01 | 1224-02-00-0541 |


| Fig. \& Index No. | Tektronix Part No. | Serial Effective | No. Dscont | Qty | 12345 Name \& Description | Mtr. Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| -43 | 131-3960-00 | B100100 | B110494 | 1 | .CONTACT,ELEC:BNC GROUND,CU-BE ALLOY (2246 MOD A,2Y) | TK2147 | ORDER BY DESC |
|  | 131-4521-00 | B110495 |  | 2 | .CONTACT,ELEC:INPUT BNC GROUNDING,CU BE .ALLO.Y <br> (2246 MOD A,2Y) | TK1326 | ORDER BY DESC |
|  | 131-3960-00 | B700100 | B710252 | 1 | .CONTACT,ELEC:BNC GROUND,CU-BE ALLOY (2246 1Y) | TK2147 | ORDER BY DESC |
|  | 131-4521-00 | B710253 |  | 2 | .CONTACT,ELEC:INPUT BNC GROUNDING,CU BE .ALLO.Y <br> . (2246 1Y) | TK1326 | ORDER BY DESC |
| -44 | 214-3136-00 |  |  | 2 | .HEAT SINK,XSTR:TO-5,ALUMINUM | 13103 | 2228B |
|  | 358-0715-00 |  |  | 2 | .BUSHING,SNAP:0.25 ID X 0.234 THK,NYLON, .0.375 OD | 28520 | 2810 |
| -45 | 384-1702-00 |  |  | 1 | .EXTENSION SHAFT:9.97 L X 0.25,POLYMIDE | TK1908 | PER TEK DRAWING |
| -46 | ----------- |  |  | 1 | .CIRCUIT BD ASSY:CRT CONTROL (SEE A8 .REPL) |  |  |
| -47 | 358-0715-00 |  |  | 1 | ..BUSHING,SNAP:0.25 ID X 0.234 THK,NYLON, .. 0.375 OD | 28520 | 2810 |
|  | 384-1713-00 |  |  | 4 | ..EXTENSION SHAFT:0.918 L X 0.218 OD,PLASTIC | TK1908 | ORDER BY DESC |
| -48 | ---------- |  |  | 1 | CIRCUIT BD ASSY:2 MILLIVOLT BANDWIDTH .(SEE A9 REPL - NO LONGER REPLACEABLE) |  |  |

## Replaceable Mechanical Parts 2246 1Y and 2246 MOD A Service

Fig. 8 Index No.

Tektronix Part No. Serial No. Effective Dscont Oty 12345 Name \& Description Mir Code Mfr. Part No.



2246 1Y/2246 MOD A

| Fig. \& Index No. | Tektronix Part No. | Serial No. Effective Dscont | Oty | 12345 Name \& Description | Mfr. <br> Code | Mfr. Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 4- STANDARD ACCESSORIES |  |  |  |  |  |  |
|  |  |  |  |  |  |  |
| -1 | 161-0230-01 |  | 1 | CABLE ASSY,PWR,:3,18 AWG,92.0 L (STANDARD ONLY) | TK2432 | ORDER BY DESC |
| -2 | 343-1213-00 |  | 1 | CLAMP,PWR CORD:POLYMIDE | TK1163 | ORDER BY DESC |
|  | 020-0859-00 |  | 1 | COMPONENT KIT:EUROPEAN | 80009 | 020085900 |
| -3 | 161-0104-06 |  | 1 | .CABLE ASSY,PWR, $3 \times 0.75 \mathrm{MM}$ SQ, $220 \mathrm{~V}, 98.0 \mathrm{~L}$ (OPTION A1 - EUROPEAN) | S3109 | VIIGSOPO-HO5WF |
|  | 343-0170-00 |  | 1 | .RTNR,CA TO CA:UNW 0.25 OD CABLES | OJR05 | ORDER BY DESC |
|  | 020-0860-00 |  | 1 | COMPONENT KIT:UNITED KINGDOM | 80009 | 020086000 |
| -4 | 161-0104-07 |  | 1 | .CABLE ASSY,PWR,: $3 \times 0.75 M M$ SQ,240V,98.0 L (OPTION A2 - UNITED KINGDOM) | S3109 | ORDER BY DESC |
|  | 343-0170-00 |  | 1 | .RTNR,CA TO CA:U/w 0.25 OD CABLES | OJR05 | ORDER BY DESC |
|  | 020-0861-00 |  | 1 | COMPONENT KIT:AUSTRALIAN | 80009 | 020086100 |
| -5 | 161-0104-05 |  | 1 | CABLE ASSY,PWR,:3,18 AWG,240V,98.0 L .(OPTION A3 - AUSTRALIAN) | S3109 | SAA/3-OD3CCFC3X |
|  | 343-0170-00 |  | 1 | .RTNR,CA TO CA:UN 0.25 OD CABLES | OJR05 | ORDER BY DESC |
|  | 020-0862-00 |  | 1 | COMPONENT KIT:NORTH AMERICAN | 80009 | 020086200 |
| -6 | 161-0104-08 |  | 1 | .CABLE ASSY,PWR,:3,18 AWG,240V,98.0 L .(OPTION A4 - NORTH AMERICAN) | 70903 | ORDER BY DESC |
|  | 343-0170-00 |  | 1 | .RTNR,CA TO CA:UN 0.25 OD CABLES | OJR05 | ORDER BY DESC |
|  | 020-0863-00 |  | 1 | COMPONENT KIT:SWISS | 80009 | 020086300 |
| -7 | 161-0167-00 |  | 1 | .CABLE ASSY,PWR,;3.0 X 0.75,6A,240V,2.5M L .(OPTION A5 - SWITZERLAND) | \$3109 | ORDER BY DESC |
|  | 343-0170-00 |  | $\begin{aligned} & 1 \\ & 1 \\ & 1 \end{aligned}$ | .RTNR,CA TO CA:UNW 0.25 OD CABLES ACCESSORY PKG:TWO P6109 PROBES,ONE P6101A PROBE W/ACCESSORIES | OJR05 | ORDER BY DESC |
|  | 159-0023-00 |  | 1 | FUSE,CARTRIDGE:3AG,2A,250V,SLOW BLOW | 71400 | MDX2 |
|  | 016-0857-00 |  | 1 | ACCESSORY POUCH:W/PLATE | 80009 | 016085700 |
|  | 200-3232-00 |  | 1 | COVER,FRONT:ABS | 0JRZ2 | ORDER BY DESC |
|  | 070-7061-00 |  | 1 | MANUAL,TECH:OPERATOR (2246 MOD A,2Y) | $\begin{aligned} & \text { TK1908 } \\ & 80009 \end{aligned}$ | PER TEK DRAWING 070706100 |
|  | 070-7061-00 |  | 2 | MANUAL,TECH:OPERATOR (2246 1Y) | 80009 | 070706100 |
|  | 070-7062-00 |  | 2 | MANUAL,TECH:SERVICE (2246 iY) | 80009 | 070706200 |
|  | 070-7063-00 |  | 1 | CARD,INFO:REFERENCE (2246 MOD A,2Y) | 80009 | 070706300 |
|  | 070-7063-00 |  | 2 | CARD,INFO:REFERENCE (2246 1Y) | 80009 | 070706300 |
|  |  |  |  | OPTIONAL ACCESSORIES |  |  |
|  | 070-7062-00 |  | 1 | MANUAL,TECH:SERVICE (2246 MOD A,2Y) | 80009 | 070706200 |

Fig. 8 Index Tektronlx Serial No. No. Part No. Effective Dscont Oty 12345 Name \& Description Mfr.
Code Mfr. Part No.

## MANUAL CHANGE INFORMATION

At Tektronix, we continually strive to keep up with latest electronic developments by adding circuit and component improvements to our instruments as soon as they are developed and tested.

Sometimes, due to printing and shipping requirements, we can't get these changes immediately into printed manuals. Hence, your manual may contain new change information on following pages.

A single change may affect several sections. Since the change information sheets are carried in the manual until all changes are permanently entered, some duplication may occur. If no such change pages appear following this page, your manual is correct as printed.

Date: 5-13-91
Change Reference: $\qquad$ M73903

Product: $22461 Y$ and 2246 MOD A SERVICE MANUAL $\qquad$

# EFFECTIVE SERIAL NUMBER: B715003 (TY, 2R) B120200 (MOD A, 2Y, 9X) 

## replaceable electrical Parts list changes

CHANGE TO:

| A10R113 | $313-1750-00$ | RES, FXD FILM:75 OHM ,5\%,0.2W |
| :--- | :--- | :--- |
| A10R123 | $313-1750-00$ | RES, FXD FILM:75 OHM ,5\%,0.2W |

## DIAGRAM CHANGES

DIAGRAM VERTICAL INPUTS

Change the value of resistor R113 (location 1G) to $75 \Omega$. Change the value of resistor R123 (location 3G) to $75 \Omega$.

# Tektronix <br> COMMITTED TO EXCELLENCE 

Date: 5-15-91 Change Reference: M74238
Product: 22461 Y \& 2246 MOD A SERVICE MANUAL
Manual Part Number: $\qquad$

EFFECTIVE SERIAL NUMBER: B715476 (2246 1Y, 2R) B121427 (2246 MOD A, 2Y, 9X)

## replaceable electrical parts list changes

CHANGE TO:
A18L2206 108-1501-00
INDUCTOR,FIXED:100UH,10\%,1.8A

DIAGRAM POWER SUPPLY

Change L2206 (location 6K) to $100 \mu \mathrm{H}$.

Date: 9-23-91 Change Reference: $\qquad$
Product: 2246ModA/1Y SERVICE
Manual Part Number: $\qquad$

## EFFECTIVE ALL SERIAL NUMBERS

## TEXT CHANGES

Page 4-25 Step 2e. DC Volts Normal Mode Rejection Ratio
Change spec to $\pm 0.018 \mathrm{~V}$

Date: 9-25-91 $\qquad$
Product: 2246 Mod A / 1 Y SERVICE
Manual Part Number: $\qquad$ 070-7062-00

## EFFECTIVE ALL SERIAL NUMBERS

## TEXT CHANGES

Page 6-39 Removal and Replacement Instructions - "WARNING"
The component number for the Power Supply/Main Board interface connector mentioned in the warning was incorrectly identified as J1024. The correct component number is J 1204.

The correct warning statement is given below:

## WARNING

Potentially dangerous voltages exist at several points throughout this instrument. If it is operated with the cabinet removed, do not touch exposed connections or components. Before replacing parts or cleaning, disconnect the ac power source from the instrument and check that the line rectifier filter capacitors have discharged. Also check the low voltages at the Power Supply/Main Board interface connector (J1204). If any of the supply voltage or line voltage filter capacitors remain charged for more than 20 seconds, discharge them to ground through a 1 kת, 5 or 6 watt resistor.



[^0]:    a Performance Requirement not checked in manual.

[^1]:    ${ }^{\text {a }}$ Performance Requirement not checked in manual.

[^2]:    ${ }^{a}$ Performance Requirement not checked in manual.

[^3]:    ${ }^{\text {a }}$ Performance Requirement not checked in manual.

[^4]:    a Performance Requirement not checked in manual.

[^5]:    ${ }^{\text {a }}$ Performance Requirement not checked in manual.

[^6]:    a Performance Requirement not checked in manual.

[^7]:    ${ }^{a}$ Performance Requirement not checked in manual.

[^8]:    a Performance not checked in manual.

[^9]:    ${ }^{\text {a }}$ Requires a TM500-series power module.

[^10]:    CAUTION\}

    When checking semiconductors, observe the static-sensitivity precautions given at the beginning of this section.

[^11]:    $$
    \begin{aligned}
    & \text { (3) } \begin{array}{c}
    \text { Static Sensitive Devices } \\
    \text { See Manitenance Section }
    \end{array} \\
    & \square
    \end{aligned}
    $$

    

[^12]:    *See Parts List for
    serial number ranges.

